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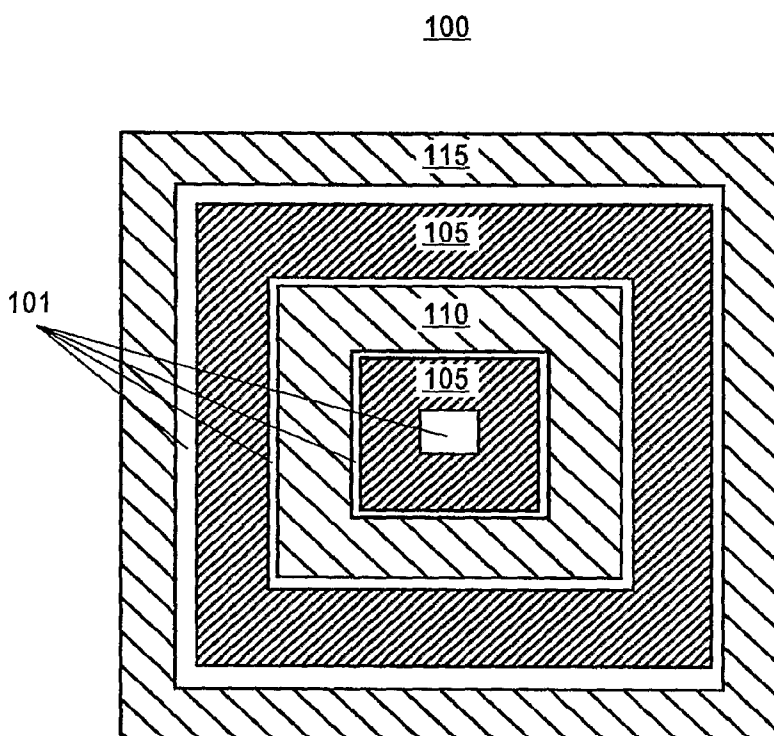
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(54) Title: SYMMETRIC BIPOLAR JUNCTION TRANSISTOR DESIGN FOR DEEP SUB-MICRON FABRICATION PROCESSES



(57) Abstract: Described herein are embodiments of a bipolar junction transistor including a plurality of base terminal rings having an emitter terminal ring between any two base terminal rings of the plurality of base terminal rings, and a collector terminal ring surrounding the plurality of base terminal rings and the emitter terminal ring and methods of manufacturing the same.

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