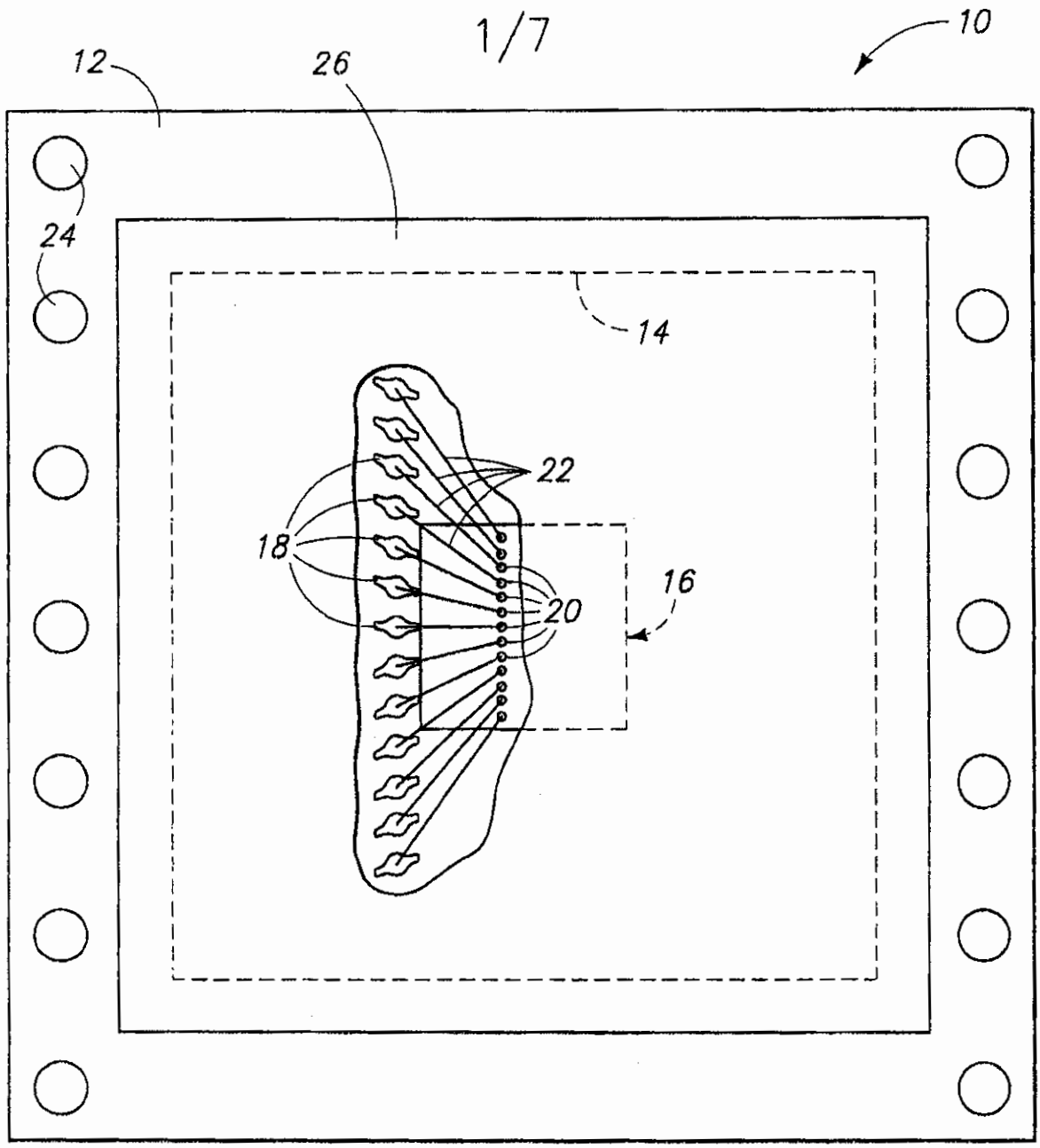


Fig 1
PRIOR ART

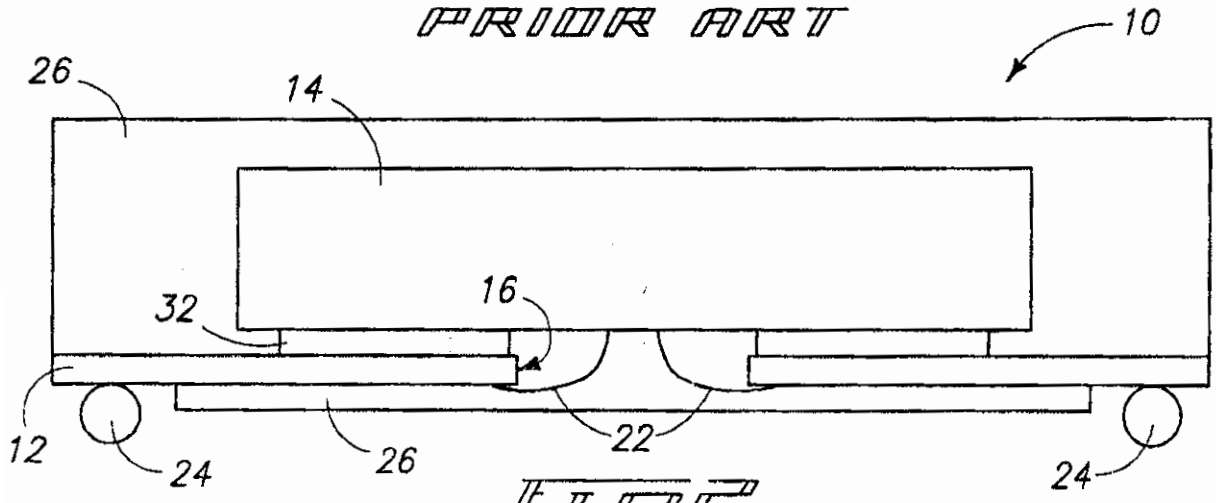


Fig 2
PRIOR ART

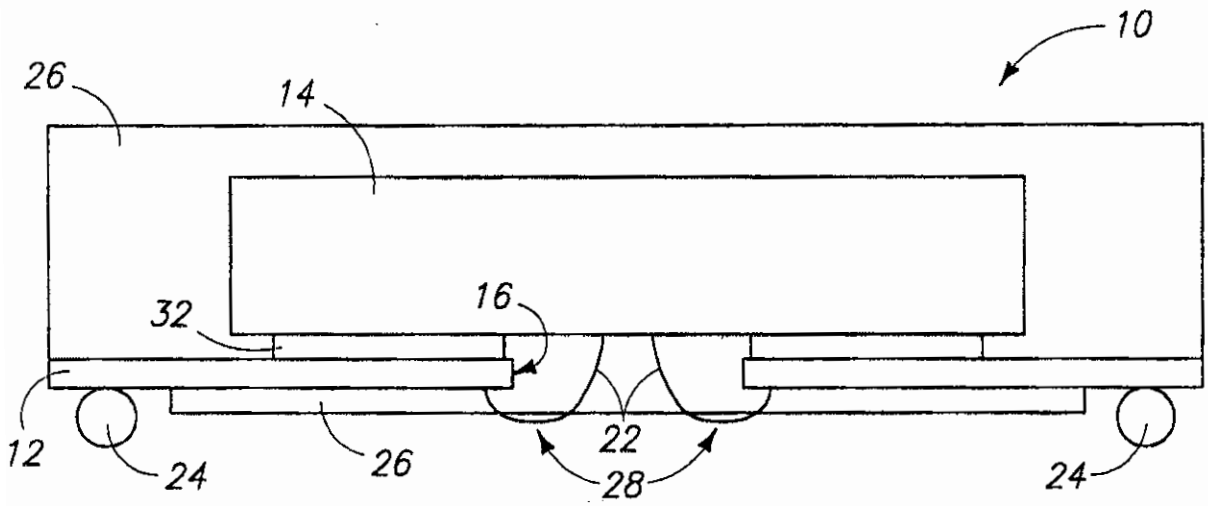


FIG 3
PRIOR ART

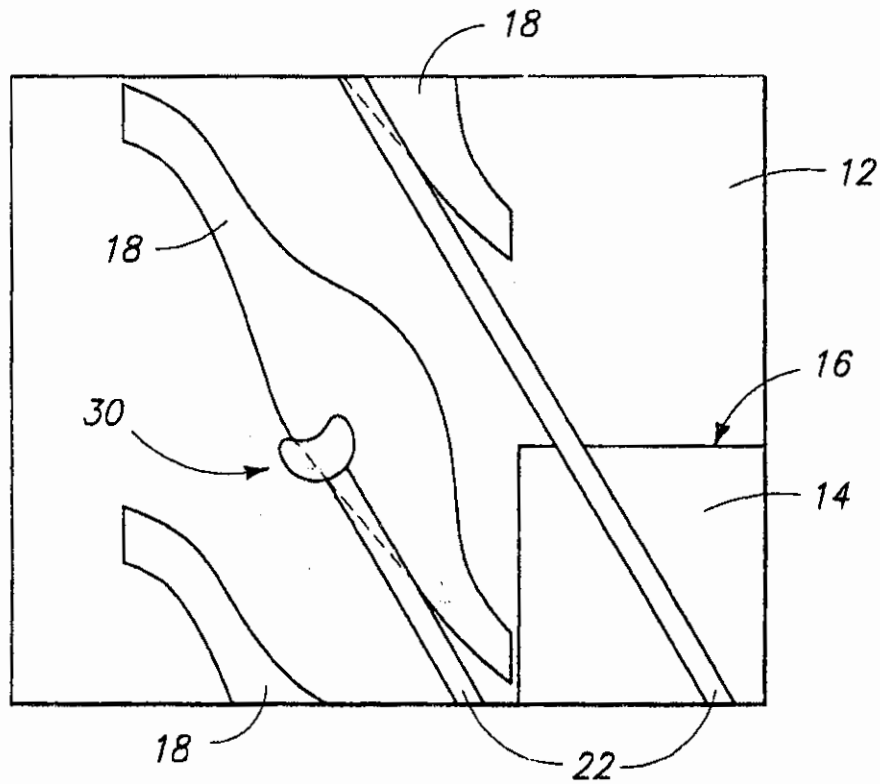


FIG 4
PRIOR ART

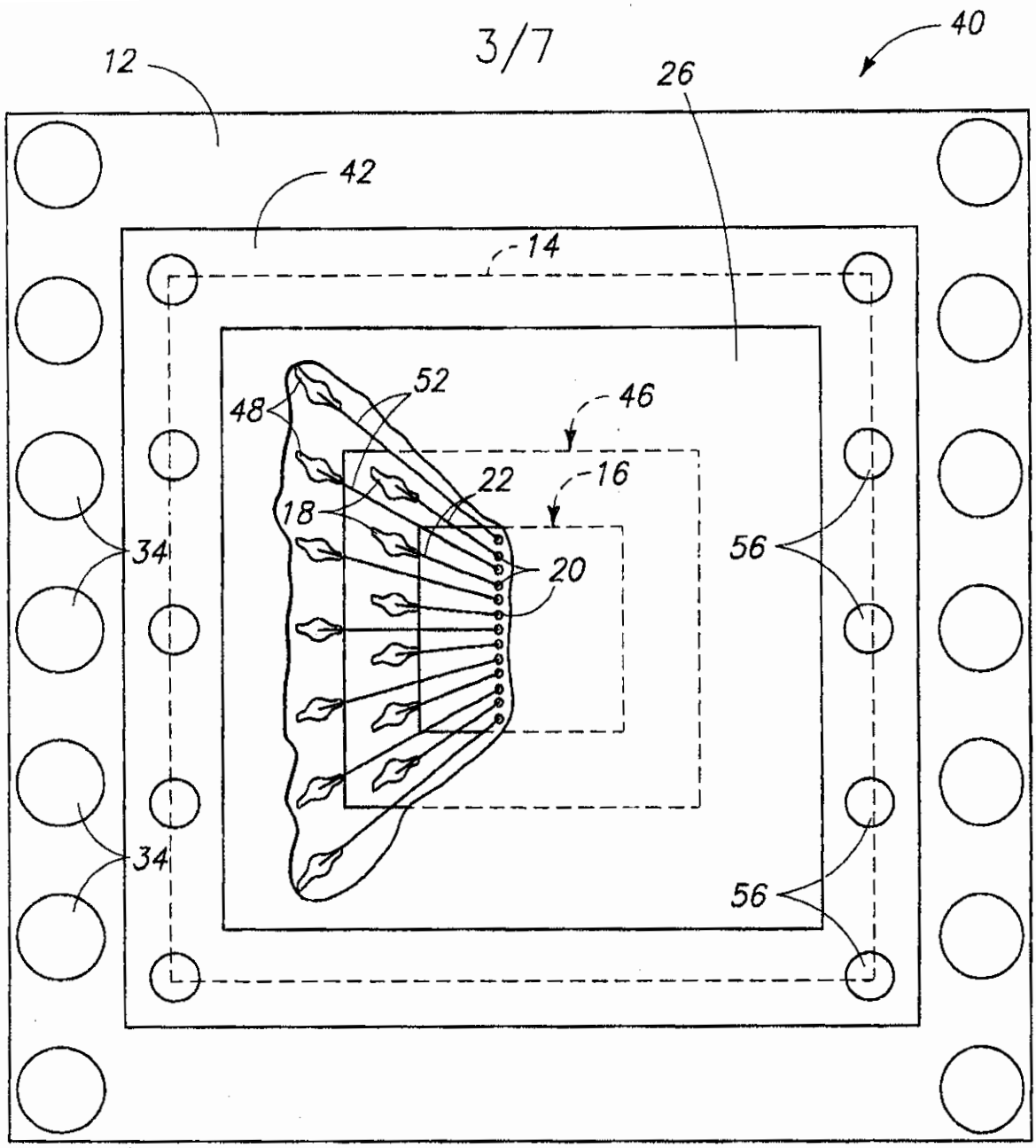

40
12
26
42
14
52
48
46
22
16
18
34
20
56
34
56

Fig. 5

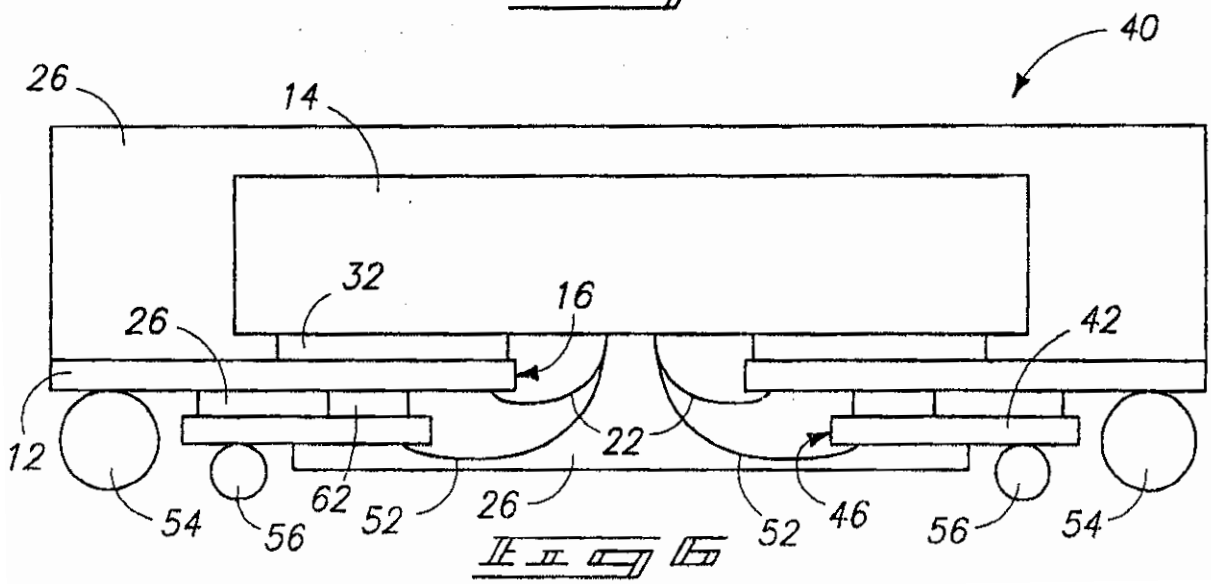

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32
16
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42
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22
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62
52
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52
46
56
54

Fig. 6

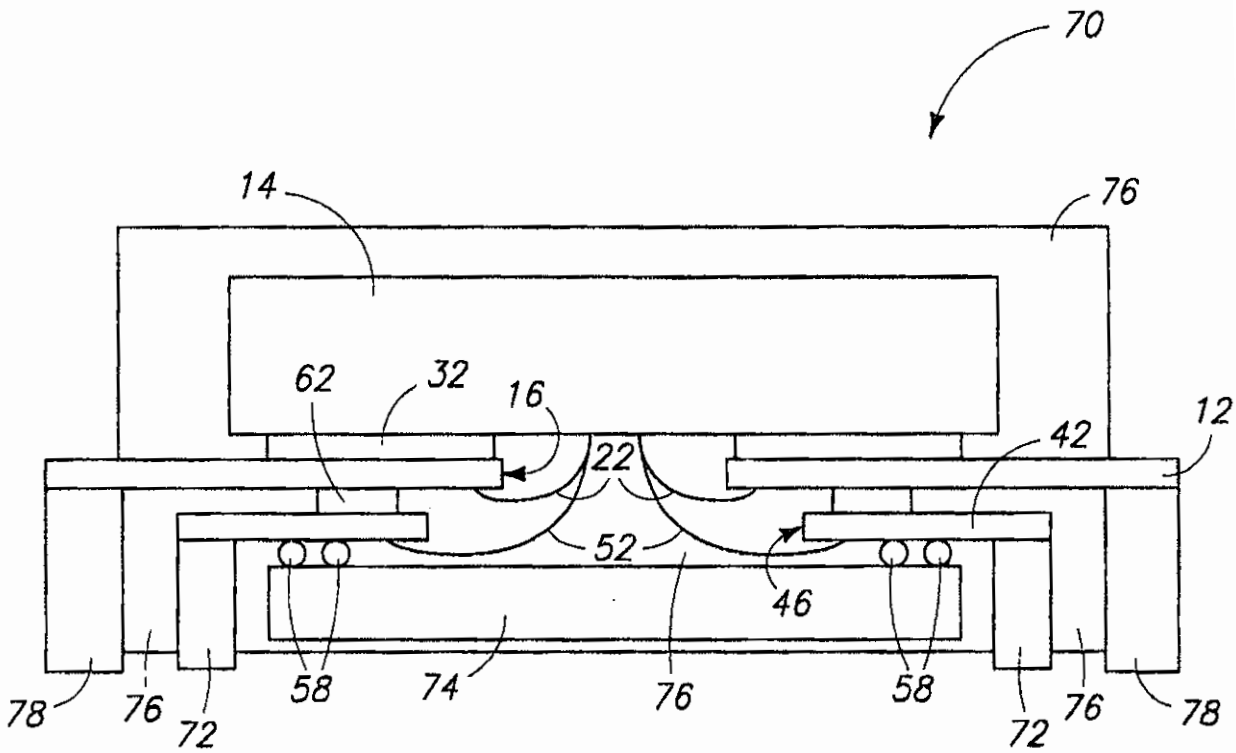


Fig. 7

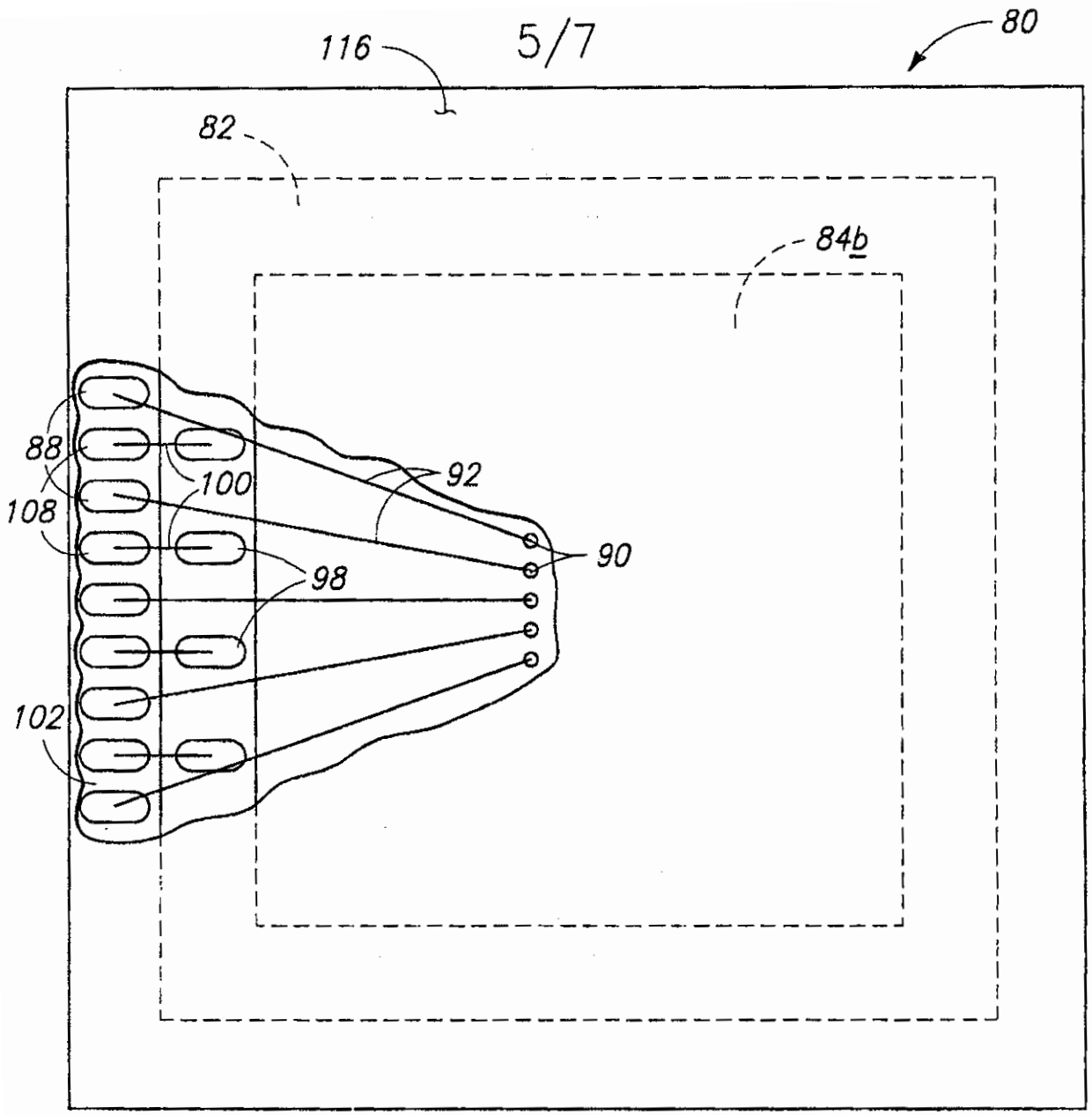

80
116
82
84b
88
108
100
92
90
98
102

Fig 8B

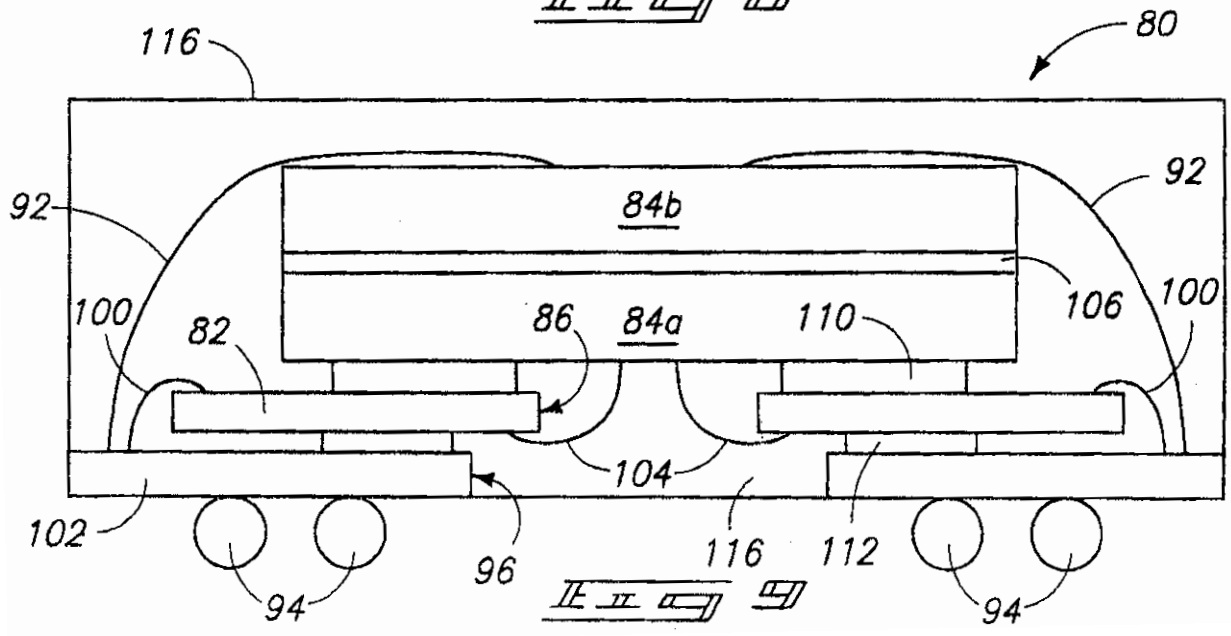

80
116
92
92
84b
106
100
100
82
86
84a
110
104
102
96
116
112
94
94

Fig 9

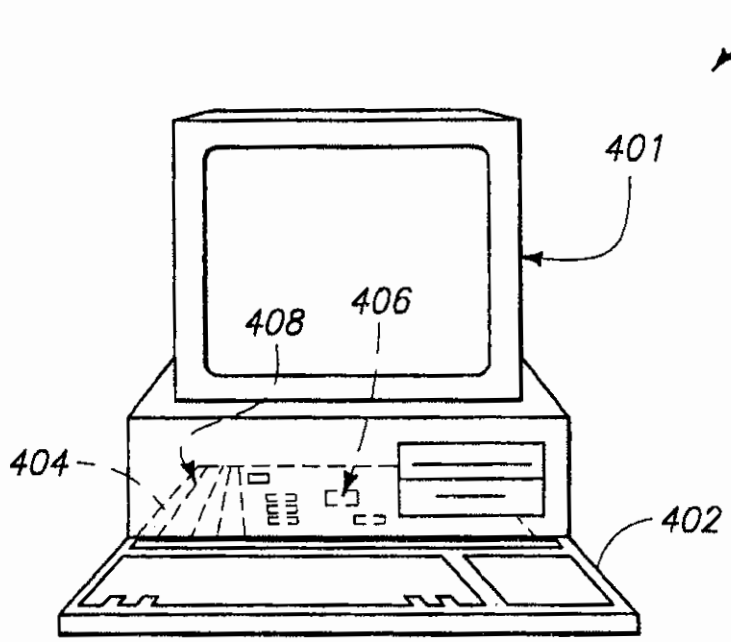


Fig 10

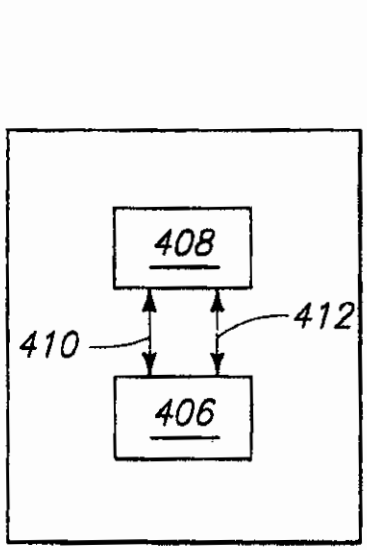


Fig 11

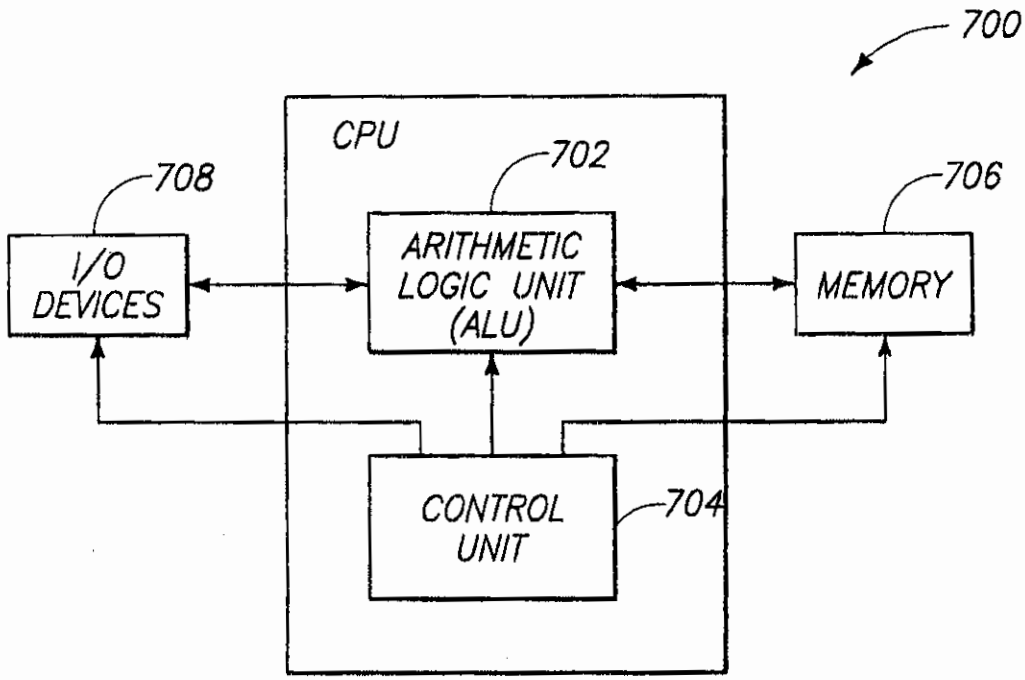

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig. 12

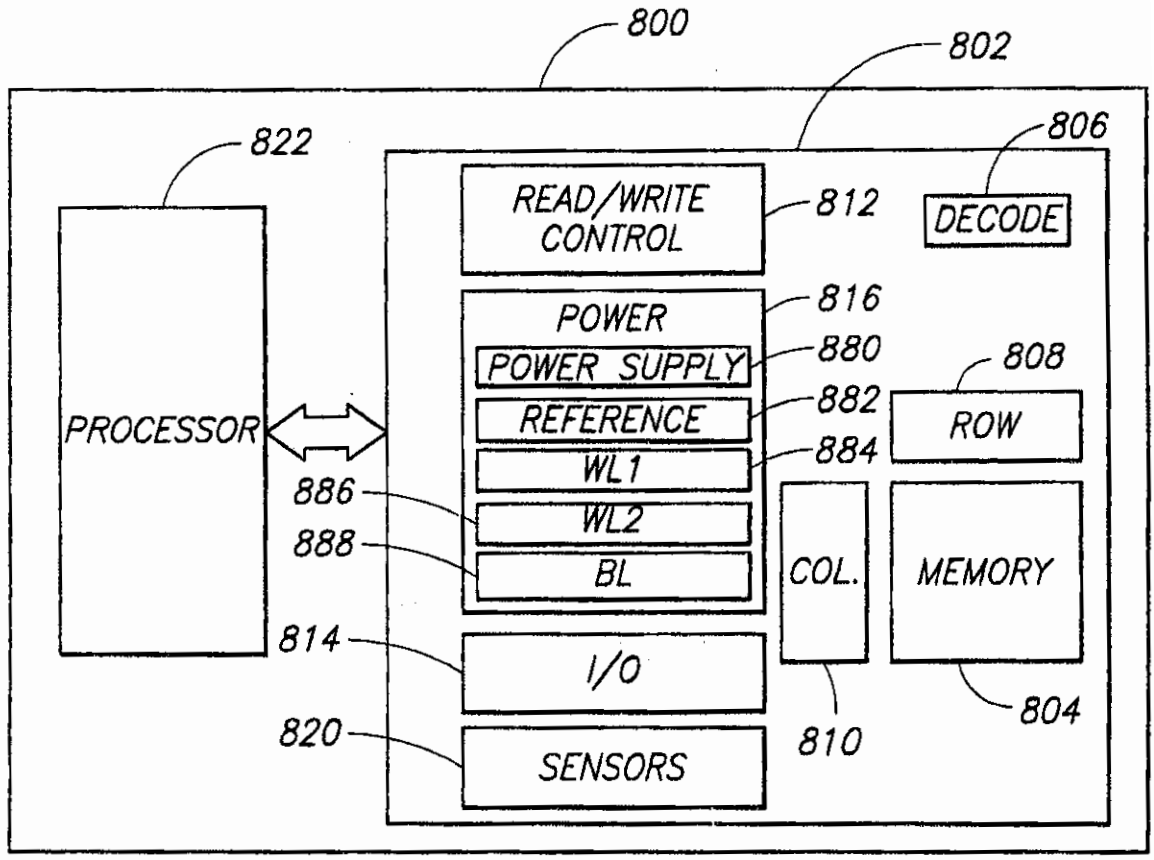

800
802
822
806
READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
PROCESSOR
REFERENCE
882
ROW
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig. 13