

ABSTRACTDUAL GATE TUNABLE OSCILLATOR

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A dual gate field effect transistor (FET) (101) is configured as a self-buffering local oscillator (7) of a tuner by arranging the FET in a cascode configuration in which the first gate electrode (G1) is coupled to the source electrode (S) through an oscillation conditioning network (203) and also to a frequency determining network (300), the second gate electrode (G2) is coupled to signal ground through a negligible impedance (109) and the drain electrode (D) is utilized as the output of the oscillator.

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