ABSTRACT OF THE DISCLOSURE

A VARIABLE POWER ADAPTIVE TRANSMITTER

A circuit comprising:

an analog output module that receives a bias signal and a control signal;

a delay module that receives a first signal and that generates a delayed first signal;

a control module that receives the delayed first signal and that generates said control signal based thereon; and

an envelope generating module that receives a second signal including amplitude information related to said first signal and that generates an envelope signal that selectively increases said bias signal to said analog output module when said envelope signal exceeds a predetermined threshold and before a corresponding portion of said first signal is received by said analog output module.

FIG. 9

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