

ABSTRACT

CHIP MODULE, COMMUNICATION SYSTEM, AND PORT ALLOCATION METHOD

This application provides a chip module, a communication system, and a port allocation method. The chip module has a plurality of first ports, at least some or all of the first ports are first selection ports, and each first selection port
5 may act as a write port or a read port. The chip module further includes a first control module. The first control module controls, based on a transmit/receive requirement of the chip module, the first selection port to be switched to a read port or a write port, to match the transmit/receive requirement of the chip module. In the foregoing technical solution, the first selection port may selectively act as a read port or a write port, so that switching can be performed based on an operating state of the chip module, thereby increasing a read/write bandwidth of the chip module. In addition, the
10 first control module controls an operating state of the first selection port, to flexibly adjust a quantity of read ports and a quantity of write ports of the chip module.

Figure 3