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Abstract

A video display unit according to the present invention is provided with a multipurpose arithmetic unit for generating display data by carrying out at least one operation within one period of the pixel clock and an instruction sequencer for describing as instructions the display data and the operational contents to be inputted to this, and by implementing the described instructions while synchronizing to the display clock, picture up/down scaling processes as well as a blending process with the OSD are realized. Thereby, it is not necessary to provide a dedicated circuit for each of required specifications such as in a conventional unit, and by rewriting instructions within the instruction sequencer, it is possible to cope with a plurality of display specifications.