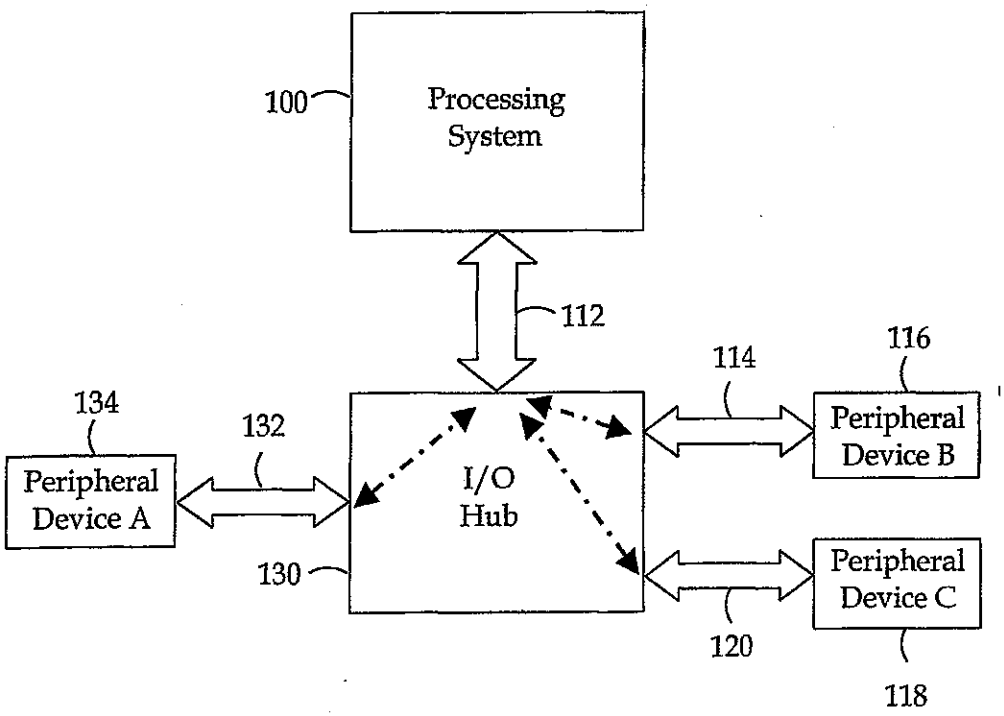


Figure 1A

(Prior Art)

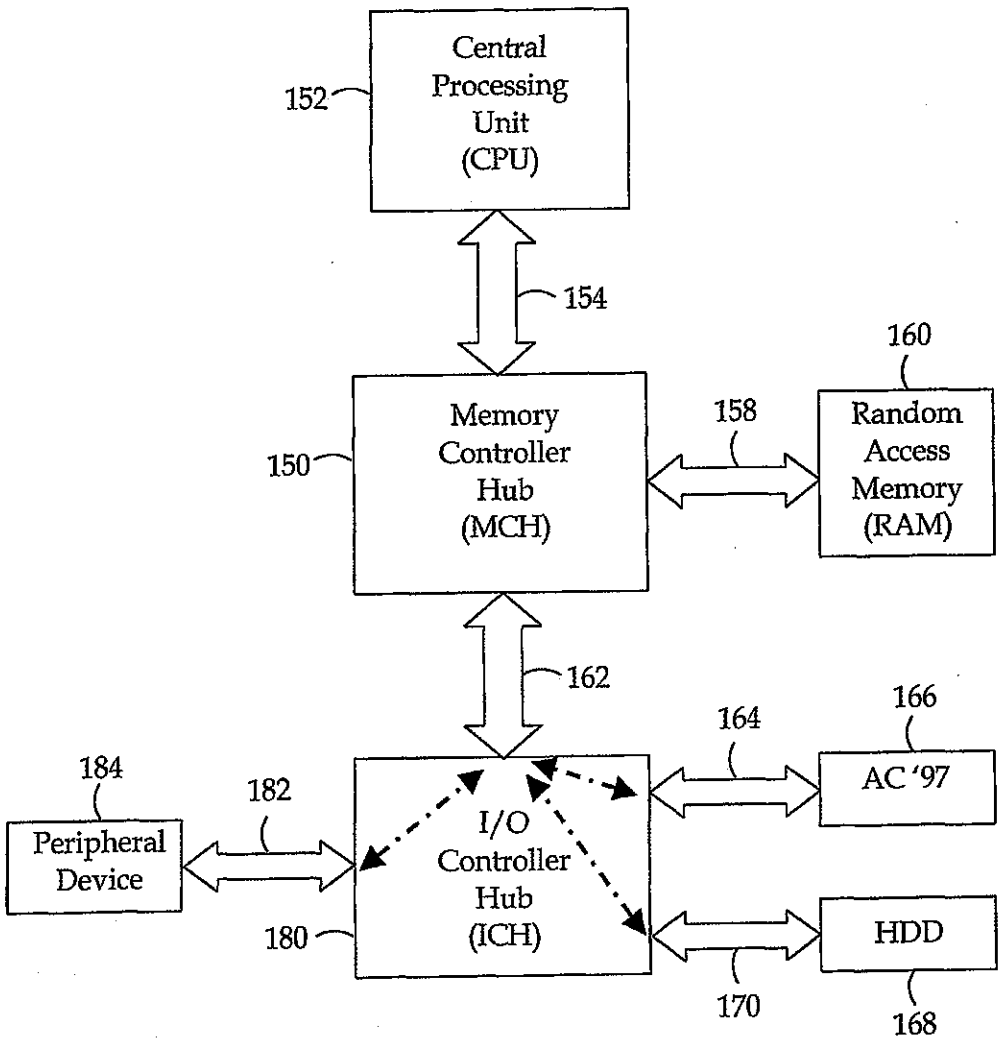


Figure 1B

(Prior Art)

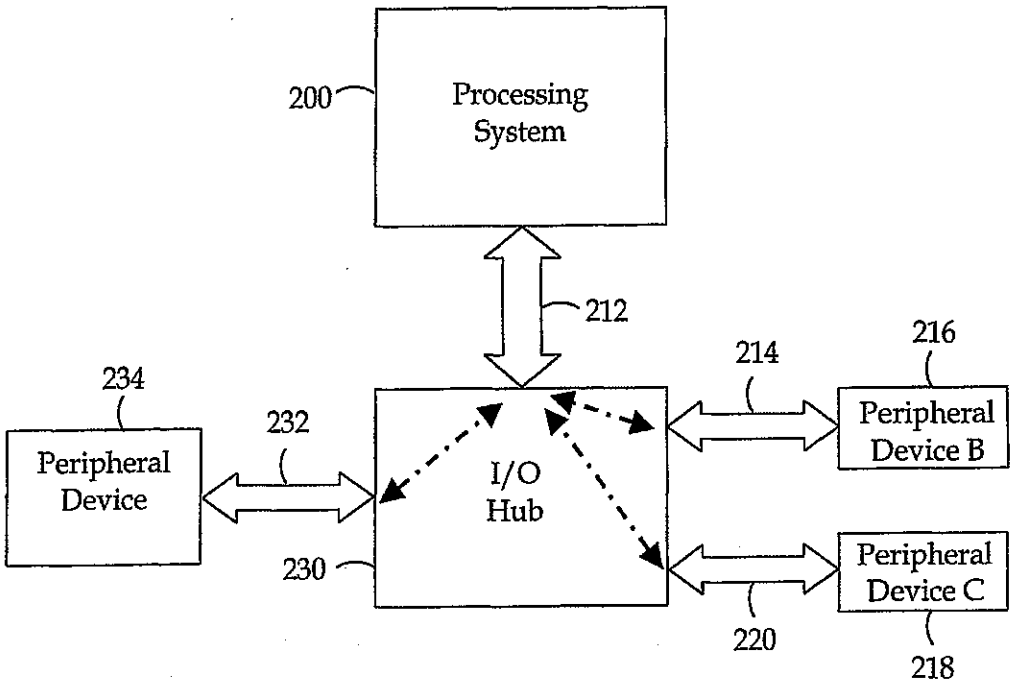

200
Processing System
212
214
216
234
232
Peripheral Device
I/O Hub
Peripheral Device B
230
Peripheral Device C
220
218

Figure 2A

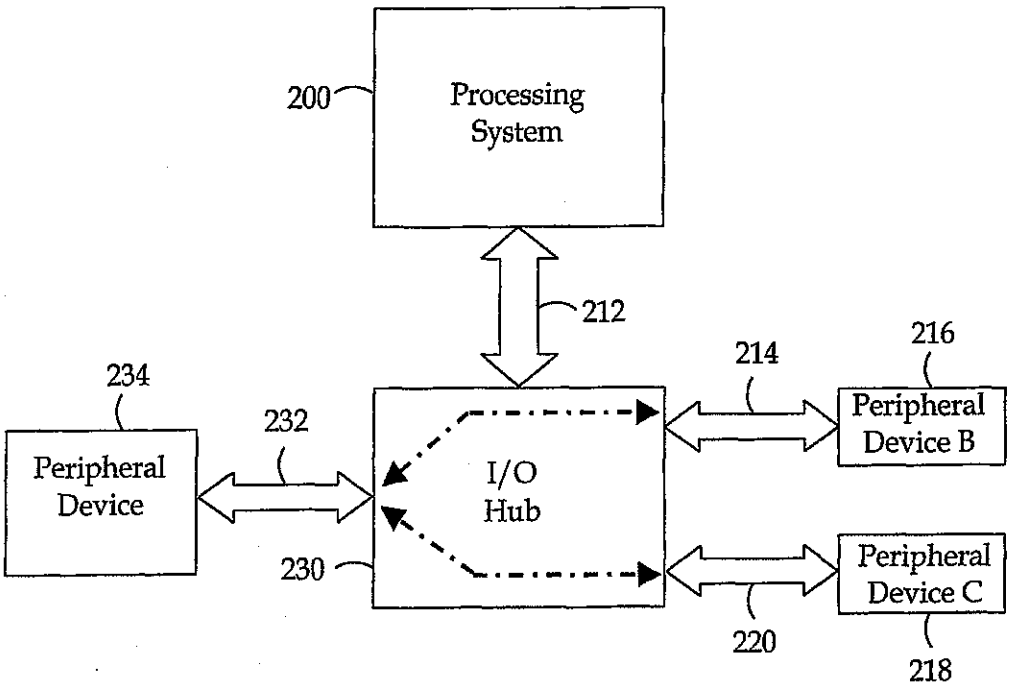

200
Processing System
212
214
216
Peripheral Device B
234
232
Peripheral Device
I/O Hub
230
Peripheral Device C
220
218

Figure 2B

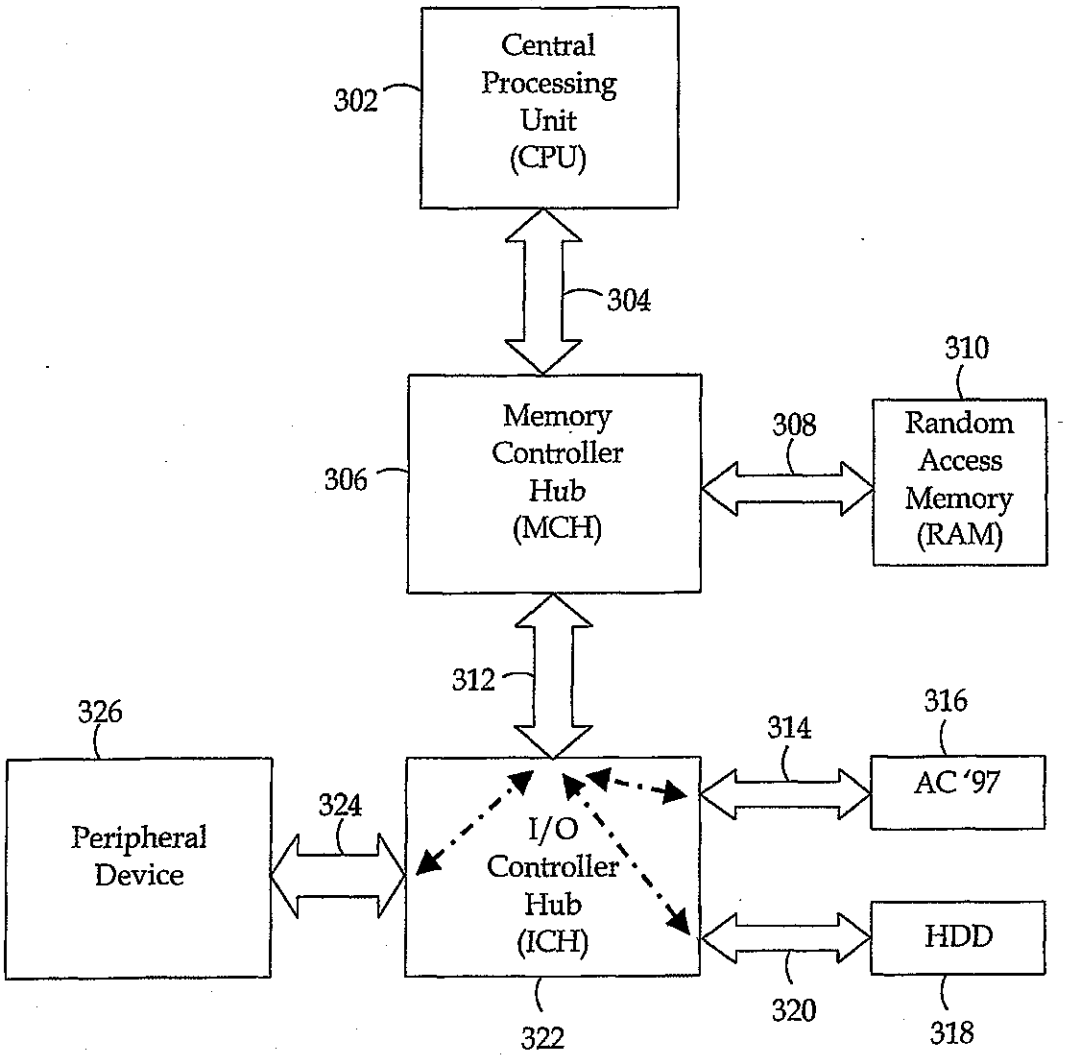

Central Processing Unit (CPU)
302
304
Memory Controller Hub (MCH)
306
308
Random Access Memory (RAM)
310
312
326
Peripheral Device
324
I/O Controller Hub (ICH)
314
AC '97
316
HDD
318
320
322

Figure 3A

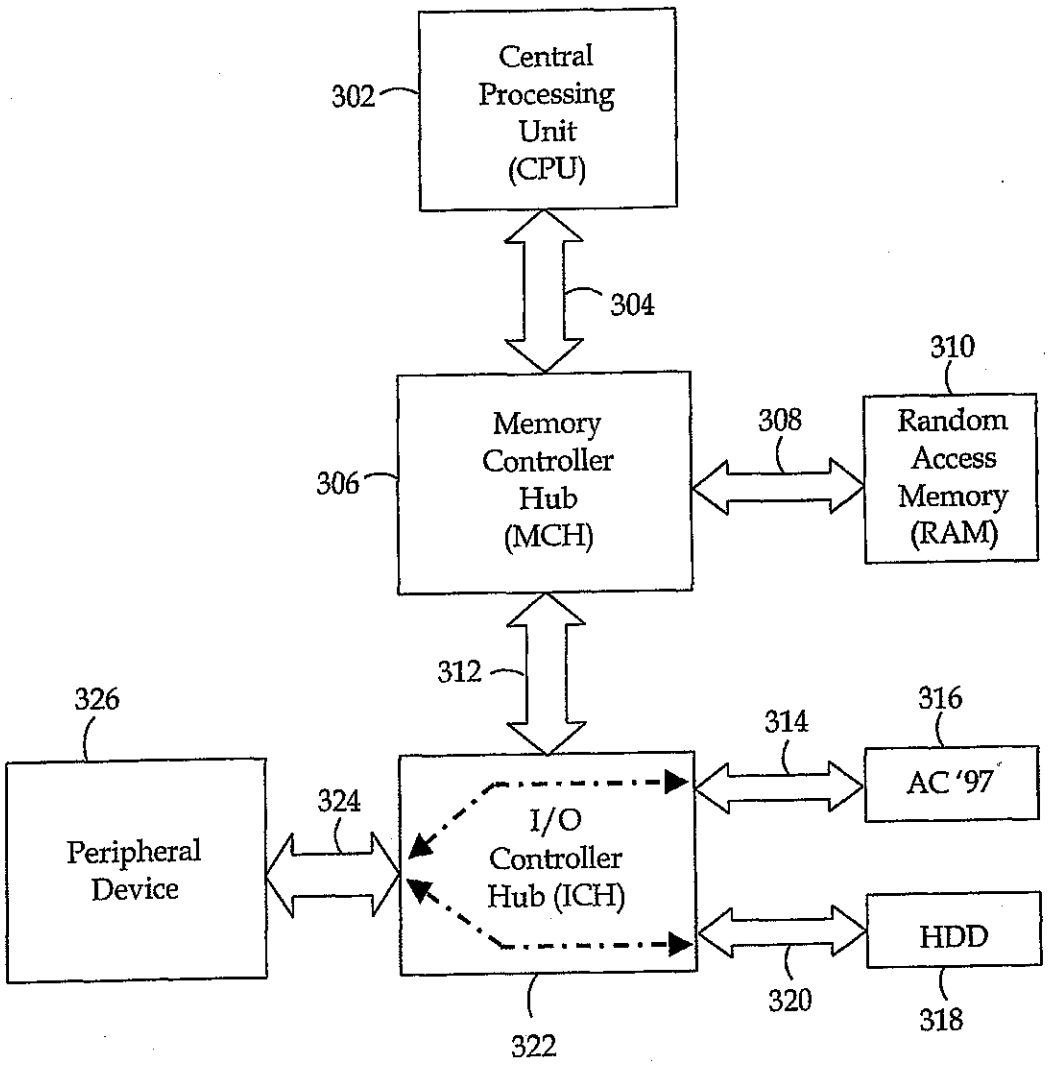

Central Processing Unit (CPU)
302
304
Memory Controller Hub (MCH)
306
308
Random Access Memory (RAM)
310
312
326
Peripheral Device
324
I/O Controller Hub (ICH)
314
AC '97
316
HDD
318
320
322

Figure 3B

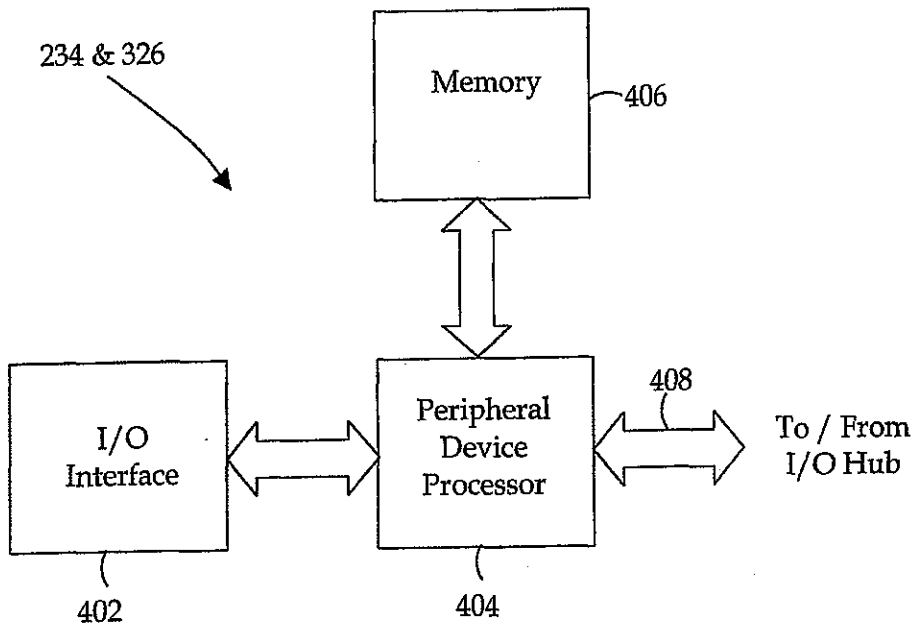

234 & 326
Memory
406
I/O Interface
Peripheral Device Processor
408
To / From I/O Hub
402
404

Figure 4

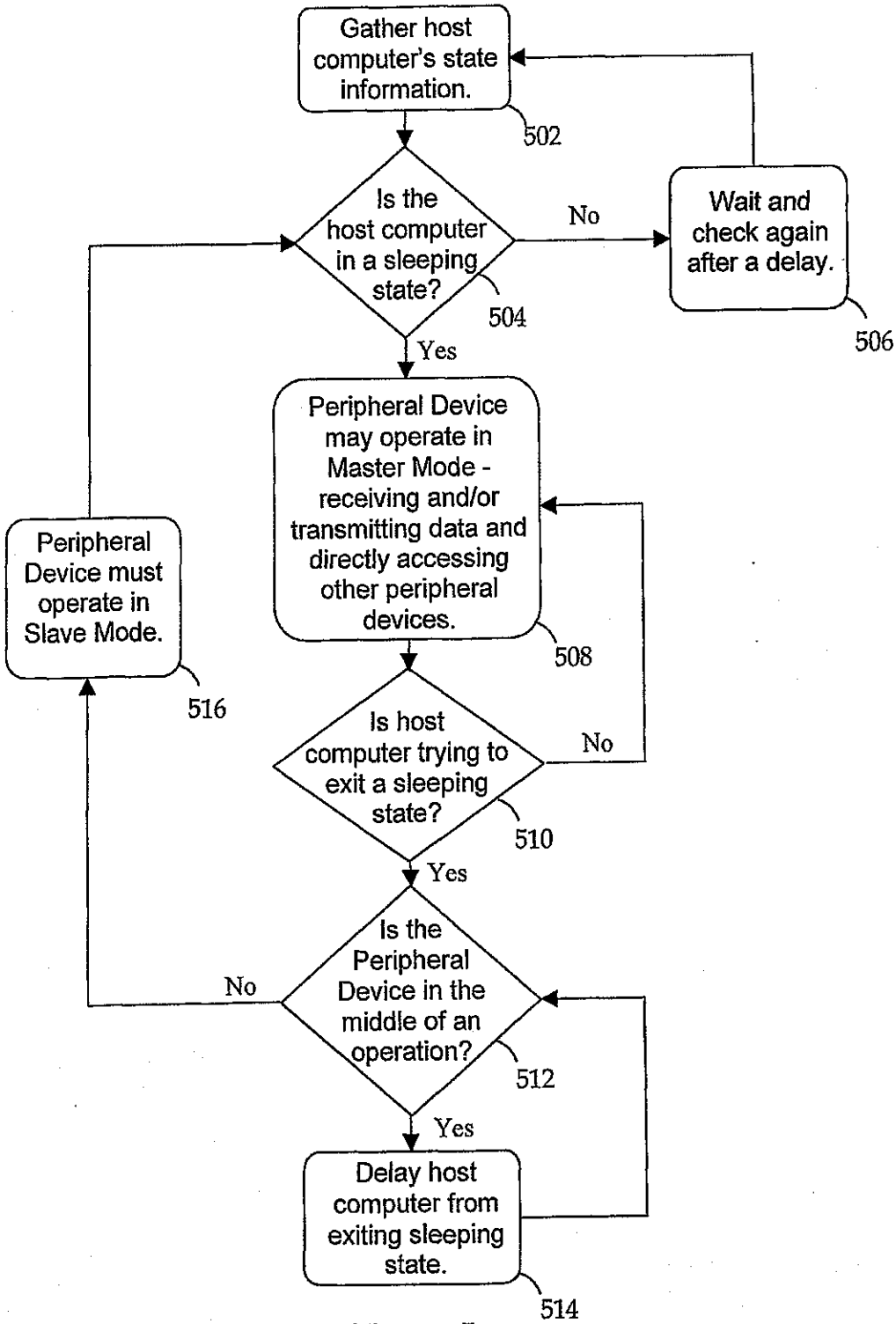

Gather host computer's state information.
502
Is the host computer in a sleeping state?
504
No
Wait and check again after a delay.
506
Yes
Peripheral Device may operate in Master Mode - receiving and/or transmitting data and directly accessing other peripheral devices.
508
Is host computer trying to exit a sleeping state?
510
No
Yes
Is the Peripheral Device in the middle of an operation?
512
No
Yes
Delay host computer from exiting sleeping state.
514
Peripheral Device must operate in Slave Mode.
516

Figure 5