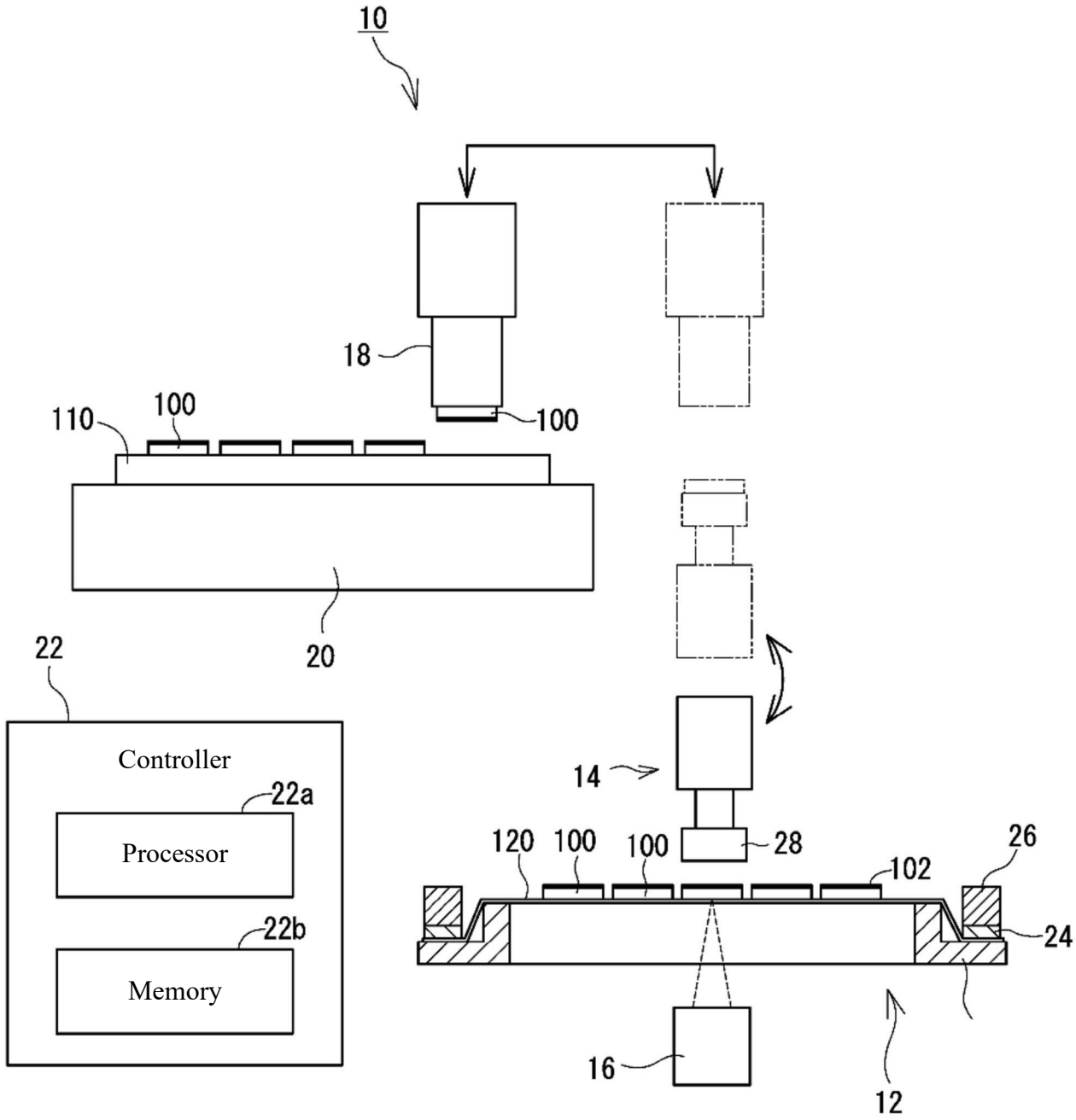

10
18
100
110
100
20
22
Controller
22a
Processor
22b
Memory
14
120
100
100
28
102
26
24
16
12

FIG. 1

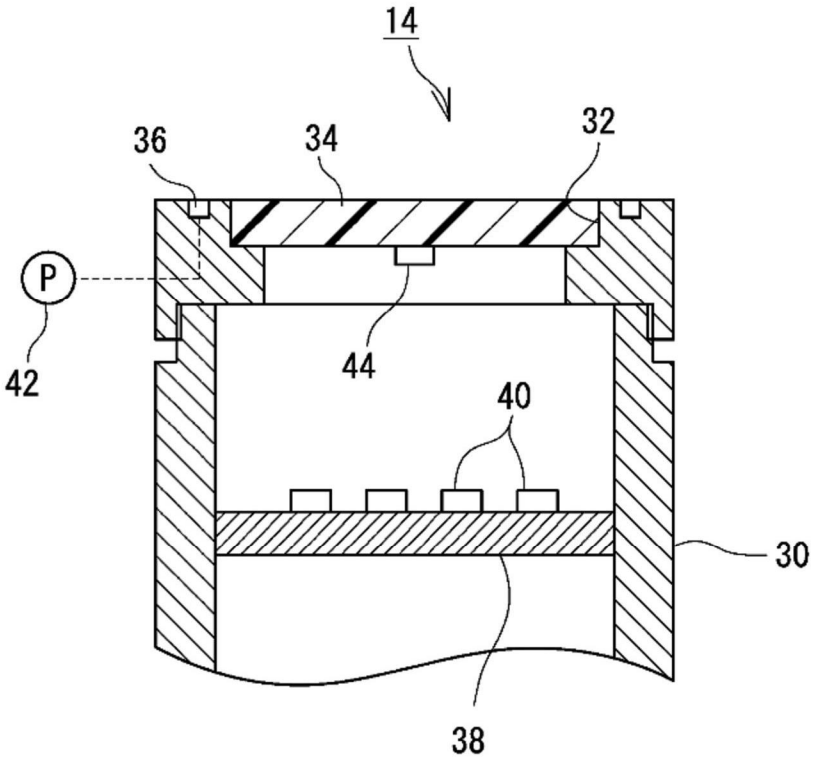


FIG. 2

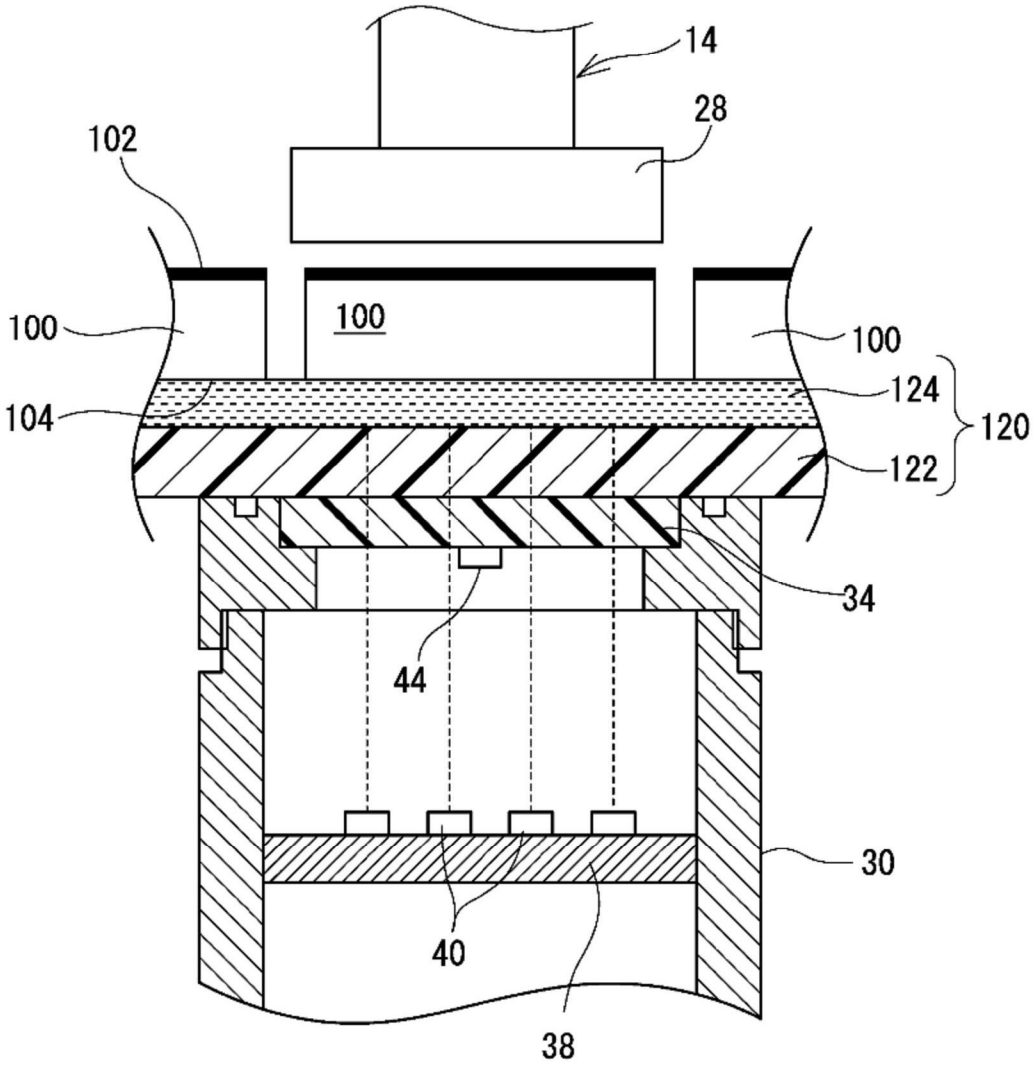


FIG. 3

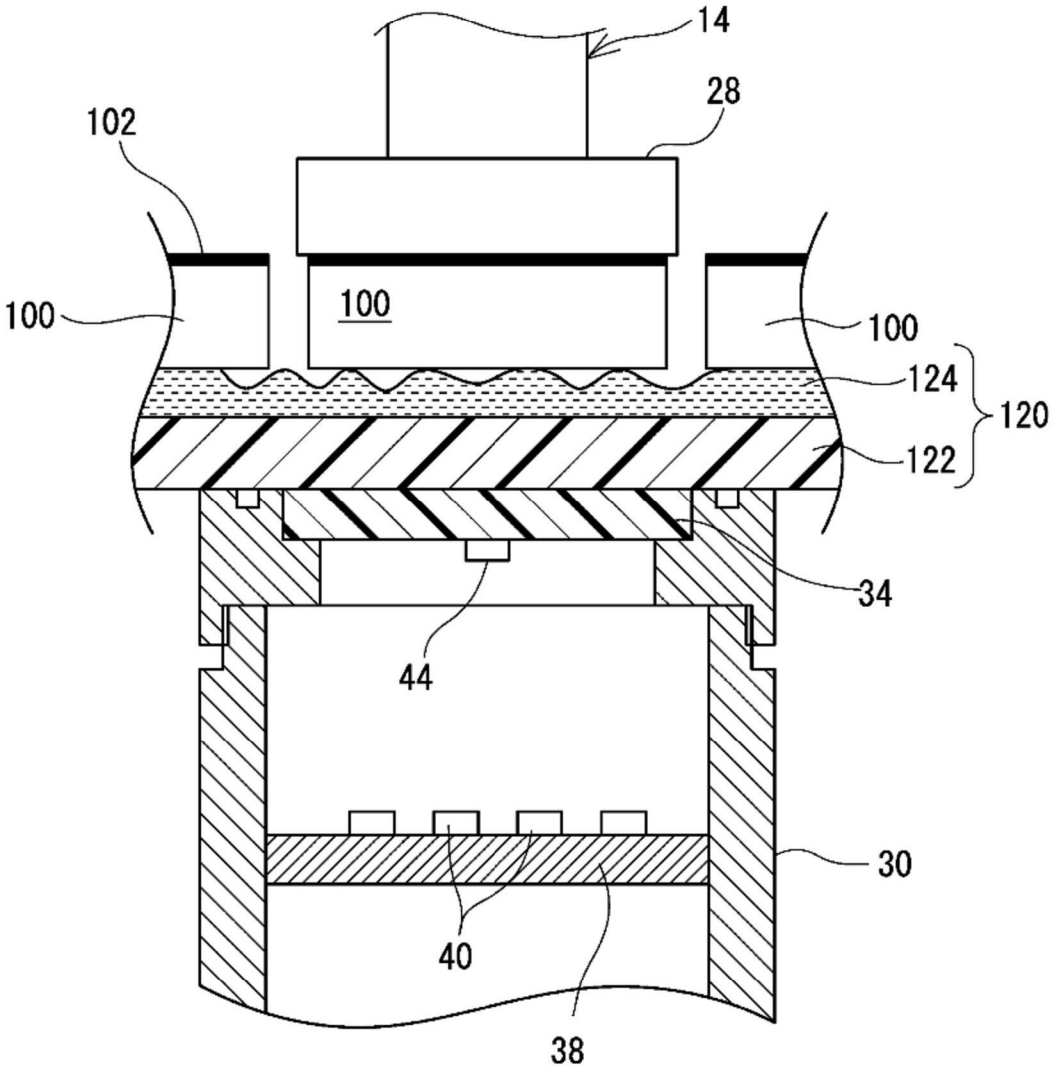


FIG. 4

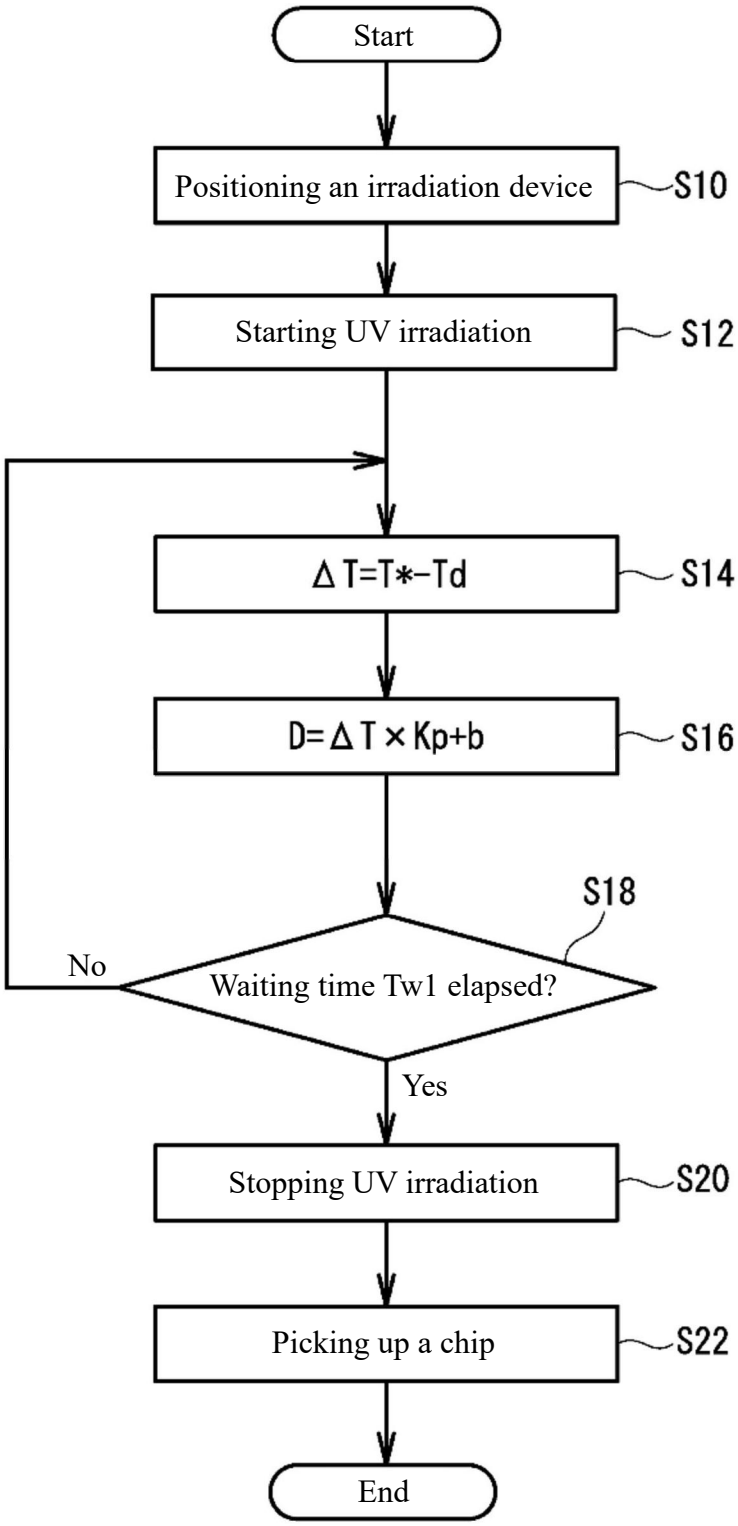

Start
Positioning an irradiation device
S10
Starting UV irradiation
S12
ΔT=T*−Td
S14
D=ΔT×Kp+b
S16
S18
No
Waiting time Tw1 elapsed?
Yes
Stopping UV irradiation
S20
Picking up a chip
S22
End

FIG. 5

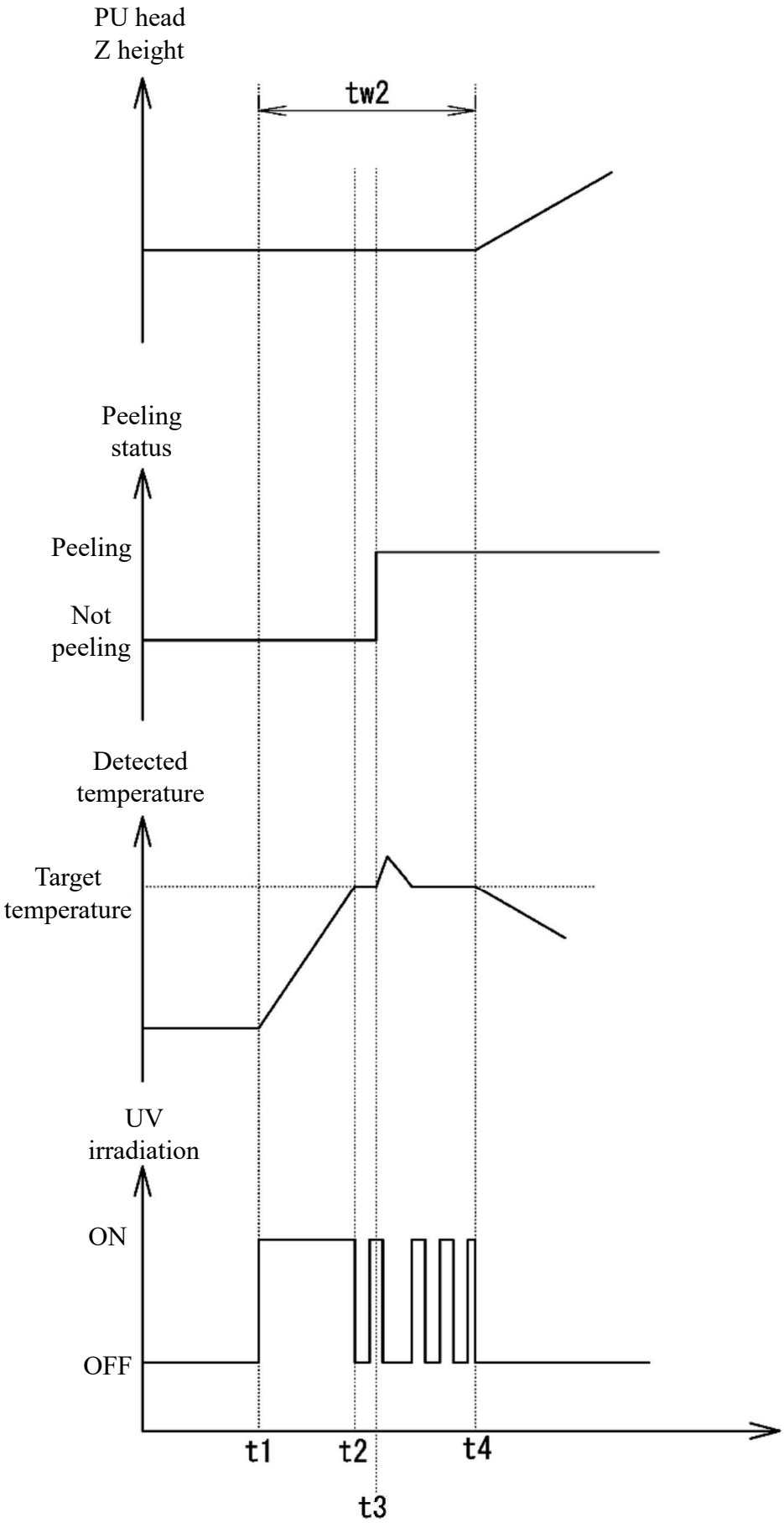

PU head
Z height
tw2
Peeling
status
Peeling
Not
peeling
Detected
temperature
Target
temperature
UV
irradiation
ON
OFF
t1
t2
t3
t4

FIG. 6

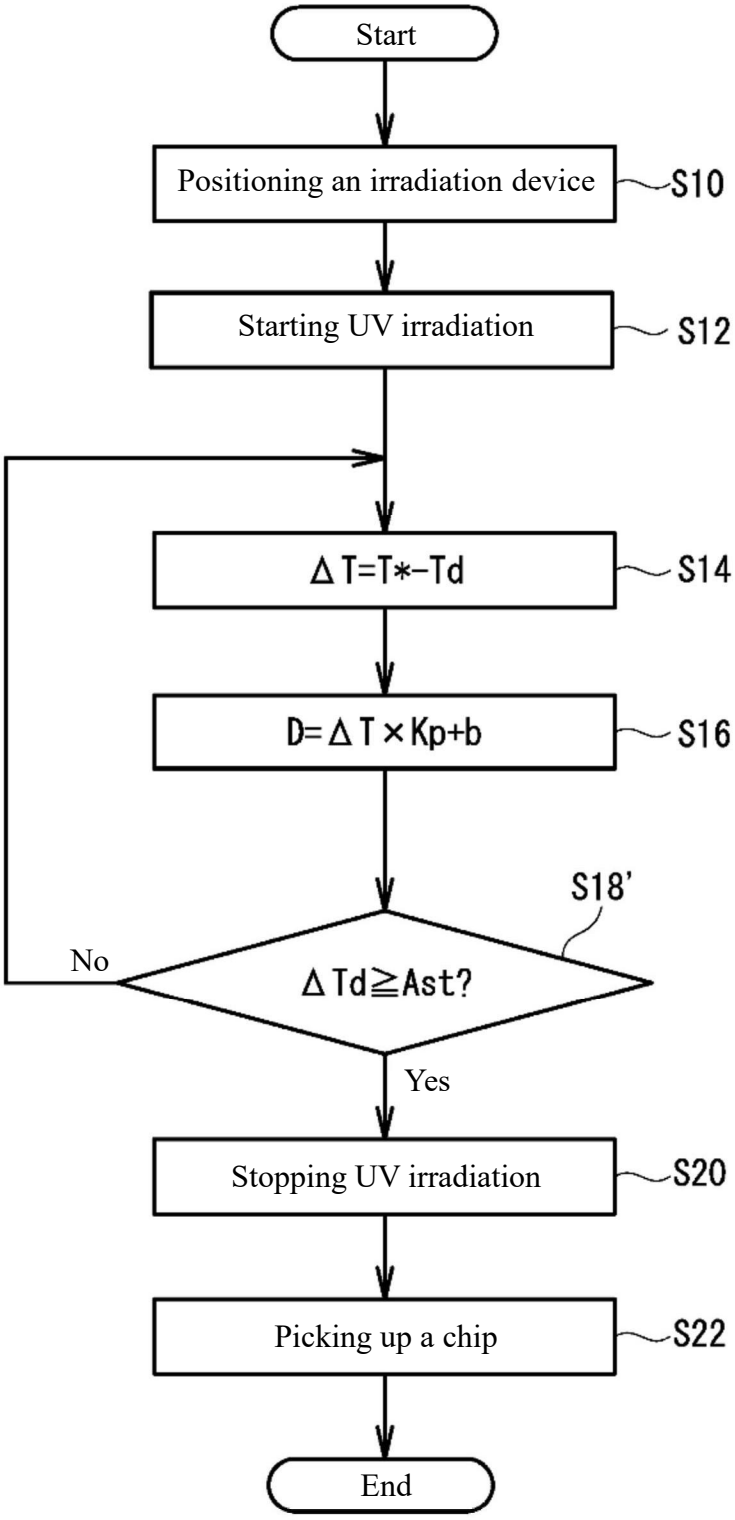

Start
Positioning an irradiation device
S10
Starting UV irradiation
S12
ΔT=T*-Td
S14
D=ΔT×Kp+b
S16
S18'
ΔTd≧Ast?
No
Yes
Stopping UV irradiation
S20
Picking up a chip
S22
End

FIG. 7

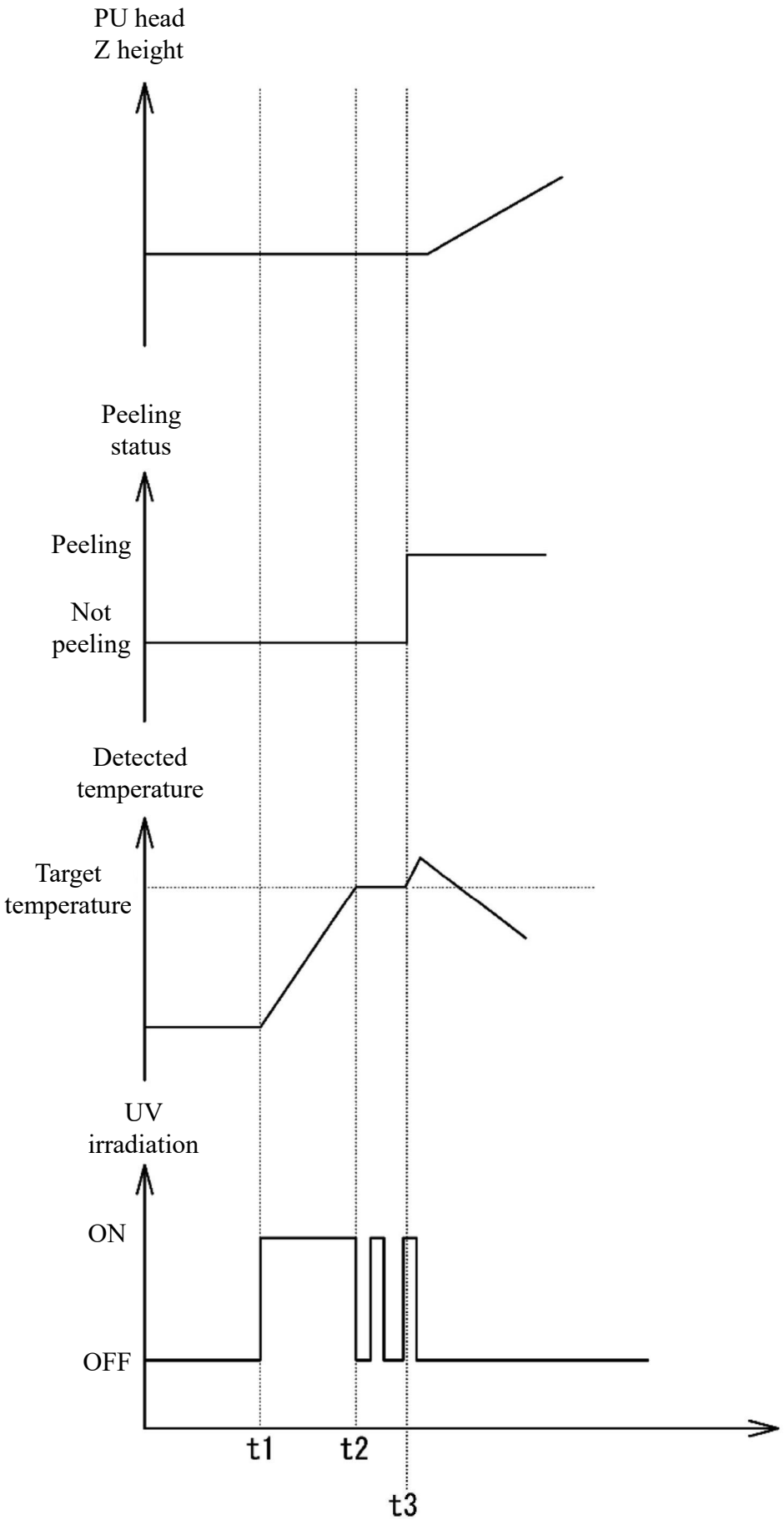

PU head
Z height
Peeling
status
Peeling
Not
peeling
Detected
temperature
Target
temperature
UV
irradiation
ON
OFF
t1
t2
t3

FIG. 8

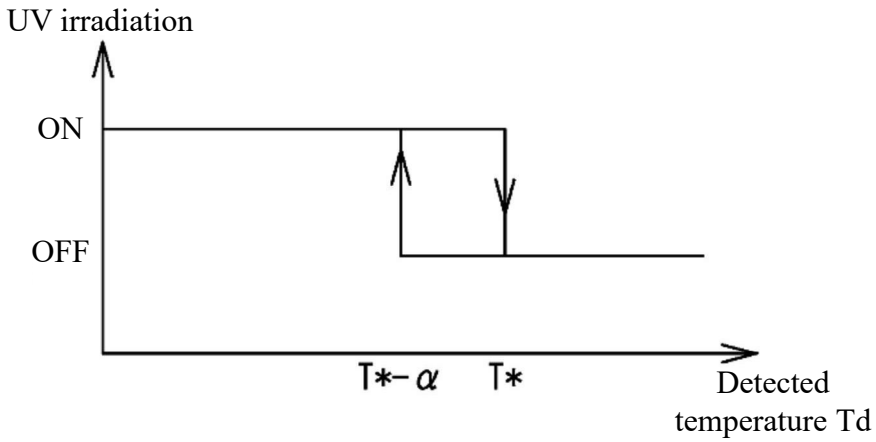


FIG. 9

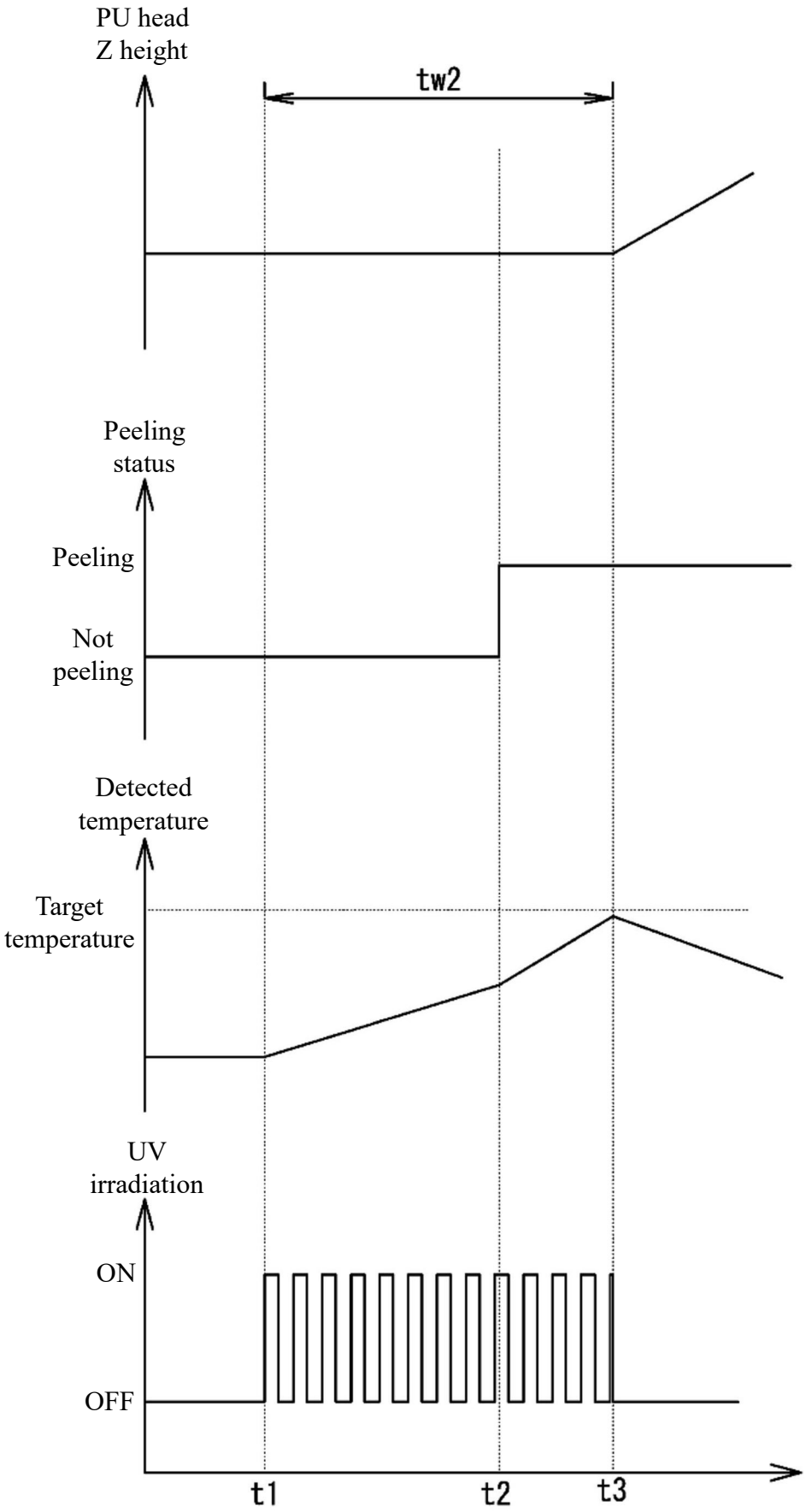

PU head
Z height
tw2
Peeling
status
Peeling
Not
peeling
Detected
temperature
Target
temperature
UV
irradiation
ON
OFF
t1
t2
t3

FIG. 10