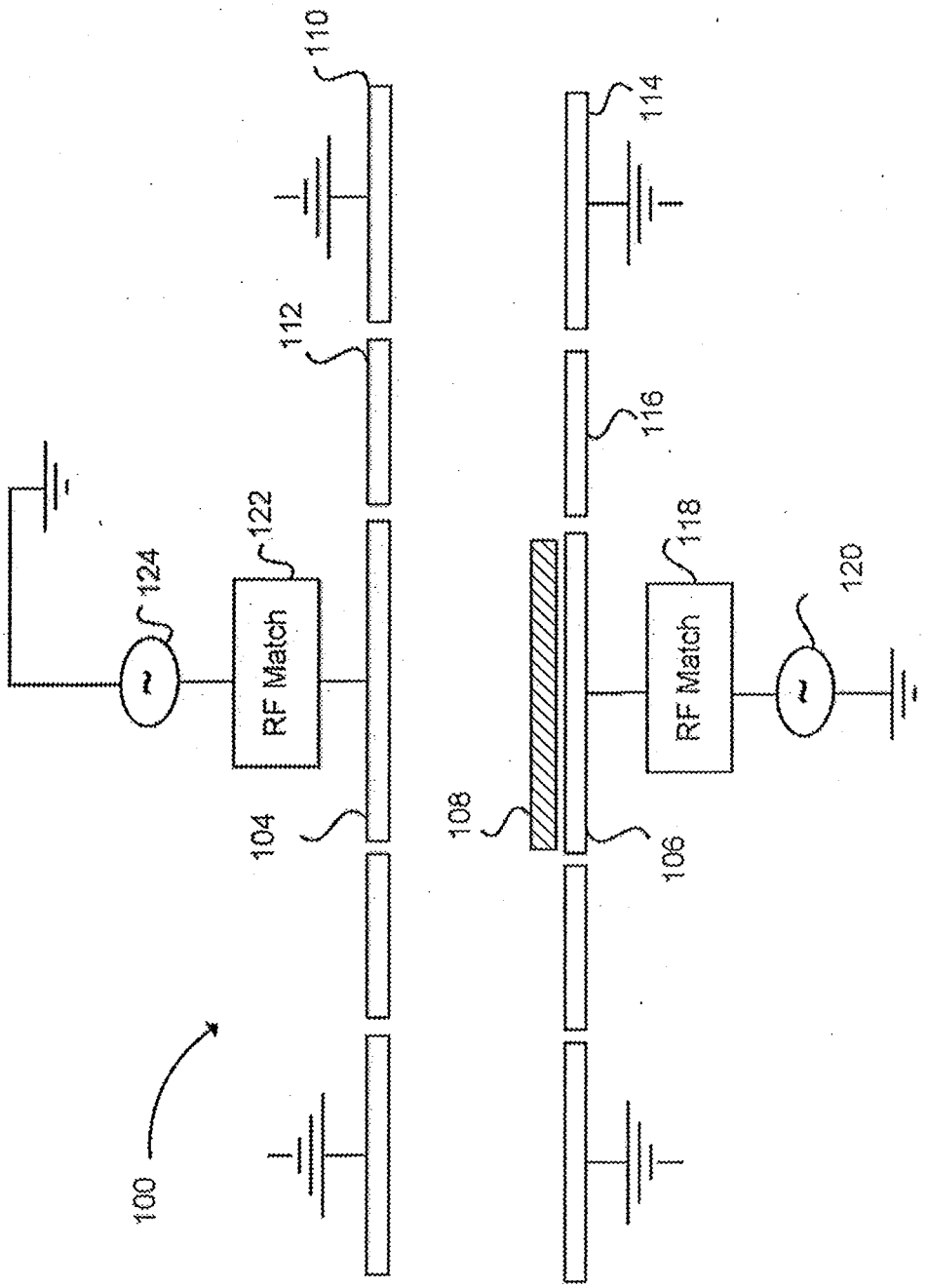


Fig, 1: (Prior art)

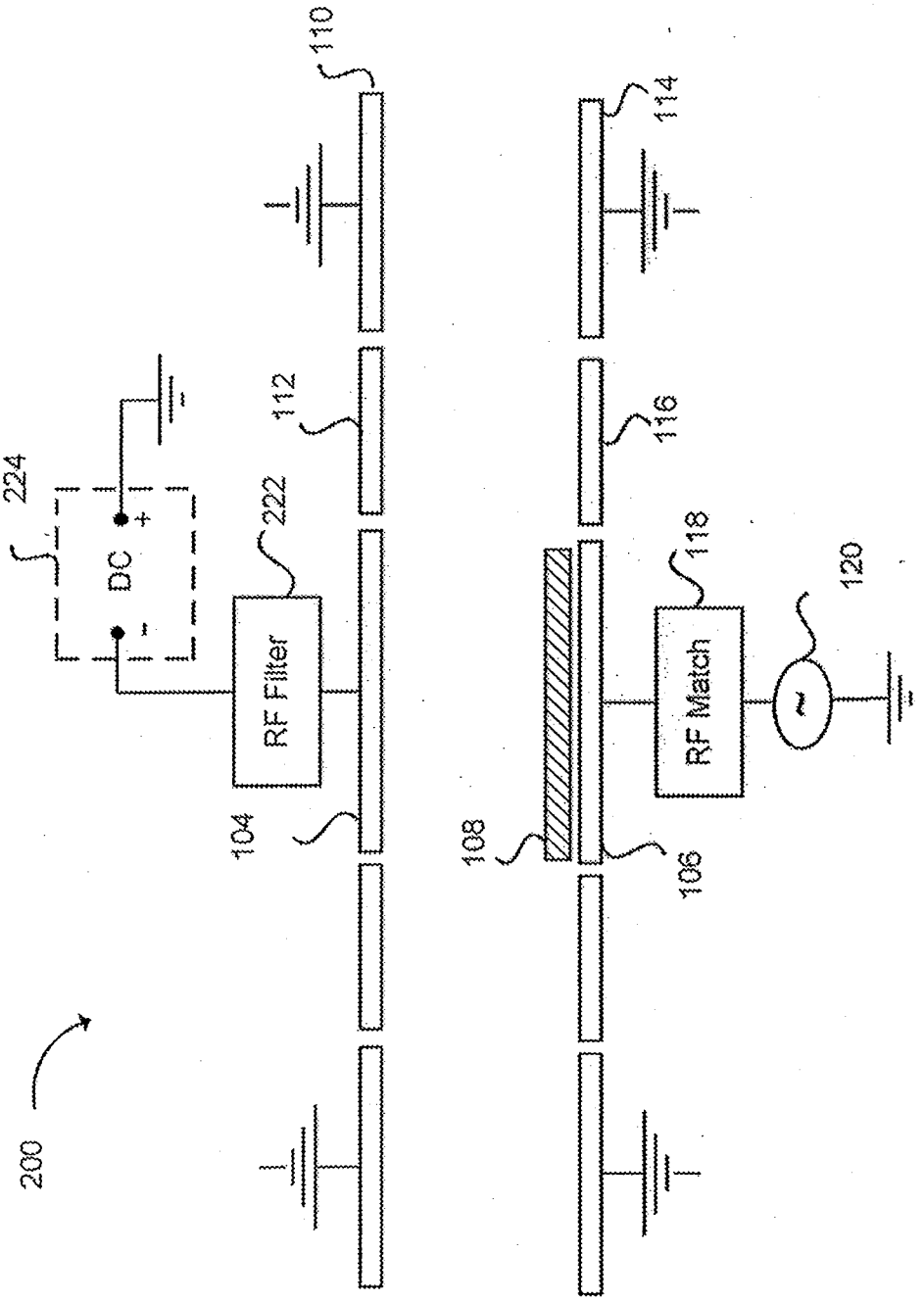


Fig. 2: (Prior art)

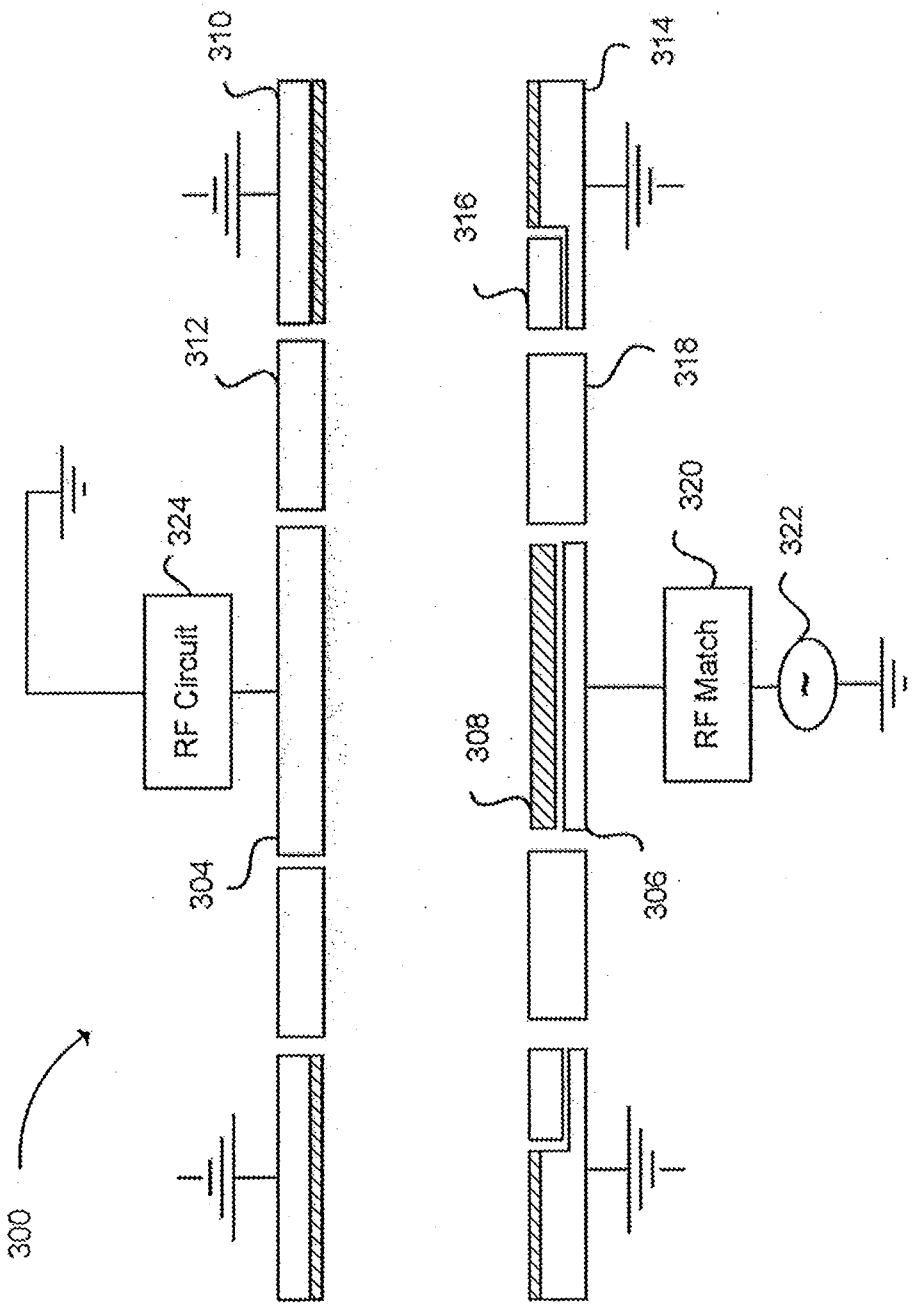

300
RF Circuit
324
304
312
310
308
306
316
318
314
320
RF Match
322

Fig. 3

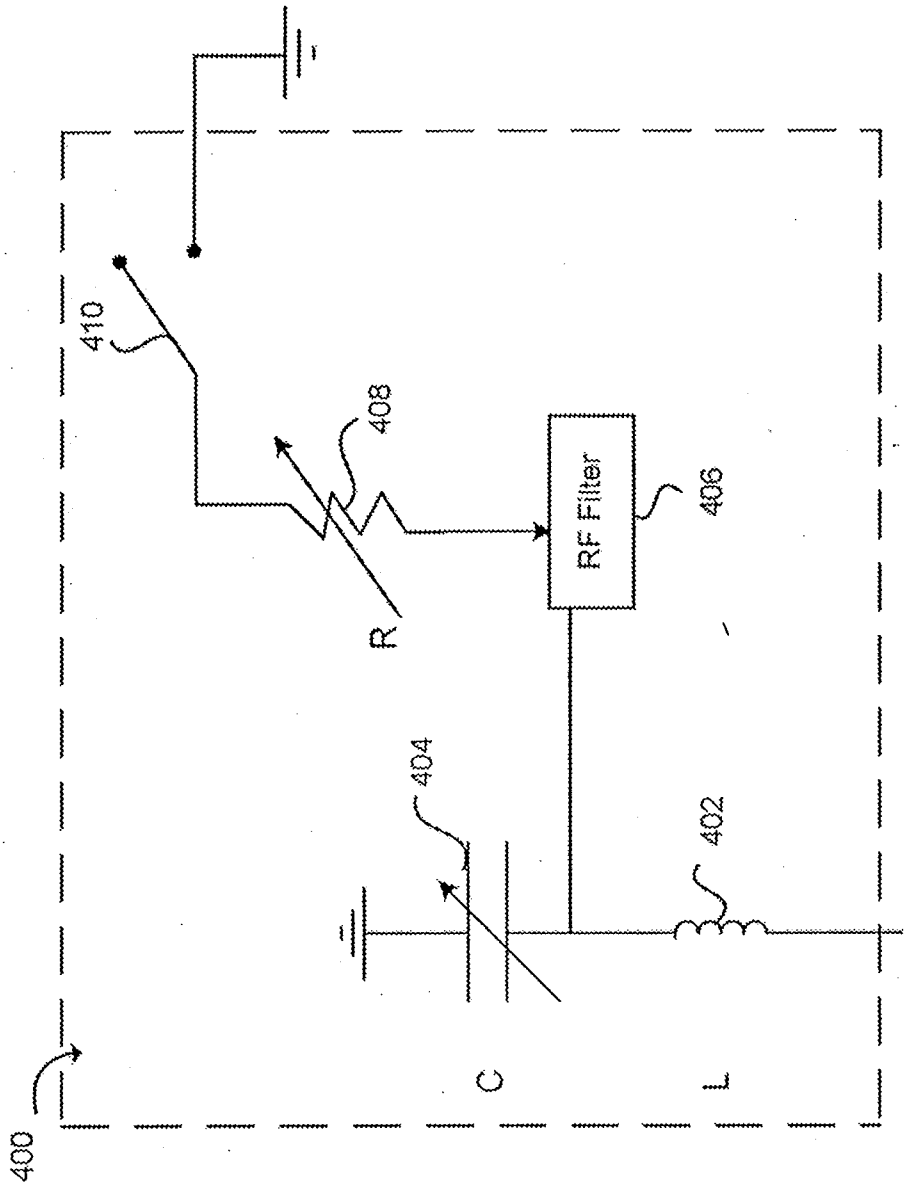


Fig. 4

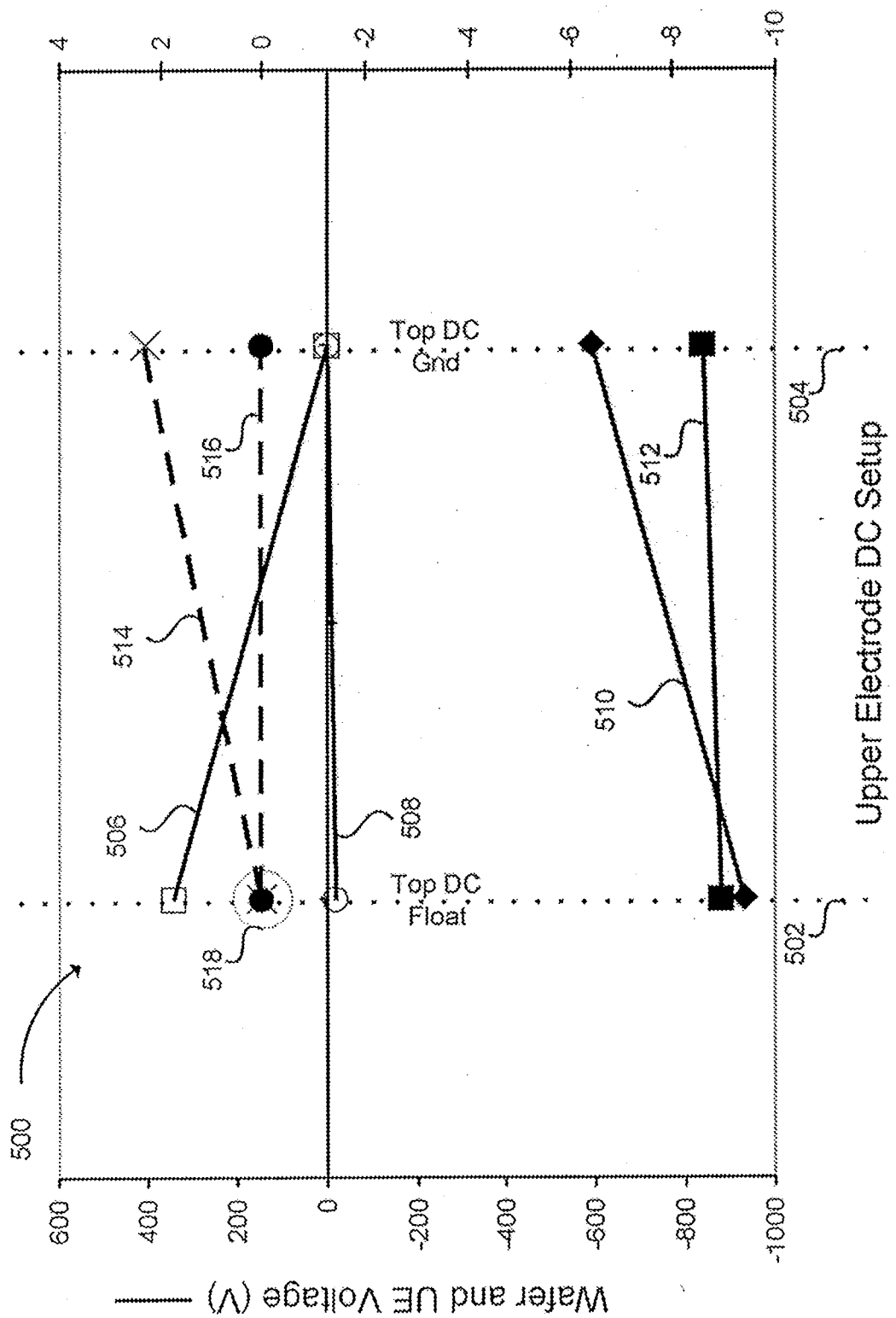

UE DC Current (A)
4
2
0
-2
-4
-6
-8
-10
Wafer and UE Voltage (V)
600
400
200
0
-200
-400
-600
-800
-1000
Top DC Gnd
Top DC Float
Upper Electrode DC Setup
500
502
504
506
508
510
512
514
516
518

Fig. 5