PCT WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 7 : G06F 3/14 — A1

(11) International Publication Number: WO 00/29934

(43) International Publication Date: 25 May 2000 (25.05.00)

(21) International Application Number: PCT/GB99/02854

(22) International Filing Date: 31 August 1999 (31.08.99)

(30) Priority Data:
9825107.7 16 November 1998 (16.11.98) GB

(71) Applicant *(for all designated States except US)*: CHECKOUT HOLDINGS LIMITED [GB/GB]; Townsend Farm Road, Townsend Industrial Estate, Houghton Regis, Dunstable, Bedfordshire LU5 5BA (GB).

(72) Inventor; and

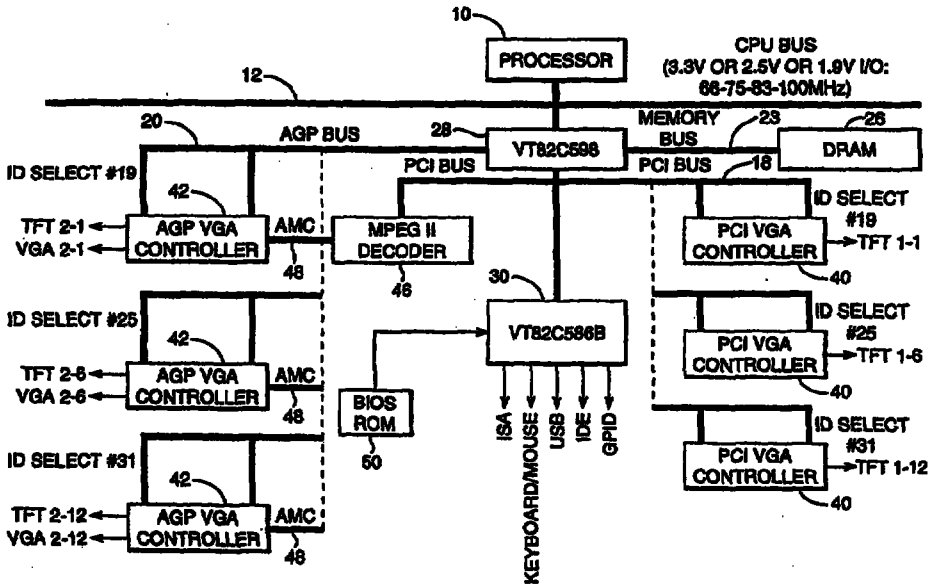
(75) Inventor/Applicant *(for US only)*: RAHEMTULLA, Karim [GB/GB]; Checkout Holdings Limited, Townsend Farm Road, Townsend Industrial Estate, Houghton Regis, Dunstable, Bedfordshire LU5 5BA (GB).

(74) Agent: LLOYD, Patrick, Alexander, Desmond; Reddie & Grose, 16 Theobalds Road, London WC1X 8PL (GB).

(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published
With international search report.

(54) Title: PC BASED SYSTEMS FOR DRIVING OF MULTIPLE SCREENS



CHECKOUT CH33 EXTENDED MULTISCREEN CONFIGURATION

(57) Abstract

A standard PC architecture is modified to enable a plurality of screens to be driven from a single PC motherboard. A plurality of screen controllers is attached to the AGP bus and the bus extended correspondingly. A plurality of screen controllers is also attached to an extended PCI bus. An MPEG II decoder, on the PCI bus is attached to each of the screen controllers on the AGP bus via a multimedia channel. Each display controller is addressed by and connected to only one of the PCI and AGP buses.