

Abstract

A subtractor (106) forms a difference signal (D) from a video input signal (C) and a further signal (M) supplied thereto. The difference signal is limited (108) and attenuated (110) and the resultant signal (F) is combined (112) with the video input signal for providing a noise reduced video output signal (H). A delay circuit (202), responsive to the video output signal, provides field (A) and frame (B) delayed video signals. A selection circuit (220) selects a median value (M) of the field delayed video signal, the frame delayed video signal and the video input signal as the further signal for application to the subtractor. Advantageously, the further signal provides the dual functions of (1) facilitating the noise reduction of the output signal and (2) concurrently reducing the occurrence of potential motion artifacts to a very substantial degree.

FIGURE 2