

ABSTRACT OF THE DISCLOSURE

A stacking tray for electrical components, such as integrated circuits, particularly those of the ball grid array (BGA type) includes an upper side and a lower side. The upper side includes corner elements of a greater height, the inner intersections of which define the periphery of the storage pockets for the electrical components. These corner elements therefore capture and support the electrical component in directions parallel to the floor of the tray. The upper side further includes lateral elements of a lesser height which extend across the periphery of the storage pocket thereby creating a ledge for capturing and supporting the electrical component in the direction perpendicular to the floor. The lower side includes lateral elements of a greater height with inner edges which abut the periphery of the storage pockets thereby capturing and supporting the electrical components in directions parallel to the floor of the tray. The lower side further includes corner elements of a lesser height which extend across the periphery of the storage pocket thereby creating an inverted ledge for capture and support the electrical component in a direction perpendicular to the floor. The corner elements are separate from and independent from the lateral elements.