

Abstract of the Disclosure

In a television receiver, employing both a beam scan velocity modulation (SVM) system (35) and an on screen display (OSD) generator (40), the OSD generator selectively operates to produce characters or graphics which are superimposed on the main video picture display. During operation of the OSD generator, the SVM circuit may tend to undesirably introduce artifacts into the picture display. Circuitry (19) is provided for selectively disabling normal SVM circuit operation during OSD operation to prevent or reduce the perception of such artifacts.

Fig. 1

This Sheet Has Been Replaced

