CLAIMS

What is claimed is:

1. A method implemented by a video decoder and comprising:

receiving, by the video decoder, a video bitstream comprising a sequence parameter set (SPS) raw byte sequence payload (RBSP), wherein the SPS RBSP comprises a first SPS level flag, wherein the first SPS level flag equal to 1 specifies that an affine inter flag is present in coding unit syntax, wherein the first SPS level flag equal to 0 specifies that the affine inter flag is not present in the coding unit syntax, and wherein the affine inter flag equal to 1 specifies that for a current coding unit, when decoding a P or a B slice, affine model based motion compensation is used to generate prediction samples of the current coding unit; and

decoding, based on the first SPS level flag, a video sequence from the video bitstream.

2. The method of claim 1, wherein the SPS RBSP further comprises a second SPS level flag, wherein the second SPS level flag equal to 1 specifies that affine_type_flag is presented in the coding unit syntax, the second SPS level flag equal to 0 specifies that affine_type_flag is not presented in the coding unit syntax.

3. The method of claim 1 or 2, wherein the video bitstream further comprises the coding unit syntax.

4. The method of claim 3, wherein the coding unit syntax comprises the affine inter flag.

5. The method of any one of claims 1 to 4, wherein the affine inter flag equal to 0 specifies that the current coding unit is not predicted by affine model based motion compensation.

6. A method implemented by a video encoder and comprising:

generating, by the video encoder, a first sequence parameter set (SPS) level flag, wherein the first SPS level flag equal to 1 specifies that an affine inter flag is present in coding unit syntax, wherein the first SPS level flag equal to 0 specifies that the affine inter flag is not present in the coding unit syntax, and wherein the affine inter flag equal to the first value specifies that for a current coding unit, when decoding a P or a B slice, affine model based motion compensation is used to generate prediction samples of the current coding unit;

encoding the first SPS level flag into an SPS raw byte sequence payload (RBSP); and

encoding the SPS RBSP into a video bitstream.

7. The method of claim 6, wherein the affine inter flag equal to 0 specifies that the current coding unit is not predicted by affine model based motion compensation.

8. An encoding apparatus comprising:

a memory; and

a processor coupled to the memory and configured to perform the method of claim 6 or 7.

9. A decoding apparatus comprising:

a memory; and

a processor coupled to the memory and configured to perform the method of any one of claims 1 to 5.

10. A computer program product comprising computer-executable instructions stored on a non-transitory medium that when executed by a processor cause an apparatus to perform the method of any one of claims 1 to 7.