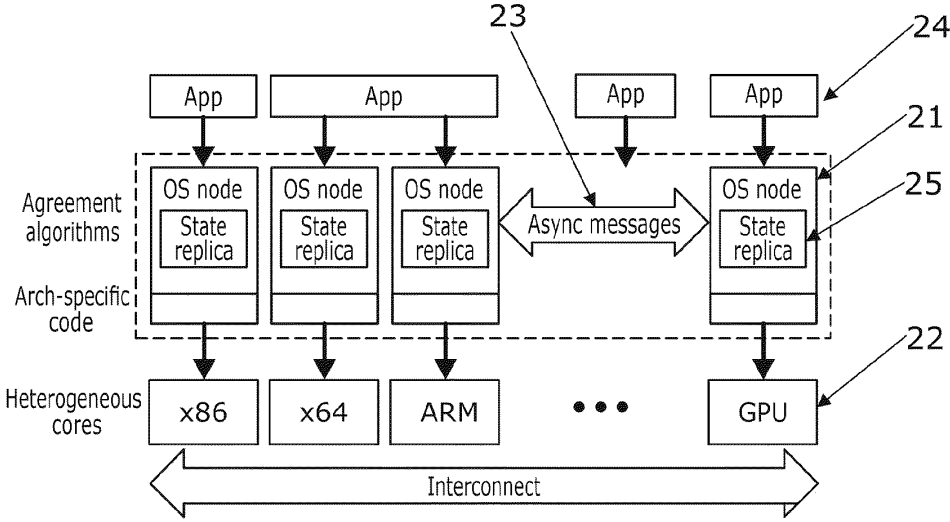
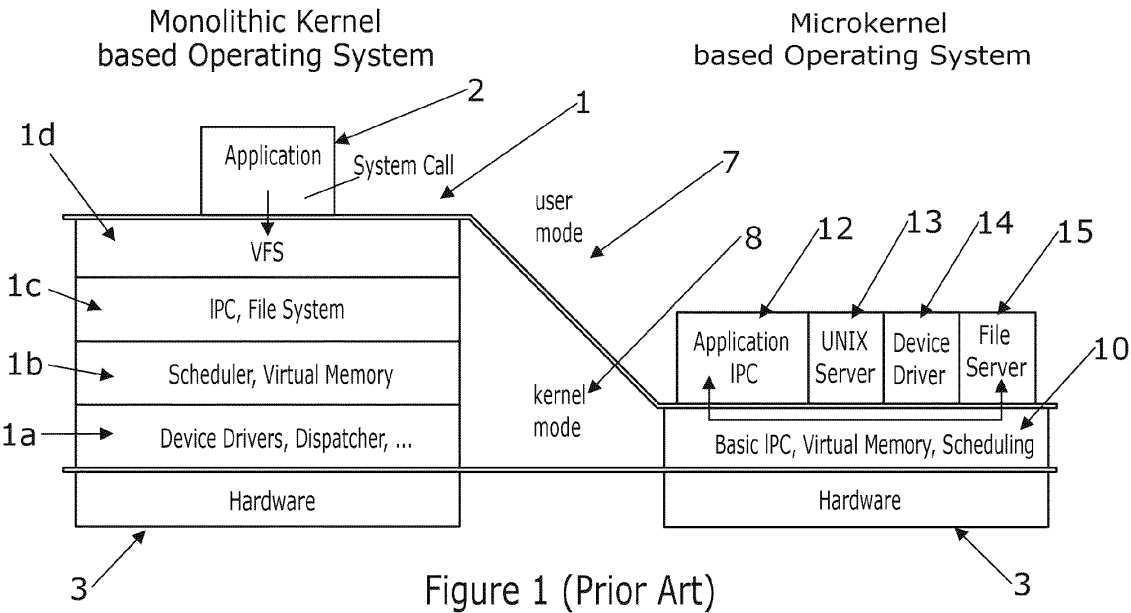




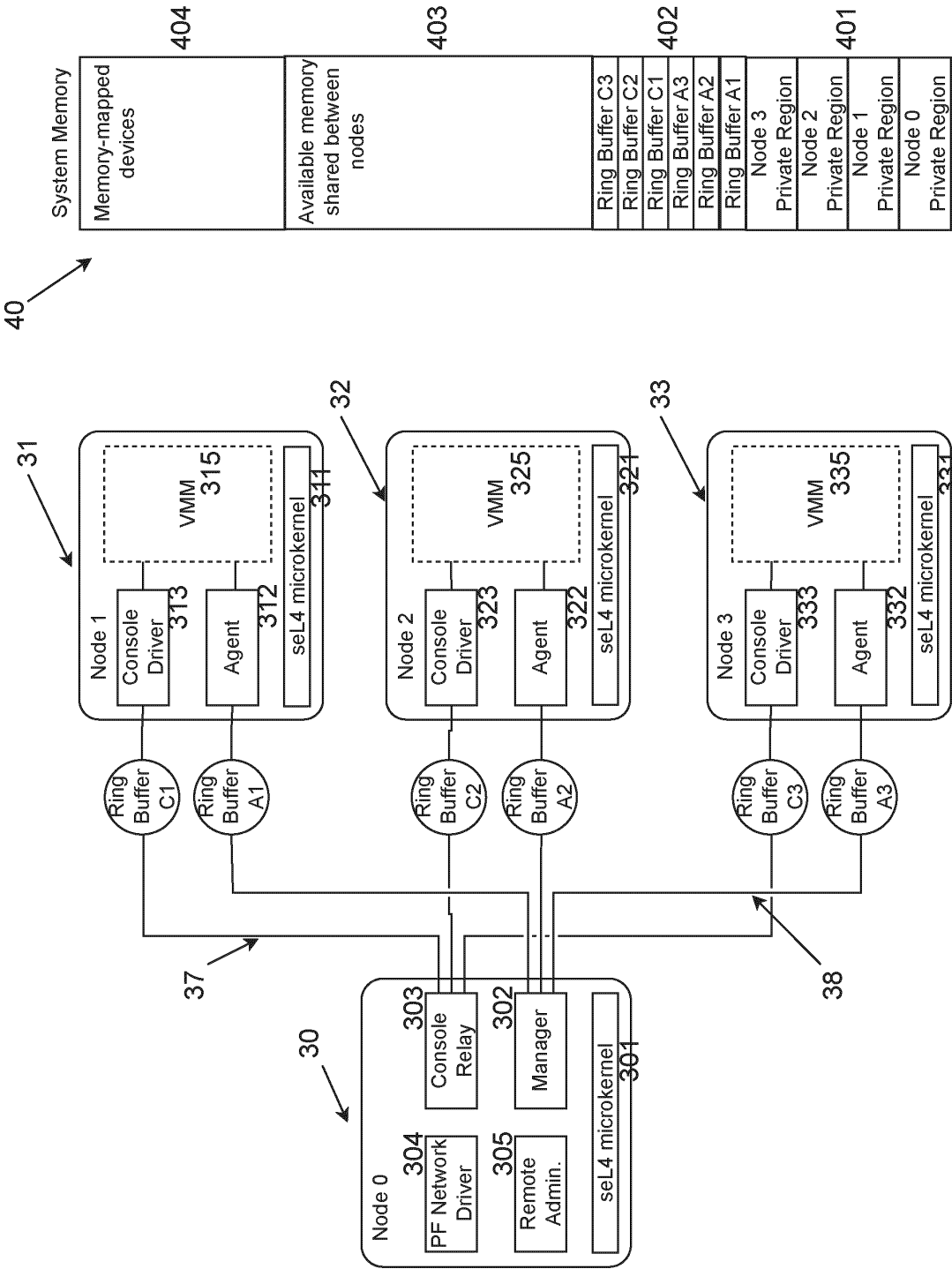


Figure 3

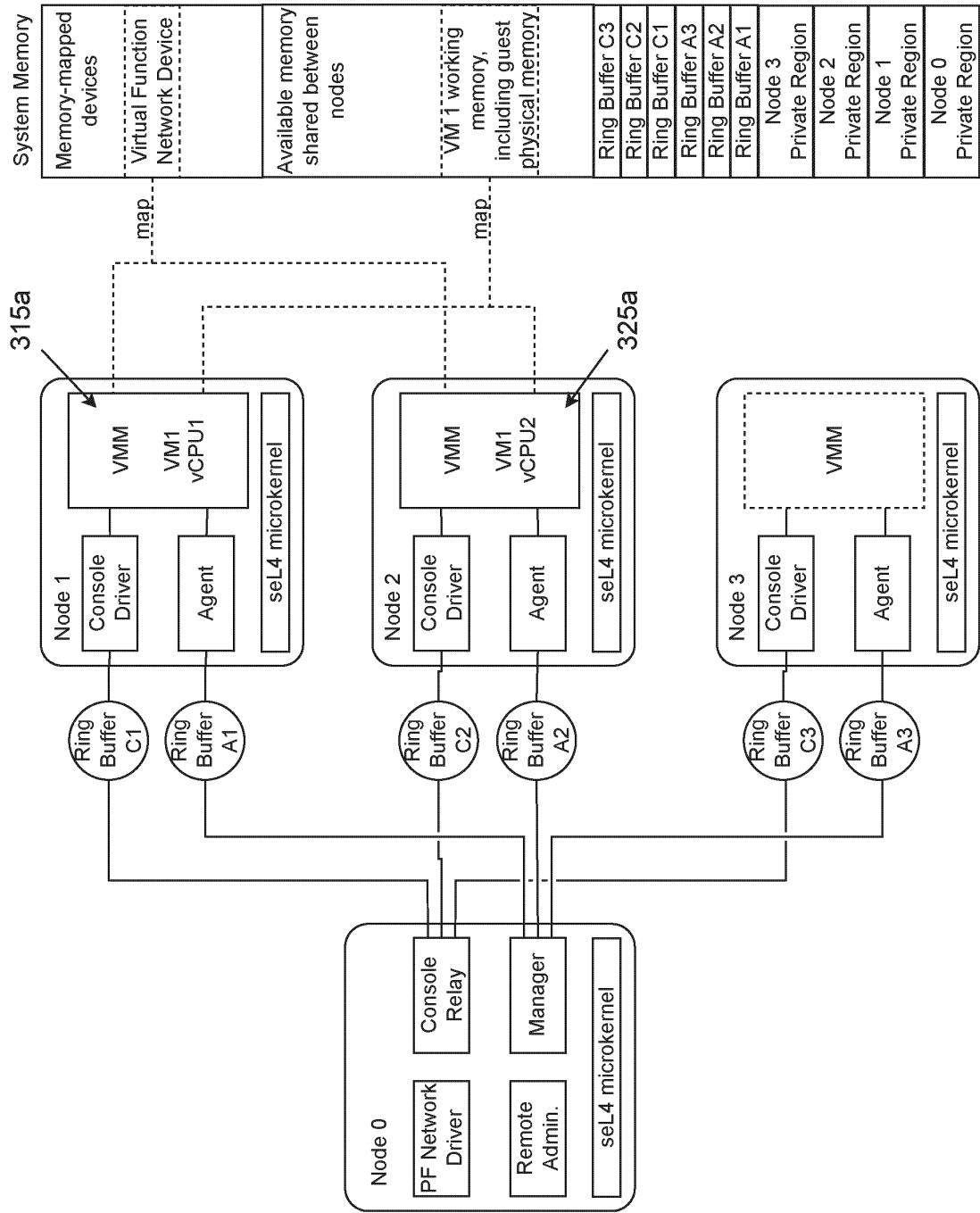


Figure 4

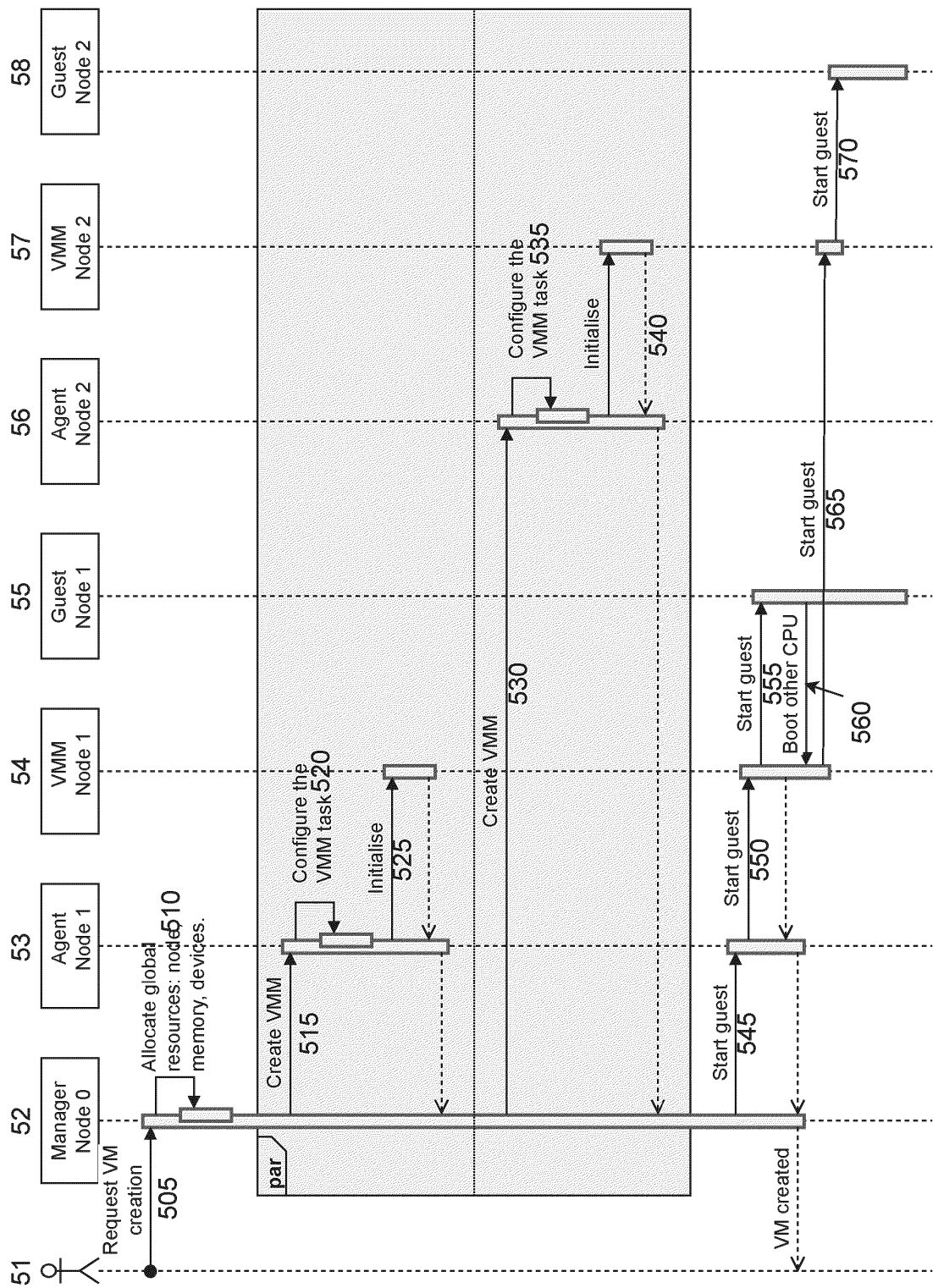


Figure 5

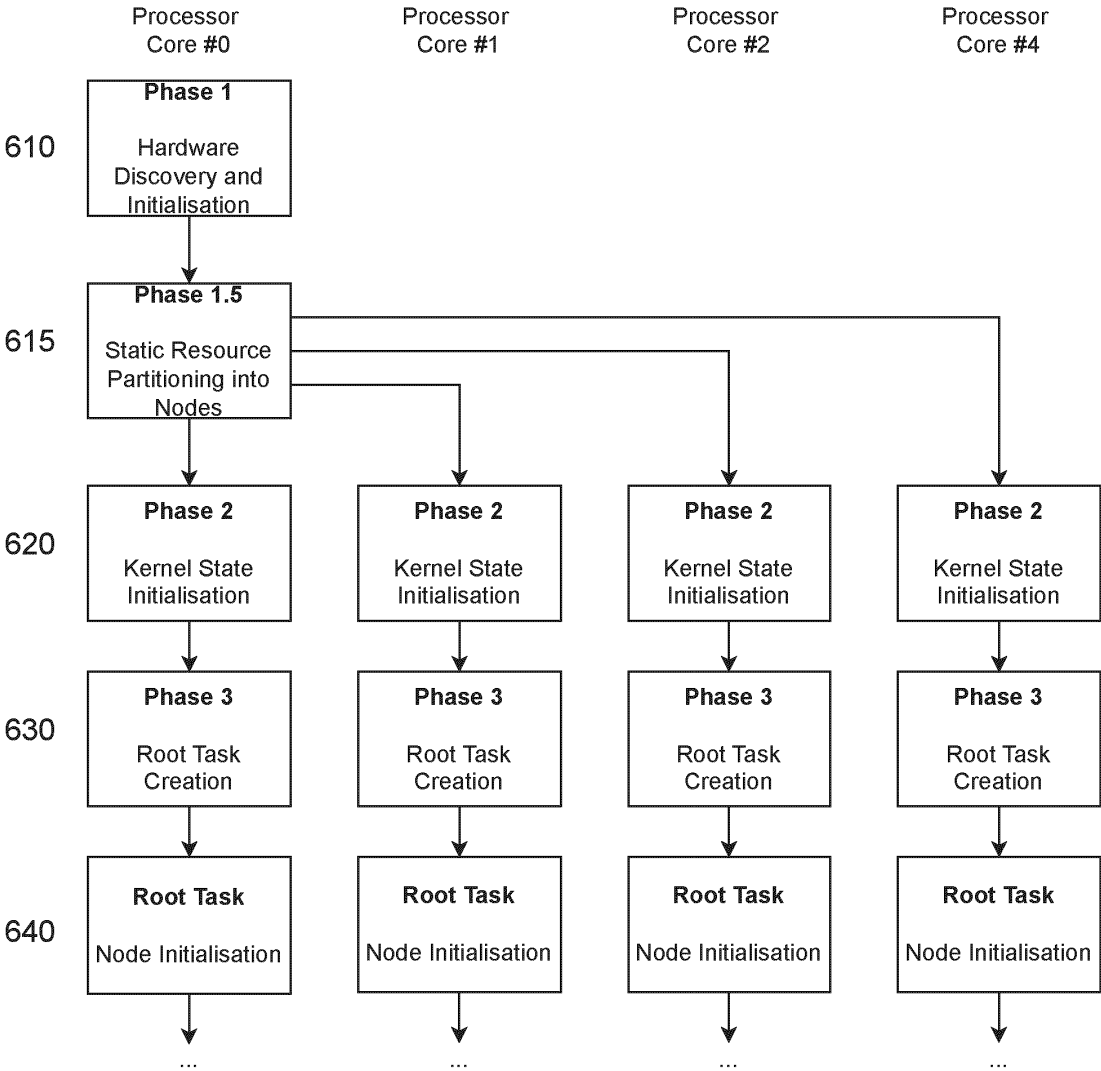


Figure 6

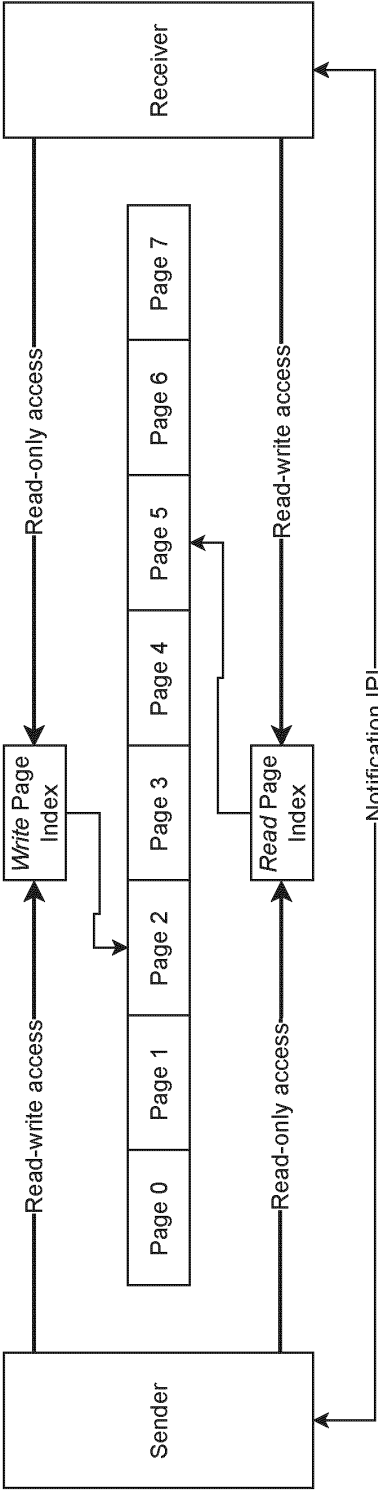


Figure 7

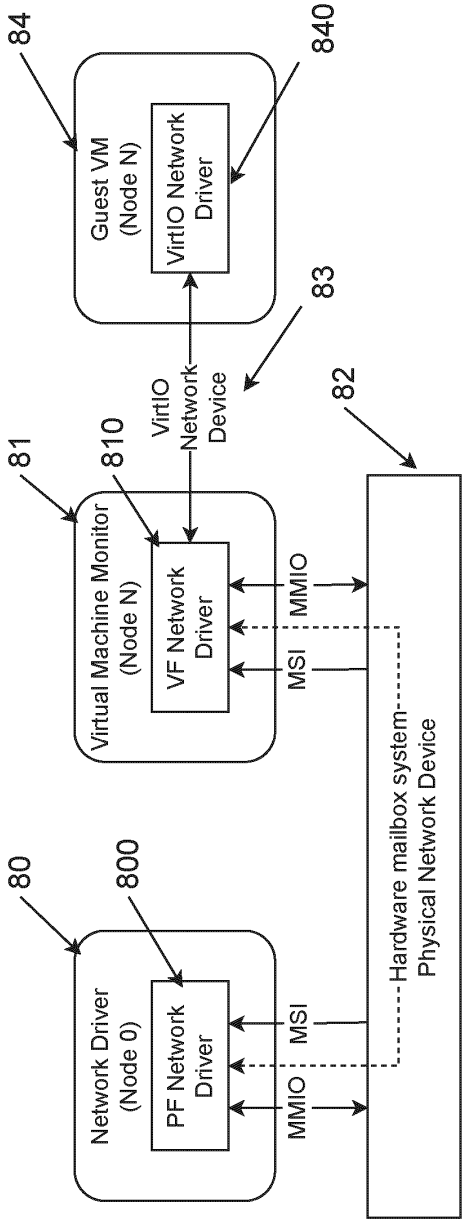


Figure 8

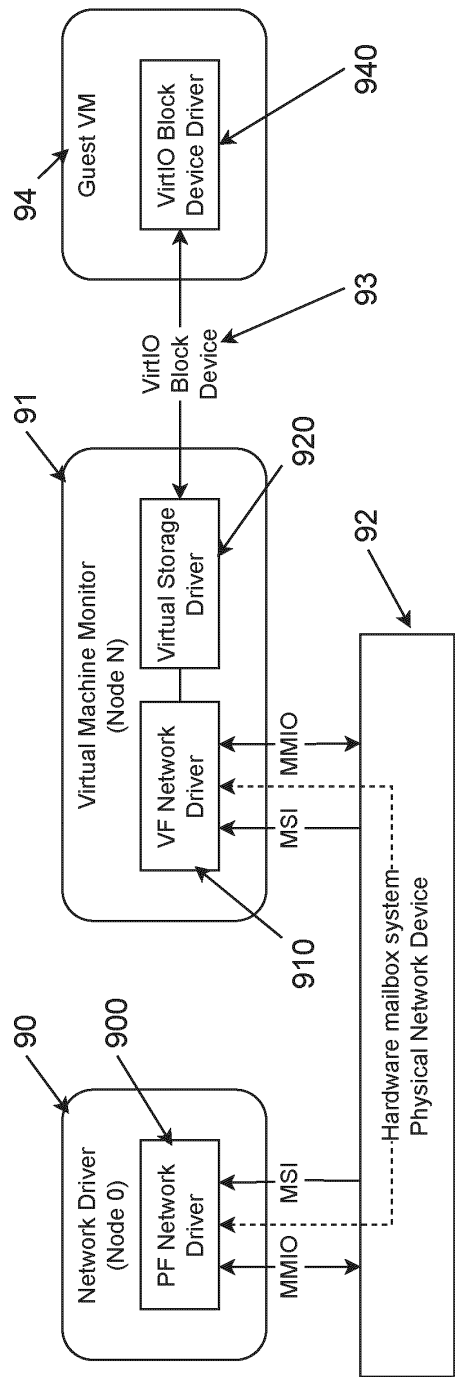


Figure 9

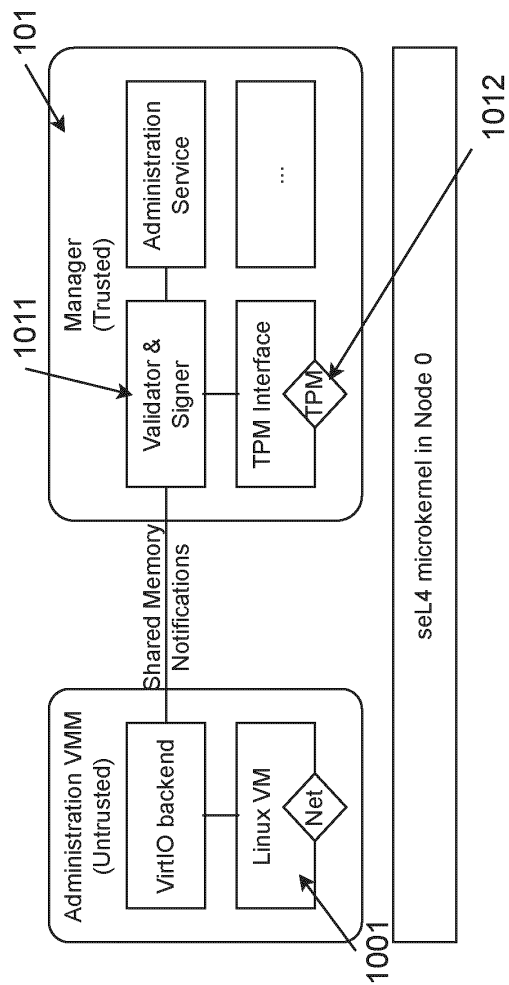


Figure 10

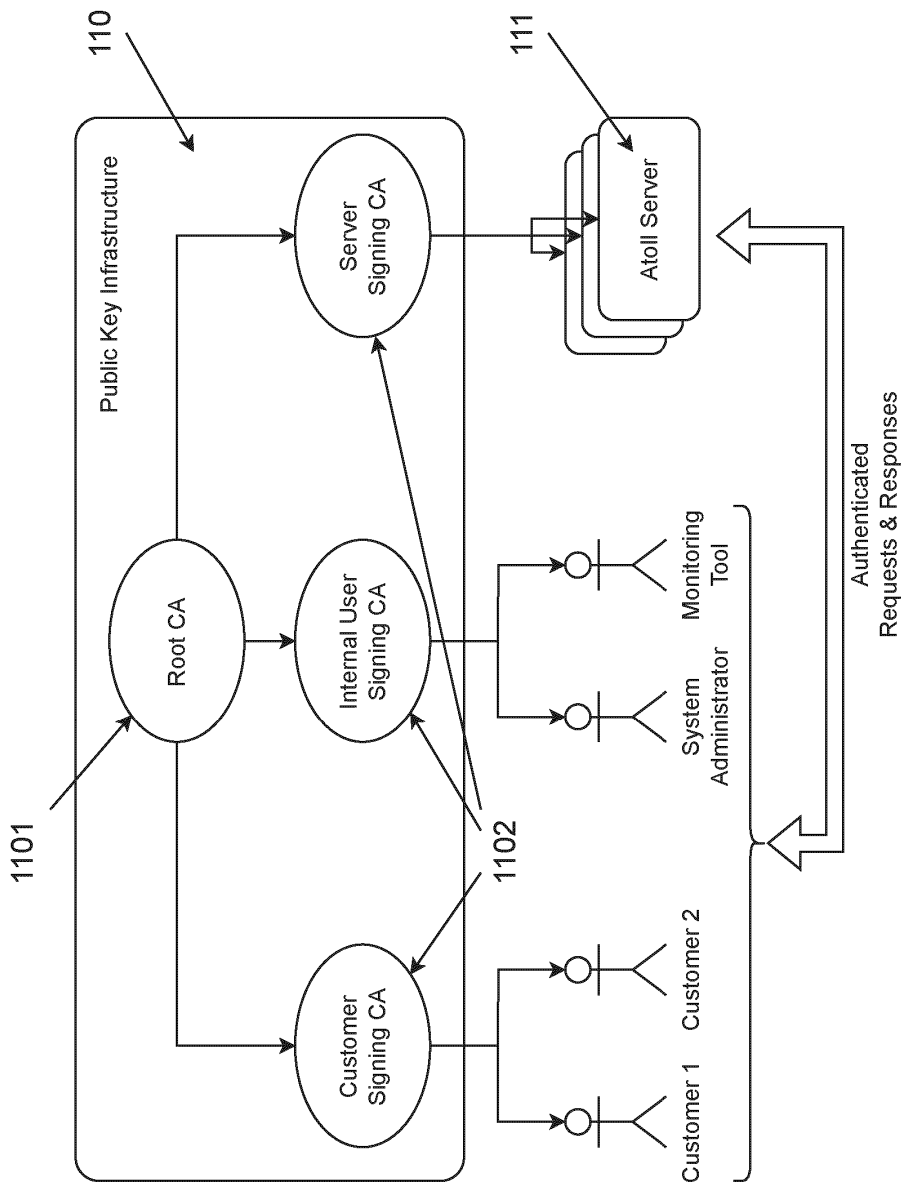


Figure 11

Physical Address
Space Allocation

Node 2: Storage controller device memory
Node 2: Storage controller device memory
Node 1: Network Interface device memory
Node 0: Network Interface device memory
Ring Buffers A: Shared physical memory
Ring Buffers B: Shared physical memory
Node 4: Private physical memory
Node 3: Private physical memory
Node 2: Private physical memory
Node 1: Private physical memory
Node 0: Private physical memory

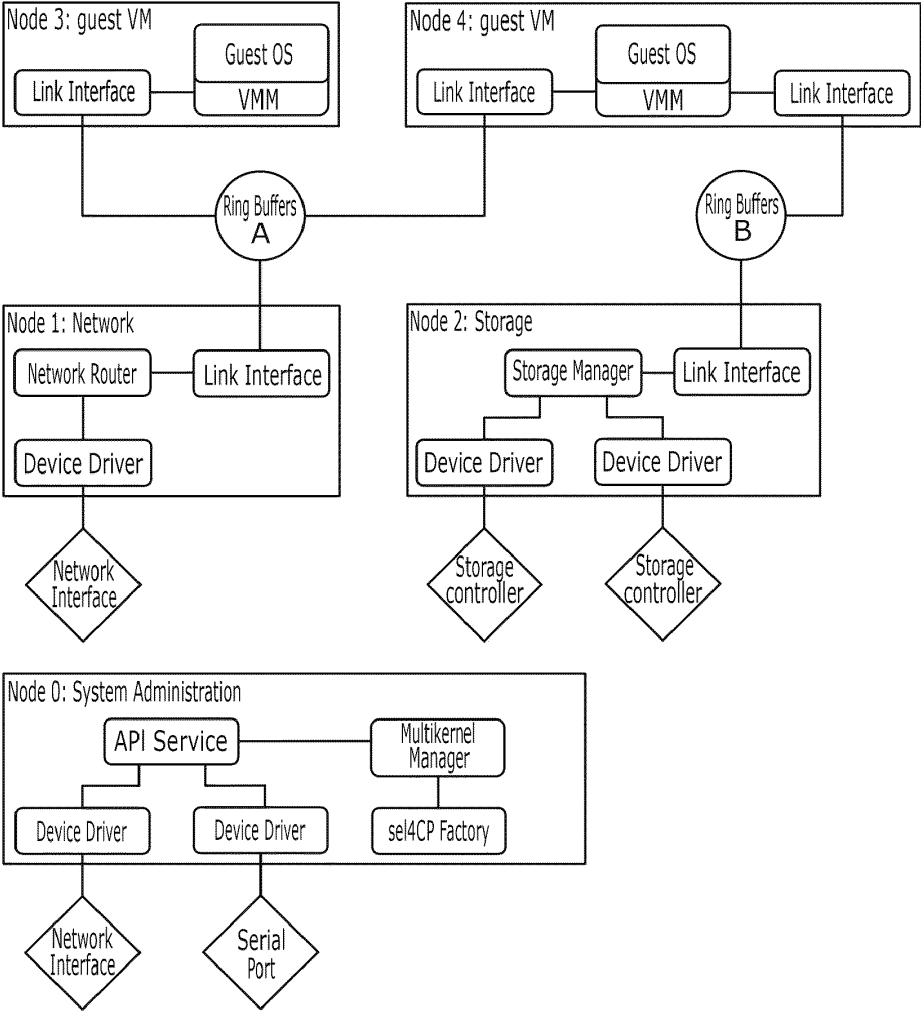


Figure 12

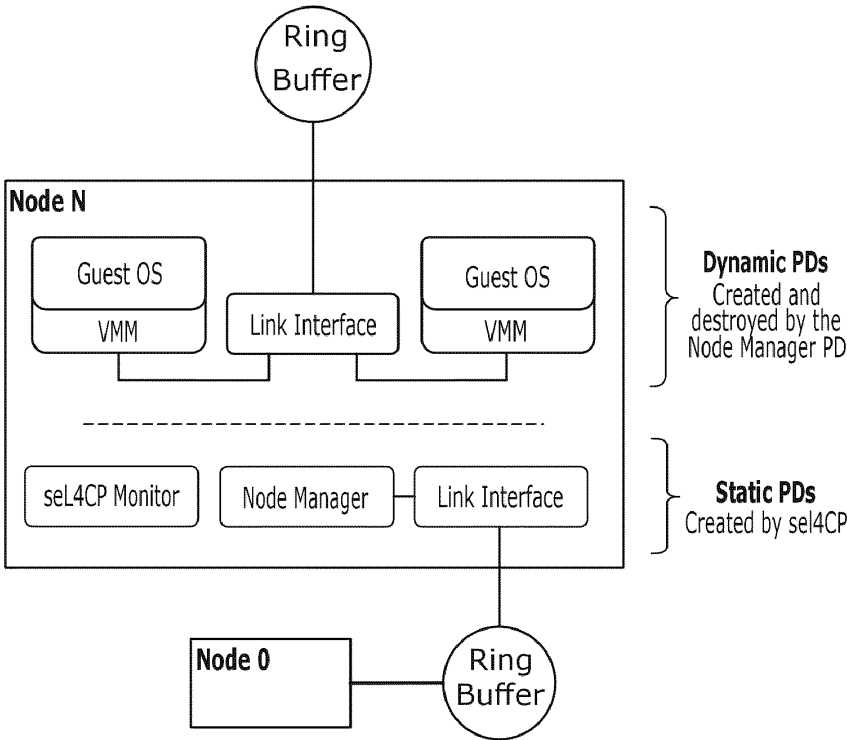


Figure 13

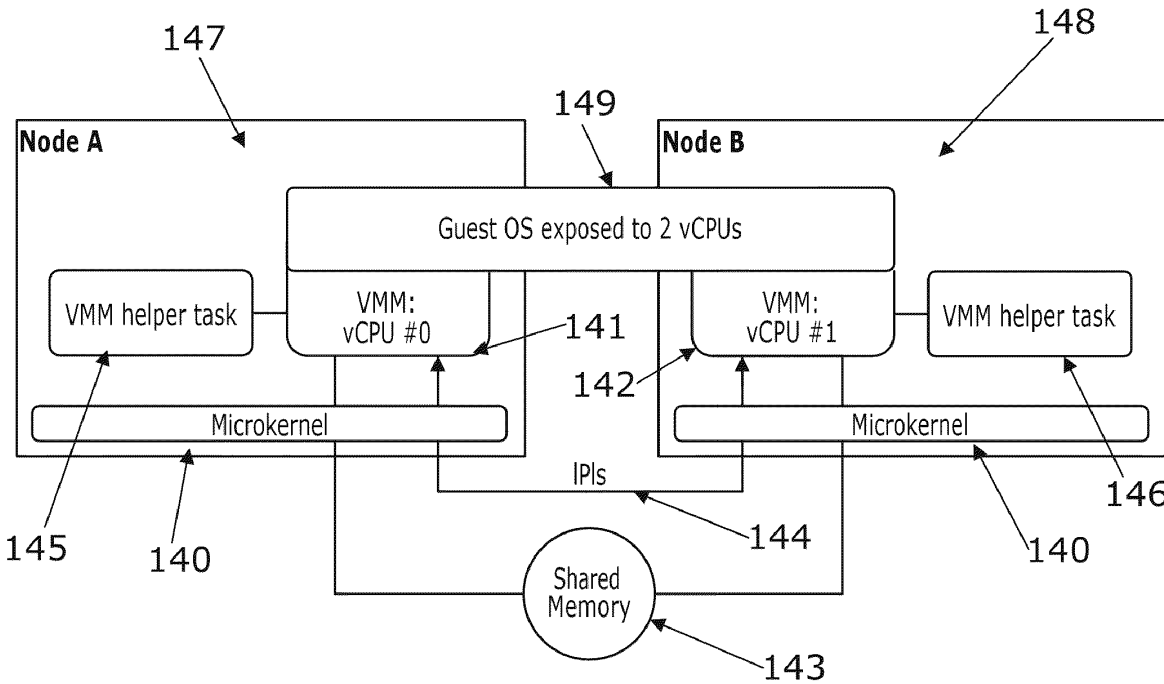


Figure 14