

METHODS AND APPARATUS FOR SERVICE ACQUISITION

Claim of Priority under 35 U.S.C. §119

[0001] The present Application for Patent claims priority to Provisional Application Serial No. 60/721,565, entitled "Method and Apparatus for Fast Channel Switching for Multimedia Broadcast Applications," filed September 27, 2005, and assigned to the assignee hereof and hereby expressly incorporated by reference herein.

[0002] The present Application for Patent claims priority to Provisional Application Serial No. 60/734,962, entitled "Methods And Apparatus For Service Acquisition" filed November 8, 2005, and assigned to the assignee hereof and hereby expressly incorporated by reference herein.

[0003] The present Application for Patent claims priority to Provisional Application Serial No. 60/742,189, entitled "Integrated Codec And Physical Layer For Efficient Multimedia Streaming" filed December 2, 2005, and assigned to the assignee hereof and hereby expressly incorporated by reference herein.

BACKGROUND

Field

[0004] The present Application relates generally to the operation of multimedia transmission systems, and more particularly, to methods and apparatus for providing fast service acquisition.

Background

[0005] In current content delivery/media distribution systems, when an application involves the transmission of a multiplex of compressed media (video and audio) where a receiver tunes to one of the channels in the multiplex, the latency in switching between channels is key to the user's experience. For example, in a conventional multimedia broadcast system, a multimedia server consists of a bank of source encoders that compress individual pieces of media that feed into a multiplexer, which consolidates the compressed media into a single multiplexed stream. The broadcast server communicates the compressed content to a broadcast receiver over a broadcast

network, which could be heterogeneous in nature and error-prone. The broadcast receiver receives at least a portion of the multiplex and a de-multiplexer extracts the media of interest. Typically, there is a client application that enables "tuning" to the media channel/program of interest. This may or may not be through user intervention.

[0006] When the source is video, channel switching is possible only at random access points (RAPs) in the compressed/coded video bitstream. These RAPs comprise intra-frames (I-frames that are independently decodable) or through progressive I-frames (which are I-blocks that are distributed over more than one video frame). The channel switching time depends on the frequency of such random access points, which is typically between 2-10 seconds (since RAPs increase the average bit rate and hence transmission bandwidth of the coded video bitstream).

[0007] Random access through I-frames and distributed I-frames for progressive intra refreshes as proposed in industry standard H.264 are the most popular approaches thus far to service acquisition at the cost of quality and bandwidth. Switching latencies in these cases are on the order of a few seconds.

[0008] In some systems channel switching and trick play (step through, fast forward, rewind) are enabled through intra frames placed/coded periodically and as often as appropriate in video streams. However, I-frames increase the bit-rate and bandwidth of the video streams significantly. Hence the frequency of I-frames is typically from one second to ten seconds in typical applications. This implies that a channel switch can, at best, occur in one second (when all required conditions are met, for example, channel switching was enabled just before an I-frame and the buffer has just refreshed).

[0009] Progressive intra refresh enables channel switching in an incremental fashion. The prediction needs to be controlled such that a complete frame is refreshed in a pre-determined duration. In this case, channel switching includes latency equal to this duration in addition to latencies induced by buffer depths and switching at lower layers.

[0010] Thus, for example, if a device is receiving a multiplex that contains one hundred compressed media channels and the device user wishes to switch between channels, conventional system may take between 1-10 seconds to perform each channel switch. The time for each channel switch is generally dependant on when in the transmission frame the switch request occurs. Thus, device users experience long and

varying delay times when switching between channels, which can be frustrating and result in an unsatisfactory user experience.

[0011] Therefore, what is needed is a system that operates to provide fast service acquisition and/or switching between services in a multiplex.

SUMMARY

[0012] In one or more aspects, a service acquisition system is provided, comprising methods and apparatus that operate to provide fast service acquisition and channel switching in a multiplex. For example, channel switching may occur in response to a user input, or in response to an interactive service. For example, an interactive channel redirection may cause a new service or channel to be acquired. Alternatively, a user input triggers a new service or channel to be acquired. In an aspect, multimedia services and non-multimedia services may be interactively acquired.

[0013] In an aspect, a method is provided for service acquisition. The method comprises generating one or more channel switch video (CSV) signals associated with one or more multimedia signals, encoding the CSV signals and the multimedia signals to produce error coded blocks, and encapsulating the error coded blocks into a multiplex signal.

[0014] In an aspect, an apparatus is provided for service acquisition. The apparatus comprises a source encoder configured to generate one or more channel switch video (CSV) signals associated with one or more multimedia signals, an error encoder configured to encode the CSV signals and the multimedia signals to produce error coded blocks, and a packer configured to encapsulate the error coded blocks into a multiplex signal.

[0015] In an aspect, an apparatus is provided for service acquisition. The apparatus comprises means for generating one or more channel switch video (CSV) signals associated with one or more multimedia signals, means for encoding the CSV signals and the multimedia signals to produce error coded blocks, and means for encapsulating the error coded blocks into a multiplex signal.

[0016] In an aspect, a machine-readable medium is provided that comprises instructions for service acquisition. The instructions upon execution cause a machine to generate one or more channel switch video (CSV) signals associated with one or more

multimedia signals, encode the CSV signals and the multimedia signals to produce error coded blocks, and encapsulate the error coded blocks into a multiplex signal.

[0017] In an aspect, at least one processor is provided for service acquisition. The at least one processor being configured to generate one or more channel switch video (CSV) signals associated with one or more multimedia signals, encode the CSV signals and the multimedia signals to produce error coded blocks, and encapsulate the error coded blocks into a multiplex signal.

[0018] In an aspect, a method is provided for service acquisition. The method comprise receiving a multiplex signal associated with a plurality of channels, detecting a selection of one of the channels, decoding a channel switch video (CSV) signal associated with the selected channel, and rendering the CSV signal.

[0019] In an aspect, an apparatus is provided for service acquisition. The apparatus comprises a receiver configured to receive a multiplex signal associated with a plurality of channels, selection logic configured to detect a selection of one of the channels, an un-packer configured to decode a channel switch video (CSV) signal associated with the selected channel, and source decoder configured to render the CSV signal.

[0020] In an aspect, an apparatus is provided for service acquisition. The apparatus comprises means for receiving a multiplex signal associated with a plurality of channels, means for detecting a selection of one of the channels, means for decoding a channel switch video (CSV) signal associated with the selected channel, and means for rendering the CSV signal.

[0021] In an aspect, a machine-readable medium is provided that comprises instructions for service acquisition. The instructions upon execution cause a machine to receive a multiplex signal associated with a plurality of channels, detect a selection of one of the channels, decode a channel switch video (CSV) signal associated with the selected channel, and render the CSV signal.

[0022] In an aspect, at least one processor is provided for service acquisition. The at least one processor being configured to receive a multiplex signal associated with a plurality of channels, detect a selection of one of the channels, decode a channel switch video (CSV) signal associated with the selected channel, and render the CSV signal.

[0023] In an aspect, a method is provided for service acquisition. The method comprises constructing a plurality of transmission frames where each transmission frame represents a selected time interval, and encoding one or more channels of data

into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer having a selected time duration.

[0024] In an aspect, an apparatus is provided for service acquisition. The apparatus comprises means for constructing a plurality of transmission frames where each transmission frame represents a selected time interval, and means for encoding one or more channels of data into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer having a selected time duration.

[0025] In an aspect, a method is provided for service acquisition. The method comprises receiving a plurality of transmission frames wherein each transmission frame represents a selected time interval and comprises one or more channels of data, and wherein selected data is encoded into predetermined transmission frames. The method also comprises buffering the plurality of transmission frames with a single buffer having a selected time duration, wherein channel jitter is absorbed.

[0026] In an aspect, an apparatus is provided for service acquisition. The apparatus comprises means for receiving a plurality of transmission frames wherein each transmission frame represents a selected time interval and comprises one or more channels of data, and wherein selected data is encoded into predetermined transmission frames. The apparatus also comprises means for buffering the plurality of transmission frames with a single buffer having a selected time duration, wherein channel jitter is absorbed.

[0027] Other aspects will become apparent after review of the hereinafter set forth Brief Description of the Drawings, Description, and the Claims.

BRIEF DESCRIPTION OF THE DRAWINGS

[0028] The foregoing aspects described herein will become more readily apparent by reference to the following description when taken in conjunction with the accompanying drawings wherein:

[0029] **FIG. 1** shows an aspect of a transmission stream that comprises a sequence of superframes that are used to transport a multimedia multiplex;

[0030] **FIG. 2** shows an illustration of RS sequencing;

- [0031] FIG. 3 shows a network that comprises an aspect of a service acquisition system;
- [0032] FIG. 4 shows a server for use in aspects of a service acquisition system;
- [0033] FIG. 5 shows a Data Channel MAC layer capsule structure prior to operation a service acquisition system;
- [0034] FIG. 6 shows a Data Channel MAC layer capsule structure after operation of a service acquisition system;
- [0035] FIG. 7 shows a method for providing aspects of a service acquisition system;
- [0036] FIG. 8 shows a device for use in aspects of a service acquisition system;
- [0037] FIG. 9 shows a method for use in aspects of a service acquisition system;
- [0038] FIG. 10 shows an aspect of a communication system;
- [0039] FIG. 11 shows a diagram of OSI layers or a protocol stack for use in aspects of a service acquisition system;
- [0040] FIG. 12 shows a diagram that illustrates Super Frame data processing from application through physical layer at both network and client sides of a communication system;
- [0041] FIG. 13 shows a diagram that illustrates fast channel switching provided by aspects of a service acquisition system;
- [0042] FIG. 14 shows a diagram that illustrates the flow of T-packets in aspects of a service acquisition system;
- [0043] FIG. 15 shows a diagram that illustrates a video frame arrangement in an aspect of a T-packet;
- [0044] FIG. 16 shows a diagram that illustrates a video frame arrangement in an aspect of a T-packet;
- [0045] FIG. 17 shows a diagram that illustrates an arrangement of transport headers (TH) and sync headers (SH) used in base and enhancement layers of a Super Frame;
- [0046] FIG. 18 shows a forward error correction (FEC) code block structure for use in aspects of a service acquisition system;
- [0047] FIG. 19 shows a frame organization for providing video and audio bitstreams for use in a service acquisition system;
- [0048] FIG. 20 shows a server for use in aspects of a service acquisition system;
- [0049] FIG. 21 shows a device for use in aspects of a service acquisition system;

[0050] FIG. 22 shows a server for use in aspects of a service acquisition system; and

[0051] FIG. 23 shows a device for use in aspects of a service acquisition system.

DESCRIPTION

[0052] The following description describes aspects of a service acquisition system for acquisition and switching between channels of a multimedia multiplex (i.e., early entry/fast tuning). The system is especially well suited for use with virtually any type of device that operates to receive a multimedia multiplex. For example, such devices include but are not limited to, portable telephones, PDAs, email devices, notebook computers, tablet computers or any other type of receiving device. Furthermore, aspects of the system may be used in any type of network environment, including but not limited to, communication networks, content distribution networks, public networks, such as the Internet, private networks, such as virtual private networks (VPN), local area networks, wide area networks, long haul networks, or any other type of data or communication network.

[0053] For the purpose of this description, aspects of a service acquisition system are described with reference to switching between channels of a multimedia multiplex that comprises a sequence of superframes where each superframe comprises four frames of data. However, aspects of the service acquisition system are not limited to this superframe structure and are equally applicable to provide service acquisition for other types of multiplex structures.

[0054] For the purpose of this description, the service acquisition system is described with reference to implementations using an open system interconnection (OSI) model. For example, various aspects are described with reference to implementation at one or more layers of the OSI model. However, aspects of the service acquisition system are not limited to this implementation, and may be implemented using any other type of hardware/software structure, configuration, or communication model.

Overview

[0055] FIG. 1 shows an aspect of a transmission stream 100 that comprises a sequence of superframes in a multimedia multiplex. Each superframe (102, 104)

comprises four frames **106** and overhead information symbols (OIS) **108**. It should be noted that the OIS symbols **108** associated with each superframe comprises information necessary for the decoding/tuning of an associated superframe. Thus, the OIS **108** is applicable to superframe **104**.

[0056] In one or more aspects, the service acquisition system operates to allow a receiver to receive the multimedia multiplex and perform fast acquisition and switching between channels in the multiplex. For example, the system allows fast channel switching to occur in the middle of a superframe and utilizes any partial data received during that superframe for the new channel. The partial data referred to is the data retrieved from the last three or less of the four frames present in the superframe during which the switch occurred. This results in reduced acquisition latency on the device since the device can utilize the partial data.

[0057] In aspects of the service acquisition system, a broadcast server operates to re-arrange application data transmitted in a superframe in a way that satisfies the following.

1. The application data (e.g. Video frames) in an individual frame of a superframe is decodable without any dependency on the data in other frames of the previous superframe. (Application Layer)
2. The result of Reed Solomon (RS) sequencing performed at the MAC layer should have no effect on the contiguity of the Real Time application data. That is, the application data that can fit in a frame is not dispersed across multiple frames in the superframe and maintains its contiguous nature. (MAC Layer)
3. The most important information (for example working keys) along with the Real Time application data is pushed as far back in the frames as possible. That is, any MAC layer packets containing padding octets are present at the start of the superframe followed by the application data (Stream 1 or 2) and then the Stream 0 data. This will ensure that a receiver always has an opportunity to get the important information carried on stream 0, as long as some application data has been retrieved before an Early Entry/Fast tuning trigger. (Stream and MAC Layer)
4. Stream layer octet interleaving is eliminated to prevent dispersing the application data (Stream layer)

[0058] In aspects of the service acquisition system, a receiving device operates to ensure that OIS information is available for the superframe in which Early Entry/Fast tuning is performed.

Modifications to Conventional Systems

[0059] FIG. 2 shows an illustration of RS sequencing 200. In a typical implementation, the MAC layer of the AIS stack takes MAC layer capsule data (multiple RS code blocks) and disperses them as across a superframe. This procedure is known as RS sequencing and is executed for all RS code blocks. For example, 16 MAC layer packets in each of the RS code blocks (202, 204) are distributed equally across four frames (i.e., 4 MAC Layer packets each).

[0060] The fragment of an RS code block in a frame containing four MAC layer packets is referred to as an RS sub-block 206. Within a frame, each RS sub-block is interleaved with the sub-blocks from other RS code blocks. For example, if there are two RS code blocks (102, 104) in a superframe, each frame contains the following in the order specified:

1. First MAC layer packet from a sub-block of first RS code block.
2. First MAC layer packet from the corresponding sub-block from second RS code block.
3. Second MAC layer packet from the same sub-block of first RS code block.
4. Second MAC layer packet from the corresponding sub-block of second RS code block.
5. And so on, till the fourth MAC packet.

[0061] The data delivered by the Real Time application is processed in a contiguous fashion by the stream layer of the AIS stack and during MAC Layer capsule formation. This contiguous nature changes when the RS code blocks containing the application data are dispersed across the superframe to achieve time diversity gains that provides better RS coding performance.

[0062] Any contiguous application data (for example, video-frame data that occupies a single frame worth of data) is dispersed among more than one frame by the MAC Layer sequencing of RS code blocks. The receiver has to wait for more than one

frame to gather application data that when assembled in a contiguous arrangement is less than a frame worth of data.

[0063] In one or more aspects, the service acquisition system operates to allow a device to extract as much real time data as quickly as possible within a single frame. To achieve this it is desirable that the real time application data maintain its contiguous nature while sent over the four frames of a superframe.

Service Acquisition

[0064] In aspects of a service acquisition system, application data is pre-interleaved by the Data Channel MAC protocol to nullify the effects of MAC Layer RS code block sequencing as follows. In an aspect, all the streams in the super frame undergo this pre-interleaving for uniformity of implementation. In an aspect, the following pre-interleaving functions are performed.

1. Data Channel MAC Protocol allocates a pre-interleaving buffer every superframe of size $(\text{NumR-SCodeBlocks} * K * 122)$ octets, where
 - a. "NumR-SCodeBlocks" is the number of RS Error Control Blocks present in the Data Channel MAC layer capsule for that superframe.
 - b. K specifies the number of data octets in the RS code word. For example, outer-code rate of 12/16 has a K of 12.
2. This buffer is formatted into a table having "NumR-SCodeBlocks" columns and 'K' rows. Thus, each cell of this table is 122 octets long.
3. The protocol starts to fill in the pre-interleaving buffer with MAC layer packets row-wise starting with the padding MAC layer packets first, followed by the MAC layer packets carrying stream 2 data, stream 1 data and stream 0 being the last.
4. Each column thus forms the top 'K' rows of an error control block.
5. RS parity octets are added for each column formed above followed by RS sequencing before delivering it to the Physical Layer.

[0065] Thus, by pre-interleaving it is possible to maintain the contiguous nature of the application data within each frame of a superframe.

Channel Switch Version (CVS)

[0066] In one or more aspects, the service acquisition system operates to encode a channel switch version (CSV) of the media in addition to normal encoding, and transmits this CSV data either in-band or out-of-band. In an aspect, the CSV is a down-sampled lower frame rate version of the video under consideration. It should be noted that similar versions can be coded for other media types, such as audio, data, etc. depending on the application. Aspects of the CSV generation may also be utilized by other applications that may include use of the CSV version (or parts of it) for error recovery, error concealment, and error control (i.e., stopping accumulation of prediction and channel errors).

[0067] The following description pertains to video broadcast application data and associated video compression algorithms. The channel switch version of the video application data is an independently decodable piece of data that is transmitted periodically to provide random access into the coded video bitstream (in addition to I-frames or distributed I-frames). When a change in the media of interest in a multiplex is requested at a receiving device, the request is communicated to the application layer, which then starts to decode the CSV. This operation is instantaneous since the CSV can be inserted at the switch points dictated by the transport and physical layer protocols. The media then resumes to full quality at the next random access point. As a result, the device user experiences a fast channel switch.

[0068] FIG. 3 shows a network 300 that comprises an aspect of a service acquisition system. The network 300 comprises a broadcast server 302 that operates to broadcast a multimedia multiplex to a device 304 using a network 306. The server 302 communicates with the network 306 through communication link 308 that comprises any suitable type of wired and/or wireless communication link. The network 306 communicates with the device 304 through communication link 310 that in this aspect comprises any suitable type of wireless communication link. For example, the communication link 310 may comprise an orthogonal frequency division multiplex (OFDM) communication link known in the telecommunication industry.

[0069] The device 304 is a mobile telephone but may comprise any suitable device, such as a PDA, email device, pager, notebook computer, tablet computer, desktop computer or any other suitable device that operates to receive a multimedia multiplex signal.

[0070] In an aspect of the service acquisition system, the server **302** comprises source encoders **316** that operate to receive input video signals **314**. In an aspect, 256 input video signals are input to 256 source encoders **316**. However, aspects of the system are suitable for use with any number of input video signals and corresponding source encoders.

[0071] Each of the source encoders **316** produces an encoded signal that is input to a forward error correction (FEC) encoder **320**. Each of the source encoders **316** also produces a channel switch video signal that is input to a CSV packer **318**. The CSV signal is a low resolution independently decodable version of a corresponding input signal. A more detailed description of the CVS signal is provided in another section of this document. The CSV packers **318** operate to pack (or encapsulate) the CSV signals and outputs encapsulated CVS signals to the FEC encoder **320**.

[0072] The FEC encoder **320** operates to error encode the signals received from the source encoders **316** and the CSV packers **318** to produce error encoded blocks that are input to a pre-interleaver **322**. In an aspect, the FEC encoder **320** provides RS coding. The pre-interleaver **322** arranges the error encoded blocks so that selected blocks appear at predetermined locations in a transmission frame after the operation of a packer **324**. For example, the pre-interleaver **322** operates to perform the functions described above to maintain the continuous nature of the application data in the generated transmission frames. As a result, the pre-interleaver **322** operates to arrange the error coded blocks so that they are optimized to provide fast service acquisition.

[0073] The packer **324** operates to encapsulate the output of the pre-interleaver **322** into a transmission frame. The operation of the pre-interleaver **322** enables fast service acquisition because it positions the CSV and other important frame information at strategic locations in the transmission frame so that fast service acquisition can occur. A more detailed description of the pre-interleaver process is provided in another section of this document.

[0074] The output of the packer **324** is a transmission frame that is input to a modulator/transmitter **326** that operates to transmit a modulated transmission frame **328** over the network **306**. For example, the modulated transmission frame **328** is transmitted from the server **302** to the device **304** using the network **306**. The transmission frame **328** comprises a sequence of superframes where each superframe comprises four frames.

[0075] At the receiving device 304, the modulated transmission frame 328 is received by a demodulator/receiver 330 that outputs the received transmission frame to un-packer 332. In an aspect, the demodulator/receiver 330 comprises a single buffer 344 having a finite time duration that operates to absorb channel jitter and support outer FEC decoding. The un-packer 332 operates to undo the encapsulation process performed by the packer 324. The output of the un-packer 332 is input to a post-interleaver 334 that operates to de-interleave the received information to produce FEC error encoded blocks.

[0076] The FEC error encoded blocks are input to a FEC decoder 336 that decodes the blocks and outputs the decoded information to CSV un-packer 338 and source decoder 340. The CSV un-packer 338 operates to unpack the CSV signal for a particular channel. The source decoder 340 operates to decode a particular channel. The tuning/channel switch logic 344 outputs control signals to the source decoder 340 and CSV un-packer 338 to control which channel of the received multiplex is decoded by the source decoder 340 and output as decoded channel 342.

[0077] In one or more aspects, the tuning/channel switch logic 344 is triggered by user input or by any other activity occurring at the device 304. When a channel switch is requested by a user, the tuning/channel switch logic 344 operates to tune the source decoder 340 to the selected channel and corresponding CSV information. The source decoder 340 operates to decode the CSV information to produce a low-resolution version of the selected new channel for display to the device user. In the meantime, the source decoder 340 begins decoding information from the transmission frame for the selected new channel. This process is facilitated by the strategic arrangement of the error coded blocks provided by the pre-interleaver 322. Thus, aspects of the service acquisition system operate to provide CSV signals and pre-interleaving to facilitate fast channel switching at a receiving device.

[0078] FIG. 4 shows a server 400 for use in aspects of a service acquisition system. For example, the server 400 is suitable for use as the server 302 shown in FIG. 3. The server 400 comprises processing logic 402 and modulator/transmitter logic 404, which are coupled to a data bus 406. The server 400 also comprises source encoders 408, CVS packers 410, pre-interleaver logic 412, FEC encoder 414, and packer 416, which are also coupled to the data bus 406.

[0079] In one or more aspects, the processing logic 402 comprises a CPU, processor, gate array, hardware logic, memory elements, virtual machine, software, and/or any combination of hardware and software. Thus, the processing logic 402 generally comprises logic to execute machine-readable instructions and to control one or more other functional elements of the server 400 via the internal data bus 406.

[0080] The modulator/transmitter logic 404 comprises hardware logic and/or software that operate to allow the server 400 to transmit a multimedia multiplex over a data network for reception by one or more receiving devices. In an aspect, the modulator/transmitter logic 404 comprises a communication channel 418. For example, in an aspect, the communication channel 418 comprises a broadcast channel configured to allow the server 400 to broadcast a multimedia multiplex.

[0081] The source encoders 408 comprise any number or type of source encoders that operate to receive corresponding input multimedia streams 420 to produce source encoded streams 422 and CSV information 424. For example, in an aspect, the CVS information 424 comprises low-resolution independently decodable versions of the input multimedia streams 420. A more detailed description of the CSV information 424 is provided in another section of this document.

[0082] The CVS packers 410 comprise any combination of hardware and software that operates to pack or encapsulate the CSV information 424. As a result, encapsulated CVS information 426 is produced. In an aspect, the CSV information 424 is packed for delivery as an in-band signal. In another aspect, the CSV information 424 is delivered as an out of band signal.

[0083] The FEC encoder 414 comprises any combination of hardware and software to perform error coding, such as RS coding on the source-encoded signals 422 and the encapsulated CVS information 426. For example, the FEC encoder 414 operates to produce error coded blocks.

[0084] The pre-interleaver 412 comprises a CPU, processor, gate array, hardware logic, memory elements, virtual machine, software, and/or any combination of hardware and software. The pre-interleaver 412 operates to pre-interleave the error coded blocks output from the FEC encoder 414 so the input source maintains its contiguous nature when packed into frames of a superframe. For example, in an aspect, the pre-interleaver 412 operates to form a pre-interleave table that is packed with error coded blocks as described above.

[0085] The packer (encapsulator) **416** comprises any combination of hardware and software. In an aspect, the packer **416** operates to encapsulate the error coded blocks from the pre-interleave table to generate a multiplex comprising a sequence of superframes where each superframe comprises four frames. In an aspect, the multiplex is sent to the modulator/transmitter for transmission over a network to one or more receiving devices.

[0086] During operation of one or more aspects, the server **400** operates to prepare a multiplex for transmission to receiving devices, wherein the multiplex comprises CSV information and pre-interleaved error coded blocks that are organized to allow for fast service acquisition. For example, when a channel change event occurs, the CSV information is quickly decoded by a receiving device to render a low-resolution version of the new channel. Furthermore, aspects of the system organize the error coded blocks of each frame of the superframe to enable the new channel to be quickly decoded.

[0087] In an aspect, the server **400** operates to construct a plurality of transmission frames where each transmission frame represents a selected time interval (i.e., one second). The server **400** also operates to encode one or more channels of data into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed at a receiving device using a single buffer having a selected time duration. Thus, the system operates to provide for fast service acquisition and channel switches between channels of a multimedia multiplex, while allowing receiving devices to absorb channel jitter using a single buffer.

[0088] In one or more aspects, the server **400** operates to perform one or more of the following functions in aspects of a service acquisition system.

1. Generate CSV information for each channel to be encoded into a multiplex.
2. Include the CSV information with video information to be error coded.
3. Perform pre-interleaving to rearrange error blocks within a superframe prior to encapsulation, which maintains the contiguous nature of the input source signal.
4. Perform encapsulation to produce a multiplex for transmission.

[0089] In an aspect, the service acquisition system is embodied in a computer program comprising program instructions stored on a computer-readable medium, which when executed by at least one processor, for instance, the processing logic **402**,

provides the functions described herein. For example, the program instructions may be loaded into the server **400** from a computer-readable medium, such as a floppy disk, CDROM, memory card, FLASH memory device, RAM, ROM, or any other type of memory device or computer-readable medium that interfaces to the server **400**. In another aspect, the instructions may be downloaded into the server **400** from an external device or network resource that interfaces to the server **400**. The program instructions, when executed by the processing logic **402**, provide one or more aspects of a service acquisition system as described herein.

[0090] Therefore, the server **400** operates in one or more aspects of a service acquisition system to enable fast service acquisition of services included in a multimedia multiplex. It should be noted that the server **400** illustrates just one implementation and that other implementations are possible within the scope of the aspects described herein.

Data channel MAC layer Capsule Structure

[0091] FIG. 5 shows an aspect of a Data Channel MAC layer capsule structure **500** prior to operation of aspects of a service acquisition system. The Data Channel MAC layer capsule **500** comprises the following in the order specified below.

1. Stream 0 packet - This comprises an always-present Stream 0 packet that includes the Data Channel MAC layer capsule header **502**.
2. Stream 1 packet - Integral number of MAC layer packets carrying stream 1 data.
3. Stream 2 packet - Integral number of MAC layer packets carrying stream 2 data
4. MAC packets containing Stuffing Packets to make the MAC layer capsule an integral multiple of "K", where "K" is the number of information blocks in an RS error control block. For example, for an outer code rate of 12/16, "K" is 12.
5. $(\text{NumR-SCodeBlocks}) \times (N-K)$ R-S parity packets.

[0092] Given the Data Channel MAC layer capsule structure **500** and the relative small size of the stream 0 packet, the MAC layer packets containing the stream 0 packet are certain to occupy only the first of the four frames in a superframe. This information

will always be lost in the case of a request for Early Entry/Fast Tuning where the receiver only receives the last three or less frames of a superframe. Since stream 0 carries information (example, working keys) needed to decode the data being carried on stream 1 or stream 2, it is desirable to ensure that a receiving device is able to receive the stream 0 packet if it received any part of the data carried in stream 1 or stream 2.

Re-ordered Data channel MAC layer Capsule Structure

[0093] FIG. 6 shows an aspect of a Data Channel MAC layer capsule structure **600** generated by aspects of a service acquisition system. In the MAC layer Capsule structure **600**, stream 0 is moved towards the end of the Data Channel MAC layer capsule, after Stream 1 or Stream 2 packets are placed in the capsule. This organization ensures that a receiving device is able to receive the stream 0 packet if it received any part of the data carried in stream 1 or stream 2.

[0094] In an aspect, the Data Channel MAC layer capsule structure **600** comprises the following in the order specified below.

1. MAC packets containing stuffing packets to make the MAC layer capsule an integral multiple of "K", where "K" is the number of information blocks in an RS error control block. For example, for an outer code rate of 12/16, "K" is 12. Note: RS PAD packets are few (at the most K-1) compared to the MAC packets containing Video data. Therefore, in most of the cases, frame 1 will have some MAC packets containing Video Data. (150 kbps to 500 kbps)
2. Stream 2 packet - Integral number of MAC layer packets carrying stream 2 data.
3. Stream 1 packet - Integral number of MAC layer packets carrying stream 1 data.
4. Stream 0 packet - This comprises an always-present Stream 0 packet that includes the Data Channel MAC layer capsule header **602**.
5. $(\text{NumR-SCodeBlocks}) \times (N-K)$ R-S parity packets.

Availability of OIS information

[0095] In an aspect, for Early Entry/Fast Tuning to occur, the basic information needed on the device is the location of the channel to which a switch is to be made. This location information is present in the OIS. However, the device reads the OIS only in certain cases (e.g. receiving new logical channels or errors on an existing logical channel, etc.) when it is triggered to do so. Thus, channel switching will be slowed if a device waits until the next superframe to receive the OIS after a channel change has been triggered. Note that the OIS provided with one superframe is applicable to the subsequent superframe. Thus, the device would need to wait for an entire superframe to retrieve the OIS information.

[0096] Therefore, in an aspect, the trigger for Early Entry/Fast Tuning is provided in advance of the superframe in which the actual switch is performed. This is accomplished by performing the following.

1. Putting the Device in a mode where it receives the OIS every superframe and uses the information when Early Entry/Fast Tuning is triggered. Entry to this mode can be triggered by certain user entered key combinations input to a user interface to trigger a channel switch.
2. Or the Device reads the OIS information every superframe (thus eliminating the need for an embedded OIS)

[0097] FIG. 7 shows a method 700 for providing aspects of a service acquisition system. For clarity, the method 700 will be described with reference to the server 400 shown in FIG. 4. In an aspect, at least one processor, such as the processing logic 402, executes machine-readable instructions to control the server 400 to perform the functions described below.

[0098] At block 702, application data is encoded. For example, the application data may comprise video streams as shown at 420. In an aspect, the source encoders 408 operate to encode the application data to produce encoded signals 422.

[0099] At block 704, CSV information is generated. For example, the source encoders 408 operate to generate the CSV information 424, which in an aspect comprises a low-resolution independently decodable version of the input video 420.

[00100] At block 706, the CSV information is encapsulated. For example, the packer 410 operates to encapsulate the generated CSV information 424 to produce encapsulated CSV information 426.

[00101] At block 708, the encoded source produced at block 702 and the encapsulated CSV produced at block 706 are FEC encoded. For example, in an aspect the FEC encoder 414 operates to encode this information into error coded blocks.

[00102] At block 710, the error coded blocks produced at block 708 are pre-interleaved. For example, the pre-interleaver 412 operates to pre-interleave the error coded blocks. For example, the pre-interleaver 412 operates as described above to generate a pre-interleave buffer to pre-interleave the error coded blocks to form interleaved error coded blocks.

[00103] At block 712, the interleaved error coded blocks are encapsulated to form a multiplex that comprises a sequence of superframes where each superframe comprises four frames.

[00104] At block 714, the multiplex is modulated and transmitted over a network to receiving devices. For example, the modulator/transmitter 404 operates to receive the multiplex and modulate and transmit the multiplex over data network using the channel 418.

[00105] Thus, the method 700 operates to produce a multimedia multiplex that is transmitted to receiving devices and which allows a device to perform fast service acquisition. It should be noted that the method 700 represents just one implementation and that other implementations are possible within the scope of the described aspects.

[00106] FIG. 8 shows a device 800 for use in aspects of a service acquisition system. For example, the device 800 is suitable for use as the device 304 shown in FIG. 3. The device 800 comprises processing logic 802 and demodulator/receiver logic 804, which are coupled to a data bus 806. The device 800 also comprises source decoder 816, CVS un-packer 814, early entry/fast tuning logic 818, post-interleaver logic 810, FEC decoder 812, and un-packer 808, which are also coupled to the data bus 806.

[00107] In one or more aspects, the processing logic 802 comprises a CPU, processor, gate array, hardware logic, memory elements, virtual machine, software, and/or any combination of hardware and software. Thus, the processing logic 802 generally comprises logic to execute machine-readable instructions and to control one or more other functional elements of the device 800 via the internal data bus 806.

[00108] The demodulator/receiver logic 804 comprises hardware logic and/or software that operate to allow the device 800 to receive a multimedia multiplex over a data network from a broadcast server. In an aspect, the demodulator/receiver logic 804

comprises a communication channel **828**. For example, in an aspect, the communication channel **828** comprises any suitable type of communication link (i.e., an OFDM link) to allow the device **800** to communicate with one or more data networks to receive a multimedia multiplex.

[00109] In an aspect, the demodulator/receiver logic **804** has a buffer **832** comprising any suitable memory or storage device. In an aspect, the demodulator/receiver logic **804** operates to receive a plurality of transmission frames wherein each transmission frame represents a selected time interval and comprises one or more channels of data. Selected data is encoded into predetermined transmission frames. The buffer **832** has a selected time duration (i.e., the duration of one transmission frame) and operates to buffer the received transmission frames so that channel jitter is absorbed.

[00110] The un-packer (de-encapsulator) **818** comprises any combination of hardware and software. In an aspect, the un-packer **818** operates to unpack interleaved error coded blocks from a received multiplex. For example, in an aspect, the un-packer **818** operates to undo the operation of the packer **416** shown in FIG. 4. As a result of the operation of the un-packer **818**, interleaved error coded blocks are produced.

[00111] The post-interleaver **810** comprises a CPU, processor, gate array, hardware logic, memory elements, virtual machine, software, and/or any combination of hardware and software. The post-interleaver **810** operates to de-interleave the interleaved error coded blocks produced by the un-packer **818**. For example, the post-interleaver **810** operates to undo the operation of the pre-interleaver **412** shown in FIG. 4. For example, in an aspect, the post-interleaver **810** operates to form error coded blocks as described above.

[00112] The FEC decoder **812** comprises any combination of hardware and software to perform error decoding on received error coded blocks produced by the post-interleaver **810**. For example, the FEC decoder **812** operates to produce encoded source **824** and encapsulated CSV **820** information that is included in a received multiplex.

[00113] The CSV un-packer **814** comprises any combination of hardware and software that operates to unpack or de-encapsulate the CSV information **820** received in a multiplex. The CSV un-packer **814** operates to produce CSV information that represents a low resolution and independently decodable version of video for a selected channel in the received multiplex. For example, the CSV un-packer **814** operates to undo the operation of the CSV packer **410** shown in FIG. 4.

[00114] The source decoder **816** comprises any type of source decoder that operates to receive coded source information **824** and produce corresponding decoded source streams **826**. The source decoder **816** also operates to receive CSV information and produce a CSV signal **830** that is a low-resolution independently decodable signal, which corresponds with a selected channel of a received multiplex. Thus, the source decoder **816** operates to render decoded source streams.

[00115] The early entry/ fast tuning logic **818** comprises a CPU, processor, gate array, hardware logic, memory elements, virtual machine, software, and/or any combination of hardware and software. The early entry / fast tuning logic **818** comprises detection logic that is configured to detect a channel switch request and control the device **800** to quickly switch to the requested channel. In an aspect, the channel switch request is generated by the processing logic **802** in response to a user input. In another aspect, the channel switch request is generated by the processing logic **802** base on any other criteria. For example, the processing logic **802** may generate a channel switch request based on receiving control information from a distribution network.

[00116] During operation of one or more aspects, when a channel switch request is triggered, the early entry / fast tuning logic **818** operates to control the decoding of a received multiplex so that the selected services may be decoded. In an aspect, the CSV for the selected channel is quickly decoded and rendered so that the device user receives a very fast, albeit low-resolution, displayed response to the channel change request.

[00117] Because of the arrangement of the error coded blocks provided in aspects of the service acquisition system, the information needed to decode the selected new channel is readily available. For example, in an aspect, OIS are arranged to occur at the end of a superframe so that these symbols can be retrieved even if an early entry / fast tuning trigger occurs during the middle of a superframe.

[00118] In one or more aspects, the device **800** operates to perform one or more of the following functions to provide fast acquisition and channel switching in aspects of a service acquisition system.

1. Receive an Early Entry / Fast Tuning trigger to tune to a new channel.
2. Perform post-interleaving to undo rearrangement of error coded blocks by a transmitter.

3. Un-package CSV information from the error coded blocks.
4. Decode CSV information to produce a CSV signal that can be rendered on the device.
5. Determine data for the requested new channel.
6. Decode the data to produce a video signal for the requested channel.
7. Render the video signal associated with the requested channel.

[00119] In an aspect, the service acquisition system is embodied in a computer program comprising program instructions stored on a computer-readable medium, which when executed by at least one processor, for instance, the processing logic **802**, provides the functions described herein. For example, the program instructions may be loaded into the device **800** from a computer-readable medium, such as a floppy disk, CDROM, memory card, FLASH memory device, RAM, ROM, or any other type of memory device or computer-readable medium that interfaces to the device **800**. In another aspect, the instructions may be downloaded into the device **800** from an external device or network resource that interfaces to the device **800**. The program instructions, when executed by the processing logic **802**, provide one or more aspects of a service acquisition system as described herein.

[00120] Therefore, the device **800** operates in one or more aspects of a service acquisition system to enable fast acquisition and channel switching between services provided in a multimedia multiplex. It should be noted that the device **800** illustrates just one implementation and that other implementations are possible within the scope of the described aspects.

[00121] **FIG. 9** shows a method **900** for use in aspects of a service acquisition system. For clarity, the method **900** will be described with reference to the device **800** shown in **FIG. 8**. In an aspect, at least one processor, such as the processing logic **802**, executes machine-readable instructions to control the device **800** to perform the functions described below.

[00122] At block **902**, a multimedia multiplex is received and buffered. For example, the multiplex is received by the demodulator/receiver **804**. The received multiplex is generated in accordance with aspects of the service acquisition system so that the multiplex comprises a super frame generated as described above to provide fast service acquisition and channel switching. For example, CVS information and pre-

interleaving as described above are used to produce the super frame. In an aspect, the received multiplex is buffered by the buffer **832** to absorb channel jitter.

[00123] At block **904**, a channel in the received multiplex is decoded. For example, the processing logic **802** operates to control the functional elements of the device **800** to decode a channel in the received multiplex.

[00124] At block **906**, a test is performed to determine if an Early Entry/Fast Tuning trigger has been received. For example, the trigger may be provided by a device user or may be automatically generated. In an aspect, the processing logic **802** operates to determine if the Early Entry/Fast Tuning trigger has been received. If the trigger has not been received, then the method returns to block **904** where the same channel is decoded. If a trigger is received, the method proceeds to block **908**.

[00125] At block **908**, a decoder is switched to decode the newly requested channel. For example, the processing logic **802** operates to control the device **800** to begin decoding the newly requested channel.

[00126] At block **910**, a CSV associated with the newly requested channel is rendered. For example, the CSV is a low-resolution of the newly requested channel that is encoded into the received multiplex. In an aspect, the CSV un-packer **814** operates to unpack the CSV so that it can be rendered while the decode attempts to switch to the newly requested channel.

[00127] At block **912**, the newly requested channel is decoded and rendered. For example, the processing logic **802** operates to control the device **800** to render the requested channel. Therefore, after the Early Entry/Fast Tuning trigger, the device user sees a low resolution version of the requested channel followed by the full resolution version when the decode process of the device is able to decode the channel. In an aspect, the device operates to render the new channel from the next available I-Frame.

[00128] Thus, the method **900** operates to allow a device to perform fast service acquisition and channel switching according to aspects of a service acquisition system. It should be noted that the method **900** represents just one implementation and that other implementations are possible within the scope of the described aspects.

Creation of the CSV

[00129] In one or more aspects, the source encoders **408** operate to generate the CSV according to the following. A human vision model is often included in a video codec to

mimic how a human perceives disparities between a source and its compressed version. Girod's model reflects the visual response to disparities directly on to the input video signal. This enables quantification of the minimum acceptable disparities in terms of the visual response to design optimal encoders and decoders. Spatial and temporal masking effects occur due to saturation in the fovea. Spatial masking refers to the effect of a video signal on the visibility of noise at a given pixel location. Temporal masking is similar to spatial masking except that the noise visibility at a pixel location is due to the video signal at the same spatial location but in a temporally adjacent frame.

[00130] In other words, temporal masking relates to the time response of the eye toward appearing and disappearing objects. Scene changes, introduction of new objects in the video and switching to new channel are examples of appearing objects and the response time for the human eye is around 44 milliseconds (ms). While the response time for disappearing objects as in cross fades is around 100 ms. The human eye integrates the incident signal along the time axis before it is interpreted by the brain. (30 frames per second provides sufficiently smooth motion rendition for most applications). Hence our eyes are more tolerant to (motion) interpolated images than to sudden instantaneous disruptions (such as camera flashes).

[00131] These observations are adopted in creation of the CSV. Predictive coding is heavily used in video compression due to high spatial and temporal correlation within and across frames. In generating the CSV, very high compression can be applied due to the poorer sensitivity of human eye to new content.

Independent side-channel

[00132] Sub-sampling in spatial (e.g. from QVGA - 320x240 pixels to Q²VGA - 160x120 pixels) and temporal domain (from 30fps to 5fps) are simple compression mechanisms that can be inverted (up-sampled to original resolution and frame rate up conversion using methods at the decoder as a post-processing operation). Such post-processing mechanisms are part of standard image processing libraries in most terminals including handhelds. Re-sampling in spatial domain involves low-pass filtering (to avoid aliasing) followed by decimation. Frame rate up conversion also involves spatial pixel interpolation as part of the motion compensation process after motion vector interpolation.

[00133] Since the CSV is required to be independently decodable, this places an I-frame at the beginning of the CSV since the primary data is not available to be used as reference. The typical average bit rate for transmitting QVGA video at 30fps is approximately 256kbps using the H.264 codec. While the average bit rate to transmit the same video at Q²VGA resolution and 5fps frame rate is approximately 10kbps. Hence the overhead for CSV is about 5%.

Core Information

[00134] Hybrid video coding entails spatial and temporal prediction followed by transformation, quantization and entropy coding. The essence of the compressed data lies in the intra-coded data used for reference, the mode of prediction and motion information. The primary bitstream carries all of this information to generate a full fidelity video. The CSV can be created as a scaled, compact form of this core information using one or more of the following methods:

1. Intra data: Combining a 2x2 window of macroblocks in the primary into one MB in the CSV - by simple merging of mode information. If 3 out of 4 or all 4 modes are the same, the mode of the corresponding macroblock in the CSV is set to this mode. Outlier macroblock(s) is (are) so marked. In H.264, intra MBs have 2 modes - Intra_16x16 or Intra_4x4. The latter indicates presence of directional information (and not a uniformly flat region) which is critical for mode based spatial error concealment.
2. Inter data: Motion vector processing. In an aspect, merging and scaling can be applied to combining MBs in the primary to form a MB in the CSV. For example, oversampling all inter modes to 4x4 and combining them appropriately paying attention to outliers is one technique that may be utilized.

[00135] Data partitioning of the coded video bitstream based on priority is a popular method of improving error resilience and enabling scalability in video coding. For example, in H.264, slice data partitioning is a feature that enables each slice to be coded as 3 separate data units called Network Abstraction Layer Units (NALU). Partition A carries critical information such as macroblock headers and motion vector information. Partition B contains intra coefficient data which is next in priority and partition C contains coefficient data for inter coded or temporally predicted blocks. In an aspect, the CSV could be generated by merging and scaling Partition A data to enable use of

CSV for error recovery and concealment and to avoid computationally intensive encode operations like Motion estimation.

Transmission of CSV

[00136] In single layer encoding schemes, CSV can be carried in the user data syntax of standard compliant bitstreams. For example, the CSV can be encoded as one of the Supplemental Enhancement Information syntax elements in H.264 or even as a redundant picture or slice. Alternatively, the CSV can be carried as an auxiliary packet in a side channel (e.g. supplemental channels in 1x networks) or in a low priority channel if layered coding and modulation is supported.

Applications

[00137] In an aspect, transport layer functionality is provided by Stream and MAC layers. Each service (a conglomeration of associated audio, video and/or data) is transmitted in one or more channels. A client at the device "tunes" to the channel(s) of interest and reads just the data pertaining to the service (or in general TV terms - channel) of interest out of the multiplex.

[00138] When a channel change is initiated by the user (e.g. a key press), the application notifies the physical layer which tunes to the corresponding channel(s). This can occur at Superframe (1 sec) boundaries or at frame boundaries (0.25 sec). In an aspect, the video codec carries a random access point (I or distributed I frames) at the beginning of every superframe. In order to enable faster channel switching (at frame boundaries), the CSV is carried in the 3rd (or possibly 4th) frame. Compressed data is written into the Physical layer packets in such a way as to enable this feature. Thus, a channel change initiated in the 1st half of a second (synchronized to frame time) can occur (indicated by video playback of the new channel) at the 3/4th sec mark of the superframe (ignoring decoder buffers). Given that B-frames are supported, a potential delay of 2 frames is possible (66ms). This is in addition to buffer latencies in display/render queue. Hence, a channel switch latency of $0.25+0.066+0.264 = 0.58$ sec is achievable.

Application of CSV to Error recovery and concealment

[00139] Since the CSV is a condensed version of the primary data that co-exists in time at the decoder (whether it is received in a side-channel, in the enhancement layer or in the primary bitstream as user-data), it can provide valuable insights into primary data that are affected by errors due to error-prone environments such as wireless channels.

[00140] CSV can be used to recover (identify the extent of loss) lost or corrupt parts of the primary through correlation of re-sync markers in the primary with the corresponding slice or MB. Frame numbers, picture-order-count in H.264, sequence numbers and/or presentation time stamps can be used to identify the corresponding frames in the CSV. When intra macroblocks in the primary are lost (either in intra or predicted frames), decoding just the appropriate parts of the CSV(slice and MB header) can provide mode and direction information for concealing new (scene change that is coded as I-frame) or appearing objects (occlusions coded as intra MBs or slices in P or B-frames).

[00141] When inter macroblock data is lost in the primary, be it just the motion vectors or inter coefficients, information in the corresponding frame of CSV can be extended for recovery and concealment. If method 1 for hybrid coding was used, the corresponding information in the CSV may or may not be coded in the same mode. For example, a MB coded as intra in primary may be coded as inter in the CSV (due to lower quality requirements). This is particularly helpful, since temporal concealment algorithms are unable to derive the correct motion vector when some MBs in the window of interest are intra coded.

[00142] In a closed system, the encoder and decoder are designed and implemented with complete autonomy. Hence, the encoder is fully aware of the capabilities of the decoder implementation and the decoder is aware of the optimizations and construction of the encoder bit streams. This enables a better, more robust and cost (Rate-Distortion cost) efficient method to generate CSV and other applications of the disclosure including error recovery, error concealment and frame rate up conversion.

[00143] In one or more aspects, the system operates to transmit a CSV of the media. When indicated by the higher level application of the need, as in when the user initiates channel switching through key presses, the decoder starts decoding and displaying the CSV of the new channel almost instantaneously (e.g. after the lower layers - transport and physical layers - have switched to the new channel). This may reduce the switch time to a few milliseconds in the range of 10 - 100 milliseconds. In non-channel switch

scenarios, CSV information, for example, I-frames/MBs and MV information may be used for error recovery (with/without some processing) where in neighboring frames' information is used to conceal lost MBs or MVs.

Implementation Illustration

[00144] The following is an implementation illustrating use of an integrated codec and physical layer for efficient multimedia streaming in accordance with aspects of the service acquisition system described herein. For example, this implementation describes aspects of a highly integrated architecture provided by multimedia codecs in the application layer to provide channelization in the physical layer for efficient multimedia communications, particularly for multimedia streaming or multimedia broadcast/multicast. It should be noted that various aspects apply to any individual or combination of existing or future application, transport and physical layer or other technologies. As a result, the various aspects provide effective communications, interactions and understanding between OSI layers with tight coupling that eliminates/reduces jitter and processing buffers at various points thus reducing latencies. Thus, one of the primary advantages is fast channel switching in multimedia mobile broadcasts.

[00145] FIG. 10 shows an aspect of a communication system 1000. The system 1000 is a multimedia streaming system where the input to the system is a continuous (discrete time events but never ending) stream of data and the output from the system is a continuous stream of data. For example, the client shown in FIG. 10 may be part of a mobile device.

[00146] FIG. 11 shows a diagram of OSI layers or a protocol stack 1100 for use in aspects of a service acquisition system. For example, the protocol stack 1100 is suitable for use in the service acquisition system shown in FIG. 3. It should be noted that the protocol stack 1100 is just one implementation and that various aspects of the described system can be extended to any layered architecture. The following describes the data flow through the layers of the protocol stack 1100; however, it should be noted that the description is based on a common understanding of data structures within and between the layers.

[00147] The service acquisition system operates to maintain continuous or streaming communication of data from input to output through the network to the client. In an aspect, the system behaves as a single FIFO buffer.

[00148] Let's assume a basic unit of time (T) is equal to, e.g., one second. Let the data pertaining to this unit of time be called a Super Frame (SF). Then a SF is a packet whose payload contains data, for e.g. multimedia data that can be consumed in T seconds (T = 1 in this example).

[00149] FIG. 12 shows a diagram 1200 that illustrates Super Frame data processing from application through physical layer at both network and client sides of a communication system. In the diagram 1200, the x-axis represents time in seconds. The y-axis depicts data processing from the input through the encoder, stream/MAC layer encapsulation processing to packaging into a physical layer SF. The diagram 1200 shows the fundamental operations of SF processing across the system where the current SF is indicated by hashed blocks. The input for this SF enters the system at time (T = SF_Start -3), and it is encoded, outer coded, and encapsulated into SF at time (T = SF_Start -2). This SF is then transmitted and received (the transmitter to receiver delay is on the order of nanoseconds since the physical layer is RF) at time (T = SF_Start -1). Hence, the current SF's data fills the receiver buffer (i.e., buffer 832) at time (T = SF_Start -1) and is available for decoding and immediate presentation at time (T = SF_Start). The time stamps engrained in the audio or video data units for the current SF are offsets from time (T = SF_Start).

[00150] In a synchronous system, T = SF_Start (T in general) is tied to a system clock, for e.g. GPS when available both at the network and client, and provides the SF timing. This eliminates the need for a separate media clock (VCXO or oscillator) in the client, thus reducing cost particularly if the client is hosted on a handheld device.

[00151] In an asynchronous system, the SF processing still remains the same. The time stamps on the audio/video data units are derived from an internal clock, such as a PCR (Program Clock Reference in MPEG-2 Systems).

[00152] In either case, the receiver buffer (i.e., buffer 832) is now tightly coupled to any decoder buffer (they can potentially share the same buffer) such that there is minimal de-jitter required at the decoder (less than 100 ms). This enables fast channel switching as described below.

[00153] FIG. 13 shows a diagram 1300 that illustrates fast channel switching provided by aspects of a service acquisition system. For example, the system can provide fast channel switching in under two seconds maximum and one second on average when $T = 1$ second. The diagram 1300 represents four channels and three superframes: SF + 2, SF + 1 and SF. When a device user initiates a channel change from channel A to B in the time frame corresponding to SF, reception and(/or) decoding switches to receiving channel B at time SF+1 and the data in SF+1 is available for decoding at the beginning of SF+2.

[00154] Since video and audio compression involves temporal prediction, independent decoding is restricted to random access points such as I-frames. However, since the encoder is aware of the SF boundaries, it can suitably place random access points (I-frames, progressive refreshes, or other means) at the beginning of the SF corresponding to when physical layer acquisition is possible. This eliminates the need for random access points arbitrarily, thus improving compression efficiency. It also ensures fast channel switching since a random access point is available at the beginning of every SF for immediate decoding and display of the new channel. It should also be noted that the time duration of the SF could be set to 1 second, 5 seconds, or any unit of time as desired or as system design permits.

Application Layer Processing

[00155] Consider video data traversing through the system on a SF basis. The video encoder extracts video data or information that can be consumed in T seconds. (Consumption can be considered to be, for example, video data that is displayed for T seconds). Let this be called a T-packet or the Super Frame (SF).

Sync Layer Processing: Synchronization Information

[00156] Timing information for e.g. time stamps, for video data units (e.g. compressed access units or video frames) that comprise the current T-packet or SF is assigned to the data units. These time stamps are sampled from the incoming clock (e.g. PCR) which is carried along with the audio and video data as in an asynchronous system for e.g. DVB, ATSC using MPEG-2 Systems protocols.

[00157] In a synchronous system where the media clock is synchronous to system clock, the time stamps are a fixed offset from the incoming media clock (PCR/PTS),

which is equal to the fixed difference between the incoming media clock (PCR) and the system clock. Therefore, aspects of a synchronous system provide the following.

- a. Eliminate the need for sending a clock thus eliminating any timing de-jitter buffer.
- b. Client can synchronize off a local clock that is synchronized to common time such as GPS.

Transport Layer processing

[00158] Transport layer provides framing for sync encapsulated audio/video into fixed length packets, which correspond to the physical layer packet sizes. These are fixed length packets.

[00159] FIG. 14 shows a diagram 1400 that illustrates the flow of T-packets in aspects of a service acquisition system. For example, the diagram 1400 shows how T-packets are encoded and encapsulated in a current (or 1st) superframe, transmitted at the transmitter in the network, and received at the receiver in the client in a second superframe. As a result, the T-packets become available and processed/consumed in a third superframe. Each T-packet is independently receivable and decodable. Therefore there is minimal de-jitter at T_{SF_start} . It should be noted that the application layer is aware of the Stream/MAC layer and physical layer packetization/encapsulation/channelization logic and structure (i.e., see FIG 11).

[00160] FIG. 15 shows a diagram 1500 that illustrates a video frame arrangement in an aspect of a T-packet. For the frame arrangement shown, base and enhancement layers are provided and the source order is equal to the presentation order. Thus, a T-packet is received in source order and encoded. The order of frames after compression is retained, and the T-packet is output from the encoder in decode order.

[00161] After the T-packet traverses through the transport and physical layers in network and client, it reaches the video decoder in the client in the decode order. Hence, the decoder is able to decode one T-packet after another in continuous succession thereby eliminating/reducing the need for additional decoder buffers.

[00162] FIG. 16 shows a diagram that illustrates a video frame arrangement 1600 in an aspect of a T-packet. For example, the video frame arrangement 1600 is suitable for arranging video frames in aspects of a service acquisition system.

[00163] The following “Notes” provide information about various aspects of the video frame arrangement **1600**.

Note 1 OIS contains among other information, an index to the location of the T-packet for the program/channel of interest. The client “wakes up” only for this duration, thus saving power. For fast channel acquisition, this message and the OIS can be carried at the end of the T-packet.

Note 2 Audio and Video corresponding to the same T-packet may be encapsulated in different stream layer packets.

Note 3 Alternatively audio and video corresponding to the same T-packet may be encapsulated in the same or different MAC protocol capsule.

Note 4 Reed Solomon (RS) is an example of Outer code or forward error correction (FEC). The parity blocks of RS are carried at the end of a T-packet. When the receiver receives the first 3 frames of the T-packet without errors, the last frame of the T-packet does not have to be received. The low power handset host for the client does not have to “wake up” to receive this, thus saving power.

Note 5 A T-packet is encoded, encapsulated, and “filled” into physical layer packets. Encoder, MAC and Physical layers are aware of T-packet boundaries.

Note 6 A T-packet encoded/encapsulated in a previous second is transmitted and received during this period.

Note 7 A T-packet received in a previous second is consumed in this period.

[00164] Therefore, the video frame arrangement **1600** is suitable for use in aspects of a service acquisition system. It should be noted that the arrangement **1600** is just one implementation and that other implementations are possible with the scope of the described aspects.

[00165] **FIG. 17** shows a diagram that illustrates an arrangement **1700** of transport headers (TH) and sync headers (SH) used in base and enhancement layers of a Super Frame. In an aspect, the TH **1702** comprises a last indicator **1704** and a physical layer packet (PLP) offset **1706**.

[00166] In an aspect, the SH **1708** comprises a stream identifier **1710**, presentation time stamp (PTS) **1712**, frame identifier **1714**, random access point (RAP) flag **1716**,

frame rate identifier **1718**, and reserved bits **1720**. The frame identifier **1714** comprises and enhancement flag **1722** and a frame number **1724**.

[00167] In an aspect, the PTS **1712** is equal to the incoming time stamp (ts) for asynchronous systems, and for synchronous systems, the PTS **1712** is equal to $ts + (PCR - \text{system clock})$.

[00168] Therefore, the video frame arrangement **1700** is suitable for use in aspects of a service acquisition system. It should be noted that the arrangement **1700** is just one implementation and that other implementations are possible with the scope of the described aspects.

[00169] FIG. 18 shows a forward error correction (FEC) code block structure **1800** for use in aspects of a service acquisition system. For example, the code block structure **1800** comprises a base layer **1802** code block structure and an enhancement layer **1804** code block structure. The code block structure **1800** comprises Transport Headers, Sync Headers, and tail bits with CRC. The code block structure **1800** illustrates how audio and video frame data is organized for base and enhancement layers for use in aspects of a service acquisition system. In an aspect, Video data can be optionally encoded as a single (layer) bitstream or in multiple layers for scalability. In addition, random access points aligned with T-packet boundaries can be carried in either or all layers. It should be noted that the code block structure **1800** represents just one implementation and that other implementations are possible within the scope of the described aspects.

[00170] FIG. 19 shows a frame organization **1900** for providing video and audio bitstreams for use in a service acquisition system. The frame organization **1900** illustrates frame organization for base and enhancement layer video and a frame organization for audio. In an aspect, a base layer video frame comprises “N” video slices. Additionally, random access points **1902** are provided through independent decoding that is aligned to T-packet boundaries for both video and audio. Furthermore, additional random access points may be provided for error resilience and recovery or for faster channel switching.

[00171] It should be noted that the frame organization **1900** represents just one implementation and that other implementations are possible within the scope of the described aspects.

[00172] Thus, the various aspects of a service acquisition system operate as described herein to provide the following functions.

- a. Provide effective communications, interactions, and understanding between OSI layers with tight coupling that eliminates/reduces jitter and processing buffers at various points thereby reducing latencies. One of the primary advantages is fast channel switching in multimedia mobile broadcasts for example through data packets corresponding to one second of data that flows through the system.
- b. Synchronous and asynchronous timing throughout the system with or without a clock (PCR) carried throughout the system.
- c. Fast Channel Switching/Acquisition through reduced buffering.
- d. Audio is continuous (boxcar) but may not be synchronous.
- e. One buffer throughout the system. Minimal de-jitter buffer at the receiver (i.e., on the order of milliseconds) for when the next second of data starts to fill up and before the current second of data is completely consumed.
- f. Audio aspects. Since compressed audio frames may or may not divide evenly at one second boundaries, left over information for a current audio frame is sent with the current second (i.e. ahead of time by a few milliseconds). Due to a continuous stream input, the buffer at the receiver appears continuous.

[00173] FIG. 20 shows a server 2000 for use in aspects of a service acquisition system. The server 2000 comprises a module (2002) for generating one or more CSV signals, which in an aspect comprises the source encoder 408. The server 2000 also comprises a module (2004) for encoding a CSV signal and multimedia signals to produce error coded blocks, which in an aspect comprises the FEC encoder 414. The server 2000 also comprises a module (2006) for encapsulating error coded blocks, which in an aspect comprises the packer 416. It should be noted that the server 2000 is just one implementation and that other implementations are possible.

[00174] FIG. 21 shows a device 2100 for use in aspects of a service acquisition system. The device 2100 comprises a module (2102) for receiving a multiplex, which in an aspect comprises the receiver 804. The device 2100 also comprises a module (2104) for buffering a multiplex, which in an aspect comprises the buffer 832. The device 2100 also comprises a module (2106) for detecting a channel selection, which in an aspect comprises the tuning logic 818. The device 2100 also comprises a module (2108) for decoding a CSV signal, which in an aspect comprises the un-packer 814.

The device **2100** also comprises a module (**2110**) for rendering a CSV signal, which in an aspect comprises the source decoder **816**. It should be noted that the device **2100** is just one implementation and that other implementations are possible.

[00175] **FIG. 22** shows a server **2200** for use in aspects of a service acquisition system. The server **2200** comprises a module (**2202**) for constructing a plurality of transmission frames, which in an aspect comprises the packer **416**. The server **2200** also comprises a module (**2204**) for encoding one or more channels of data into a transmission frame, which in an aspect comprises the FEC encoder **414**. It should be noted that the server **2200** is just one implementation and that other implementations are possible.

[00176] **FIG. 23** shows a device **2300** for use in aspects of a service acquisition system. The device **2300** comprises a module (**2302**) for receiving a plurality of transmission frames, which in an aspect comprises the receiver **804**. The device **2300** also comprises a module (**2304**) for buffering the plurality of transmission frames with a single buffer, which in an aspect comprises the buffer **832**. It should be noted that the device **2300** is just one implementation and that other implementations are possible.

[00177] The various illustrative logics, logical blocks, modules, and circuits described in connection with the aspects disclosed herein may be implemented or performed with a general purpose processor, a digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA) or other programmable logic device, discrete gate or transistor logic, discrete hardware components, or any combination thereof designed to perform the functions described herein. A general-purpose processor may be a microprocessor, but, in the alternative, the processor may be any conventional processor, controller, microcontroller, or state machine. A processor may also be implemented as a combination of computing devices, e.g., a combination of a DSP and a microprocessor, a plurality of microprocessors, one or more microprocessors in conjunction with a DSP core, or any other such configuration.

[00178] The steps of a method or algorithm described in connection with the aspects disclosed herein may be embodied directly in hardware, in a software module executed by a processor, or in a combination of the two. A software module may reside in RAM memory, flash memory, ROM memory, EPROM memory, EEPROM memory, registers, a hard disk, a removable disk, a CD-ROM, or any other form of storage

medium known in the art. An exemplary storage medium is coupled to the processor, such that the processor can read information from, and write information to, the storage medium. In the alternative, the storage medium may be integral to the processor. The processor and the storage medium may reside in an ASIC. The ASIC may reside in a user terminal. In the alternative, the processor and the storage medium may reside as discrete components in a user terminal.

[00179] The description of the disclosed aspects is provided to enable any person skilled in the art to make or use the invention. Various modifications to these aspects may be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other aspects, e.g., in an instant messaging service or any general wireless data communication applications, without departing from the spirit or scope of the invention. Thus, the invention is not intended to be limited to the aspects shown herein but is to be accorded the widest scope consistent with the principles and novel features disclosed herein. The word “exemplary” is used exclusively herein to mean “serving as an example, instance, or illustration.” Any aspect described herein as “exemplary” is not necessarily to be construed as preferred or advantageous over other aspects.

[00180] Accordingly, while one or more aspects of a service acquisition system have been illustrated and described herein, it will be appreciated that various changes can be made to the aspects without departing from their spirit or essential characteristics. Therefore, the disclosures and descriptions herein are intended to be illustrative, but not limiting, of the scope of the invention, which is set forth in the following claims.

CLAIMS

WHAT IS CLAIMED IS:

1. A method for service acquisition, the method comprising:
generating one or more channel switch video (CSV) signals associated with one or more multimedia signals;
encoding the CSV and multimedia signals to produce error coded blocks; and
encapsulating the error coded blocks into a multiplex signal.
2. The method of claim 1, wherein the error coded blocks comprise error coded blocks associated with overhead information and error coded blocks comprising data associated with the one or more multimedia signals, said method further comprising pre-interleaving the error coded blocks so that the error coded blocks associated with overhead information are positioned after the error coded blocks comprising data associated with the multimedia signals.
3. The method of claim 1, wherein said generating comprises generating the one or more CSV signals that comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.
4. The method of claim 1, wherein said encoding the CSV signals and the multimedia signals comprises encoding the CSV signals and the multimedia signals using forward error correction to produce the error coded data blocks.
5. The method of claim 1, further comprising transmitting the multiplex signal over a data network.
6. An apparatus for service acquisition, the apparatus comprising:
a source encoder configured to generate one or more channel switch video (CSV) signals associated with one or more multimedia signals;
an error encoder configured to encode the CSV and multimedia signals to produce error coded blocks; and

a packer configured to encapsulate the error coded blocks into a multiplex signal.

7. The apparatus of claim 6, wherein the error coded blocks comprise error coded blocks associated with overhead information and error coded blocks comprising data associated with the one or more multimedia signals, said apparatus further comprising a pre-interleaver configured to pre-interleave the error coded blocks so that the error coded blocks associated with overhead information are positioned after the error coded blocks comprising data associated with the multimedia signals.

8. The apparatus of claim 6, wherein said source encoder is configured to generate the one or more CSV signals that comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

9. The apparatus of claim 6, wherein said error encoder is configured to provide forward error correction.

10. The apparatus of claim 6, further comprising a transmitter configured to transmit the multiplex signal over a data network.

11. An apparatus for service acquisition, the apparatus comprising:
means for generating one or more channel switch video (CSV) signals associated with one or more multimedia signals;
means for encoding the CSV and multimedia signals to produce error coded blocks; and
means for encapsulating the error coded blocks into a multiplex signal.

12. The apparatus of claim 11, wherein the error coded blocks comprise error coded blocks associated with overhead information and error coded blocks comprising data associated with the one or more multimedia signals, said apparatus further comprising means for pre-interleaving the error coded blocks so that the error coded

blocks associated with overhead information are positioned after the error coded blocks comprising data associated with the multimedia signals.

13. The apparatus of claim 11, wherein said means for generating comprises means for generating the one or more CSV signals that comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

14. The apparatus of claim 11, wherein said means for encoding the CSV signals and the multimedia signals comprises means for encoding the CSV signals and the multimedia signals using forward error correction to produce the error coded data blocks.

15. The apparatus of claim 11, further comprising means for transmitting the multiplex signal over a data network.

16. A machine-readable medium comprising instructions for service acquisition, wherein the instructions upon execution cause a machine to:

generate one or more channel switch video (CSV) signals associated with one or more multimedia signals;

encode the CSV and multimedia signals to produce error coded blocks; and

encapsulate the error coded blocks into a multiplex signal.

17. The machine-readable medium of claim 16, wherein the error coded blocks comprise error coded blocks associated with overhead information and error coded blocks comprising data associated with the one or more multimedia signals, and wherein said instructions cause the machine to pre-interleave the error coded blocks so that the error coded blocks associated with overhead information are positioned after the error coded blocks comprising data associated with the multimedia signals.

18. The machine-readable medium of claim 16, wherein the instructions for generating cause the machine to generate the one or more CSV signals that comprise at

least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

19. The machine-readable medium of claim 16, wherein said instructions for encoding the CSV signals and the multimedia signals further cause the machine to encode the CSV signals and the multimedia signals using forward error correction to produce the error coded data blocks.

20. The machine-readable medium of claim 16, wherein the instructions further cause the machine to transmit the multiplex signal over a data network.

21. At least one processor for service acquisition, the at least one processor being configured to:

generate one or more channel switch video (CSV) signals associated with one or more multimedia signals;

encode the CSV and multimedia signals to produce error coded blocks; and

encapsulate the error coded blocks into a multiplex signal.

22. The at least one processor of claim 21, wherein the error coded blocks comprise error coded blocks associated with overhead information and error coded blocks comprising data associated with the one or more multimedia signals, and wherein the at least one processor being further configured to pre-interleave the error coded blocks so that the error coded blocks associated with overhead information are positioned after the error coded blocks comprising data associated with the multimedia signals.

23. The at least one processor of claim 21, wherein being configured to generate comprises being configured to generate the one or more CSV signals that comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

24. The at least one processor of claim 21, wherein being configured to encode the CSV signals and the multimedia signals comprises being configured to

encode the CSV signals and the multimedia signals using forward error correction to produce the error coded data blocks.

25. The at least one processor of claim 21, being further configured to transmit the multiplex signal over a data network.

26. A method for service acquisition, the method comprising:
receiving a multiplex signal associated with a plurality of channels;
detecting a selection of one of the channels;
decoding a channel switch video (CSV) signal associated with the selected channel; and
rendering the CSV signal.

27. The method of claim 26, wherein said receiving comprises receiving the multiplex signal over a data network.

28. The method of claim 26, wherein said detection is based on user input.

29. The method of claim 26, further comprising decoding the multiplex signal to produce error coded data blocks and error coded information blocks.

30. The method of claim 29, further comprising de-interleaving the error coded data blocks and the error coded information blocks, wherein the error coded information blocks are positioned after error coded data blocks.

31. The method of claim 26, further comprising decoding the selected channel.

32. An apparatus for service acquisition, the apparatus comprising:
a receiver configured to receive a multiplex signal associated with a plurality of channels;
selection logic configured to detect a selection of one of the channels;

an un-packer configured to decode a channel switch video (CSV) signal associated with the selected channel; and

source decoder configured to render the CSV signal.

33. The apparatus of claim 32, wherein said receiver is configured to receive the multiplex signal over a data network.

34. The apparatus of claim 32, wherein said selection logic is configured to detect the selection of the channel based on user input.

35. The apparatus of claim 32, wherein said un-packer is configured to decode the multiplex signal to produce error coded data blocks and error coded information blocks.

36. The apparatus of claim 35, further comprising a de-interleaver configured to de-interleave the error coded data and information blocks, wherein the error coded information blocks are positioned after the error coded data blocks.

37. The apparatus of claim 32, wherein the source decoder is configured to decode the selected channel.

38. An apparatus for service acquisition, the apparatus comprising:
means for receiving a multiplex signal associated with a plurality of channels;
means for detecting a selection of one of the channels;
means for decoding a channel switch video (CSV) signal associated with the selected channel; and
means for rendering the CSV signal.

39. The apparatus of claim 38, wherein said means for receiving comprises means for receiving the multiplex signal over a data network.

40. The apparatus of claim 38, wherein said means for detecting comprises means for detecting the selected channel based on user input.

41. The apparatus of claim 38, further comprising means for decoding the multiplex to produce error coded data blocks and error coded information blocks.

42. The apparatus of claim 41, further comprising means for de-interleaving the error coded data blocks and the error coded information blocks, wherein the error coded information blocks are positioned after error coded data blocks.

43. The apparatus of claim 38, further comprising means for decoding the selected channel.

44. A machine-readable medium, comprising instructions for service acquisition, wherein the instructions upon execution cause a machine to:

- receive a multiplex signal associated with a plurality of channels;
- detect a selection of one of the channels;
- decode a channel switch video (CSV) signal associated with the selected channel; and
- render the CSV signal.

45. The machine-readable medium of claim 44, wherein the instructions for receiving further cause the machine to receive the multiplex signal over a data network.

46. The machine-readable medium of claim 44, wherein the instructions for detecting further cause the machine to detect the selected channel based on user input.

47. The machine-readable medium of claim 44, wherein the instructions further cause the machine to decode the multiplex signal to produce error coded data blocks and error coded information blocks.

48. The machine-readable medium of claim 47, wherein the instructions further cause the machine to de-interleave the error coded data blocks and the error coded information blocks, wherein the error coded information blocks are positioned after error coded data blocks.

49. The machine-readable medium of claim 44, wherein the instructions further cause the machine to decode the selected channel.

50. At least one processor for service acquisition, the at least one processor being configured to:

receive a multiplex signal associated with a plurality of channels;

detect a selection of one of the channels;

decode a channel switch video (CSV) signal associated with the selected channel; and

render the CSV signal.

51. The at least one processor of claim 50, wherein being configured to receive comprises being configured to receive the multiplex signal over a data network.

52. The at least one processor of claim 50, wherein being configured to detect comprises being configured to detect the selected channel based on user input.

53. The at least one processor of claim 50, being further configured to decode the multiplex signal to produce error coded data blocks and error coded information blocks.

54. The at least one processor of claim 53, being further configured to de-interleave the error coded data blocks and the error coded information blocks, wherein the error coded information blocks are positioned after error coded data blocks.

55. The at least one processor of claim 50, being further configured to decode the selected channel.

56. A method for service acquisition, the method comprising:

constructing a plurality of transmission frames wherein each transmission frame is associated with a selected time interval; and

encoding data associated with one or more channels into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer associated with a selected time duration.

57. The method of claim 56, wherein encoding comprises encoding by using forward error correction encoding.

58. The method of claim 56, wherein the one or more channels of data comprise one or more channel switch video (CSV) signals associated with one or more multimedia signals.

59. The method of claim 58, wherein encoding comprises generating the one or more CSV signals to comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

60. The method of claim 56, wherein encoding comprises pre-interleaving error coded data blocks and error coded information blocks associated with the one or more channels of data so that the error coded information blocks are positioned after the error coded data blocks.

61. An apparatus for service acquisition, the apparatus comprising:
means for constructing a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval; and
means for encoding data associated with one or more channels into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer associated with a selected time duration.

62. The apparatus of claim 61, wherein means for encoding comprises means for encoding using forward error correction encoding.

63. The apparatus of claim 61, wherein the one or more channels of data comprise one or more channel switch video (CSV) signals associated with one or more multimedia signals.

64. The apparatus of claim 63, wherein means for encoding comprises means for generating the one or more CSV signals to comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

65. The apparatus of claim 61, wherein means for encoding comprises means for pre-interleaving error coded data blocks and error coded information blocks associated with the one or more channels of data so that the error coded information blocks are positioned after the error coded data blocks.

66. An apparatus for service acquisition, the apparatus comprising:
a packer configured to construct a plurality of transmission frames wherein each transmission frame is associated with a selected time interval; and
an encoder configured to encode data associated with one or more channels into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer associated with a selected time duration.

67. A machine-readable medium, comprising instructions for service acquisition, wherein the instructions upon execution cause a machine to:

construct a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval; and

encode data associated with one or more channels into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer associated with a selected time duration.

68. At least one processor for service acquisition, the at least one processor being configured to:

construct a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval; and

encode data associated with one or more channels into the plurality of transmission frames, wherein selected data is encoded into predetermined transmission frames so that channel jitter can be absorbed using a single buffer associated with a selected time duration.

69. A method for service acquisition, the method comprising:

receiving a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval and comprises data associated with one or more channels, and wherein selected data is encoded into predetermined transmission frames; and

buffering the plurality of transmission frames with a single buffer associated with a selected time duration, wherein channel jitter associated with the one or more channels is absorbed.

70. The method of claim 69, comprising decoding the one or more channels of data using forward error correction decoding.

71. The method of claim 69, wherein the one or more channels of data comprise one or more channel switch video (CSV) signals associated with one or more multimedia signals.

72. The method of claim 71, wherein decoding comprises decoding the one or more CSV signals to comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

73. The method of claim 71, wherein decoding comprises de-interleaving error coded data blocks and error coded information blocks associated with the one or more channels of data so that the error coded information blocks are positioned after the error coded data blocks.

74. An apparatus for service acquisition, the apparatus comprising:

means for receiving a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval and comprises data associated with one or more channels, and wherein selected data is encoded into predetermined transmission frames; and

means for buffering the plurality of transmission frames with a single buffer associated with a selected time duration, wherein channel jitter associated with the one or more channels is absorbed.

75. The apparatus of claim 74, comprising means for decoding the one or more channels of data using forward error correction decoding.

76. The apparatus of claim 74, wherein the one or more channels of data comprise one or more channel switch video (CSV) signals associated with one or more multimedia signals.

77. The apparatus of claim 76, wherein means for decoding comprises means for decoding the one or more CSV signals to comprise at least one of a complete low-resolution version and a partial low-resolution version associated with the one or more multimedia signals.

78. The apparatus of claim 76, wherein means for decoding comprises means for de-interleaving error coded data blocks and error coded information blocks associated with the one or more channels of data so that the error coded information blocks are positioned after the error coded data blocks.

79. An apparatus for service acquisition, the apparatus comprising:

a receiver configured to receive a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval and comprises data associated with one or more channels, and wherein selected data is encoded into predetermined transmission frames; and

a demodulator configured to buffer the plurality of transmission frames with a single buffer associated with a selected time duration, wherein channel jitter associated with the one or more channels is absorbed.

80. A machine-readable medium, comprising instructions for service acquisition, wherein the instructions upon execution cause a machine to:

receive a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval and comprises data associated with one or more channels, and wherein selected data is encoded into predetermined transmission frames; and

buffer the plurality of transmission frames with a single buffer associated with a selected time duration, wherein channel jitter associated with the one or more channels is absorbed

81. At least one processor for service acquisition, the at least one processor being configured to:

receive a plurality of transmission frames, wherein each transmission frame is associated with a selected time interval and comprises data associated with one or more channels, and wherein selected data is encoded into predetermined transmission frames; and

buffer the plurality of transmission frames with a single buffer associated with a selected time duration, wherein channel jitter associated with the one or more channels is absorbed.