

DATA ERROR CORRECTION CIRCUIT AND DATA TRANSMISSION CIRCUIT

ABSTRACT

A data error correction circuit and a data transmission circuit. The data error correction circuit comprises: a decoding module (21), an input end of which is connected to a data bus, wherein the decoding module is used for receiving first data and a check code for the first data, and for outputting an error correction code for the first data on the basis of the check code; and an error correction latch module (22), a first input end of which is connected to the data bus and a second input end of which is connected to an output end of the decoding module, wherein the error correction latch module is used for latching the first data corresponding to the error correction code, generating second data according to the error correction code and the corresponding first data, and then outputting the second data. By means of a data error correction circuit, check and error correction can be carried out on odd data and even data which are alternately transmitted.

[Fig. 2]