

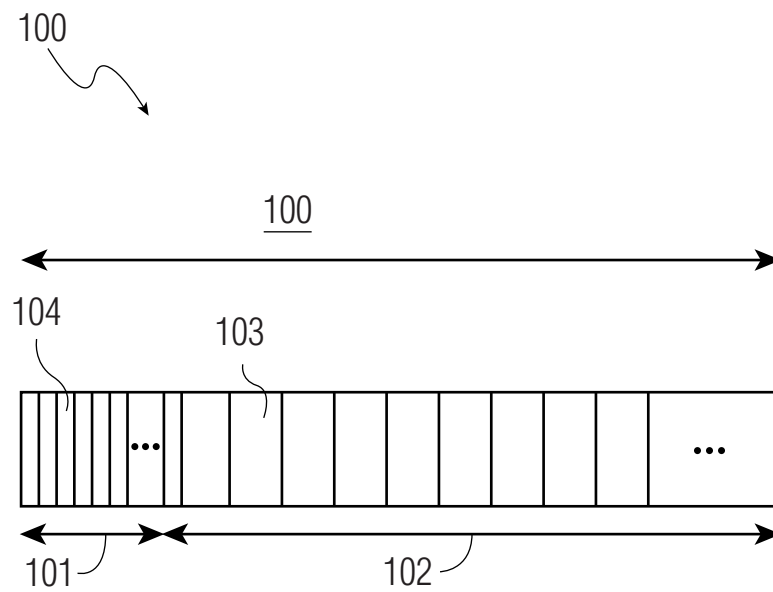


FIG. 1

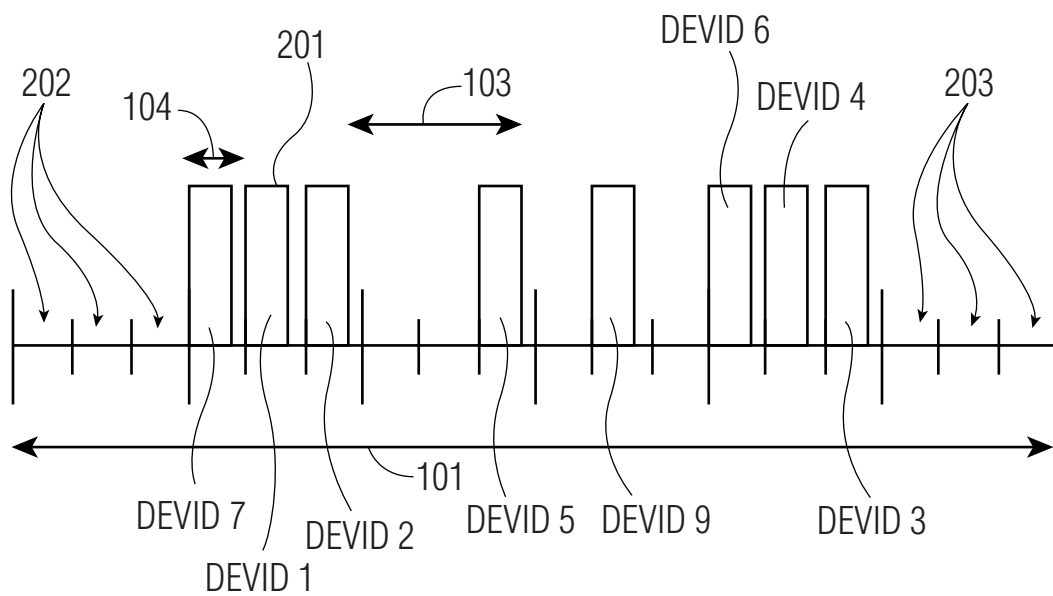


FIG. 2

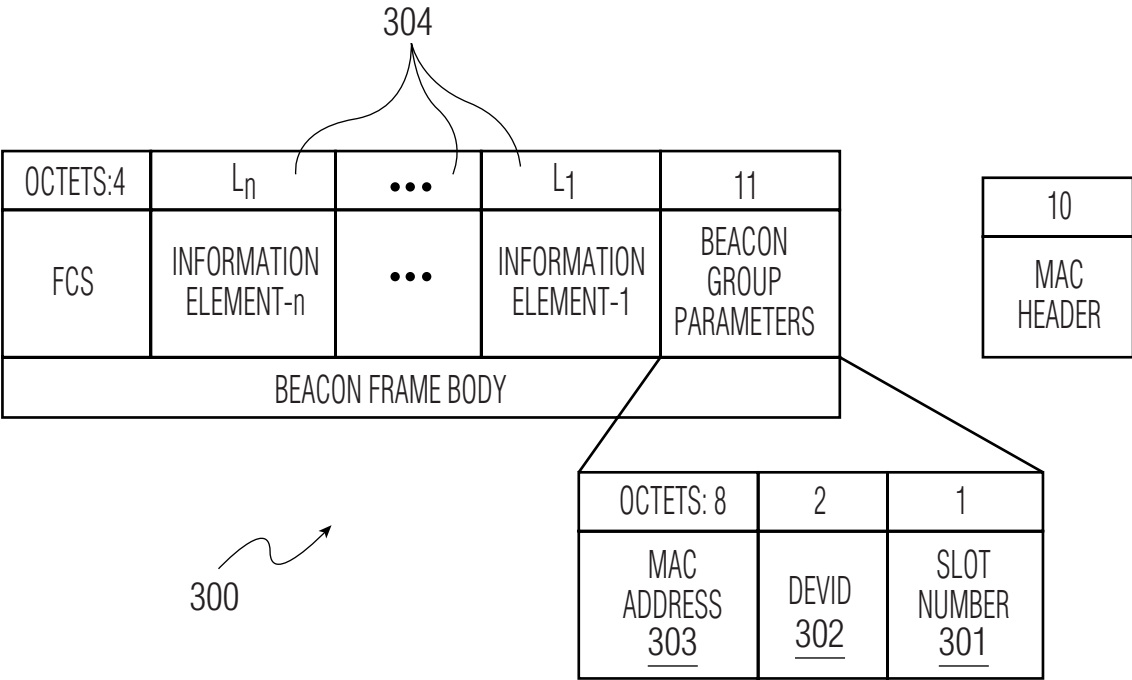


FIG. 3

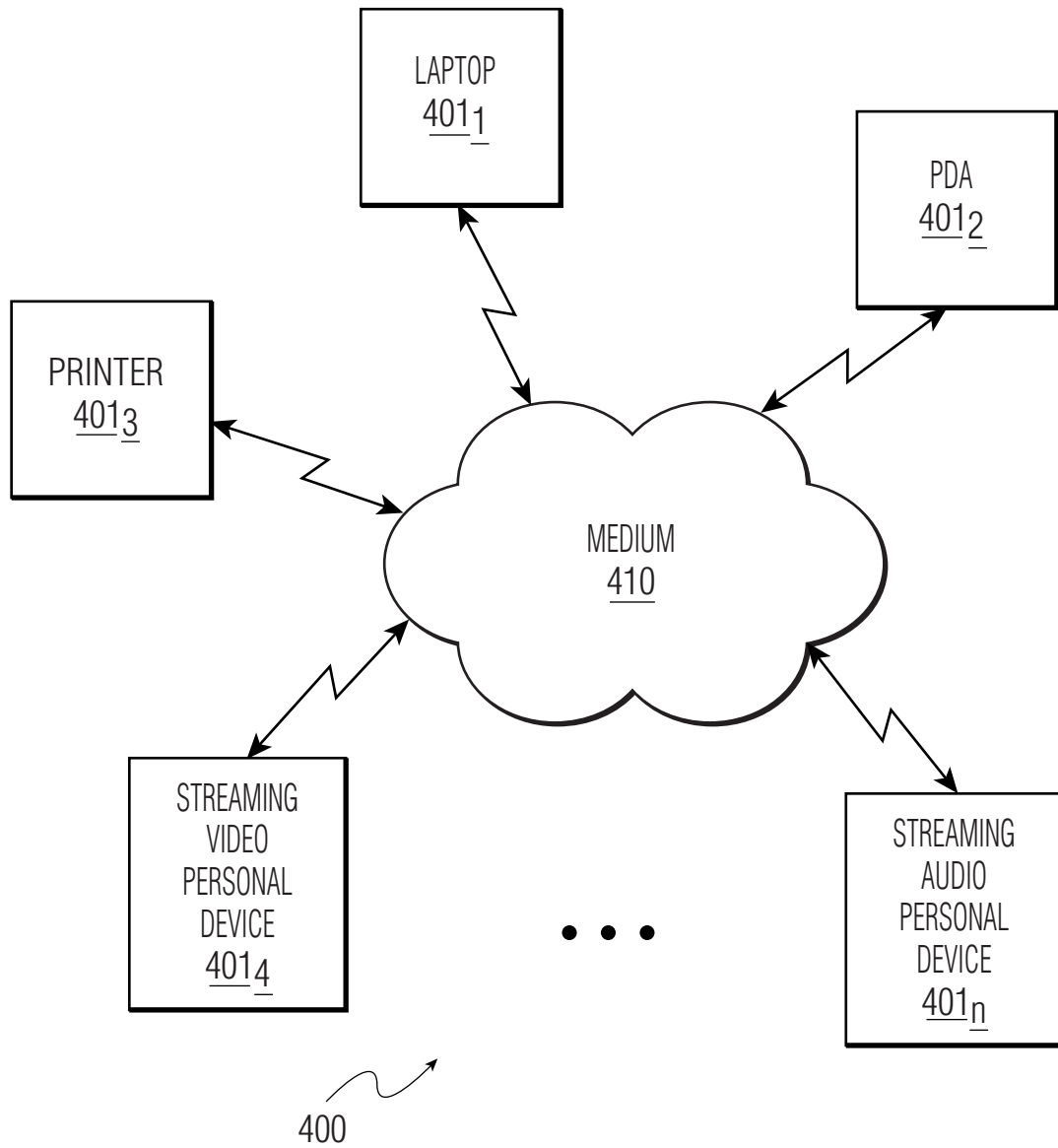


FIG. 4

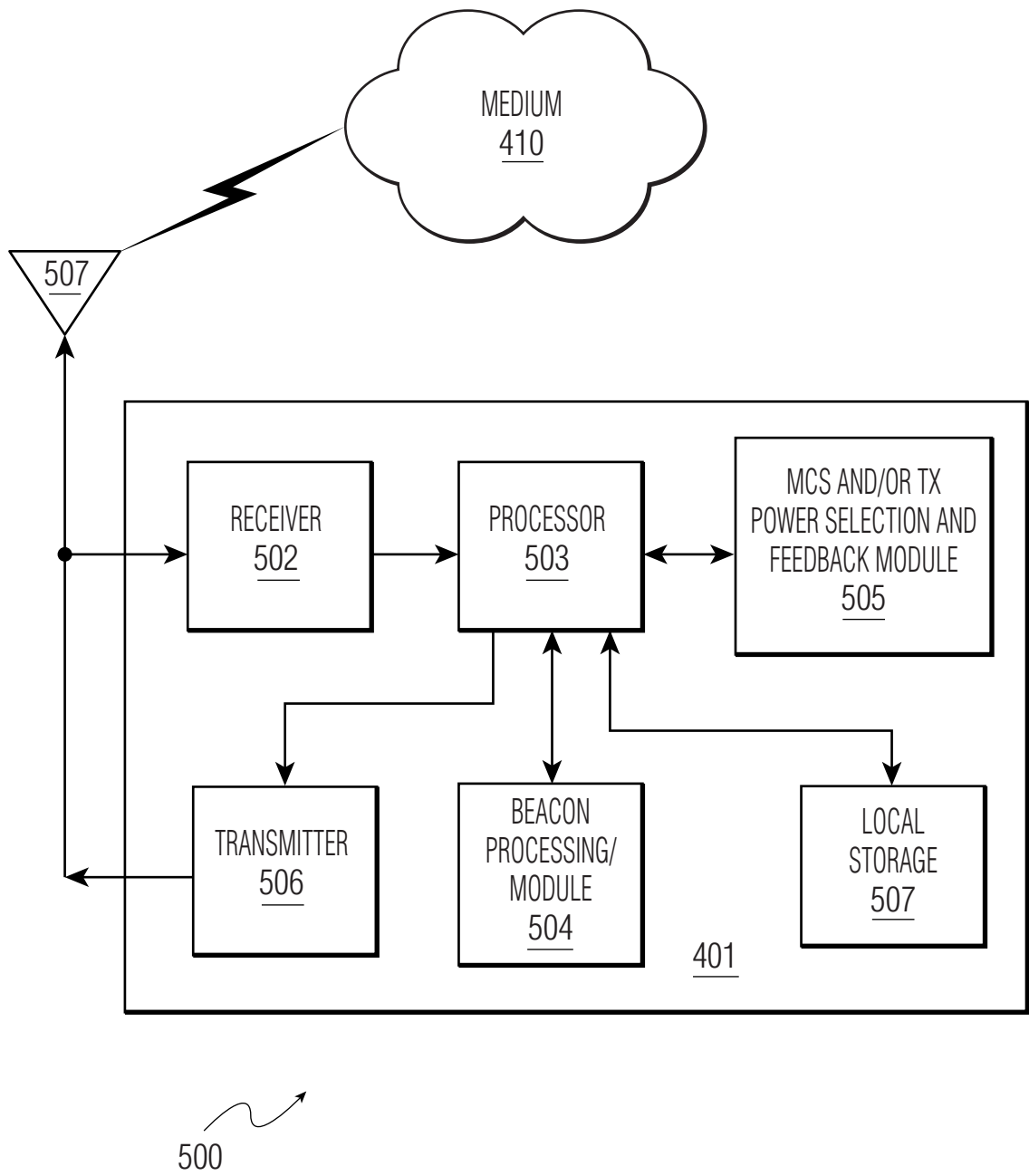


FIG. 5

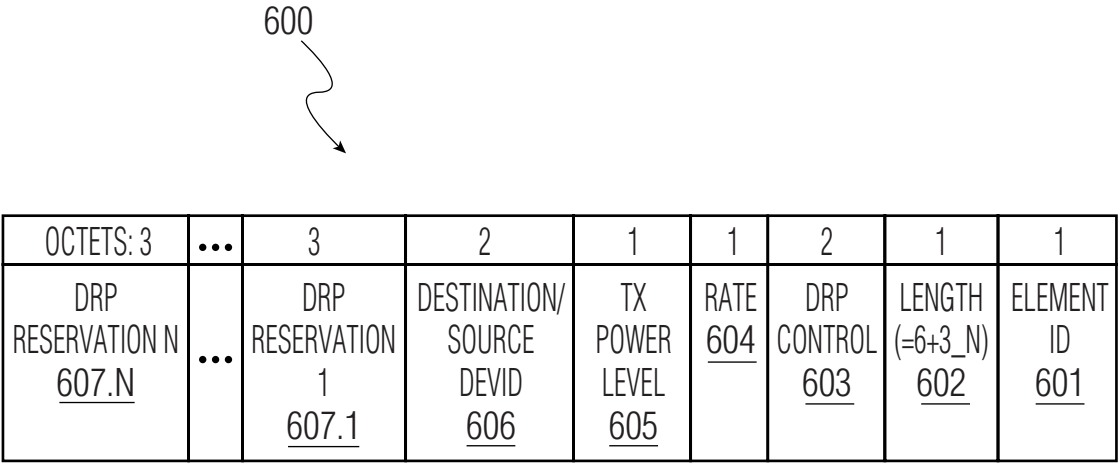


FIG. 6A

b15-b12	b11-b8	b7-b5	b4-b2	b1-b0
RESERVED <u>635</u>	UP/StreamIndex <u>634</u>	DRP RESERVATION PRIORITY <u>633</u>	DRP RESERVATION TYPE <u>632</u>	ACK POLICY <u>631</u>

FIG. 6B

600

OCTETS: 3	...	3	2	2	1	1
DRP RESERVATION N <u>607.N</u>	...	DRP RESERVATION 1 <u>607.1</u>	DESTINATION/ SOURCE DEVID <u>606</u>	DRP CONTROL <u>603</u>	LENGTH (=4+3_N) <u>602</u>	ELEMENT ID <u>601</u>

FIG. 7A

b15-b14	b13-b12	b11-b8	b7-b5	b4-b2	b1-b0
TX POWER <u>702</u>	RATE <u>701</u>	UP/StreamIndex <u>634</u>	DRP RESERVATION PRIORITY <u>633</u>	DRP RESERVATION TYPE <u>632</u>	ACK POLICY <u>631</u>

FIG. 7B

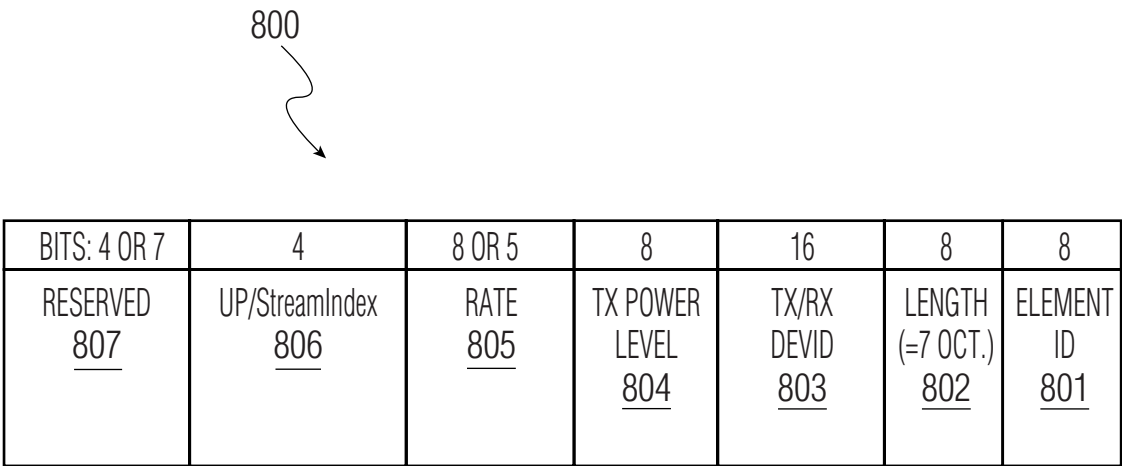


FIG. 8

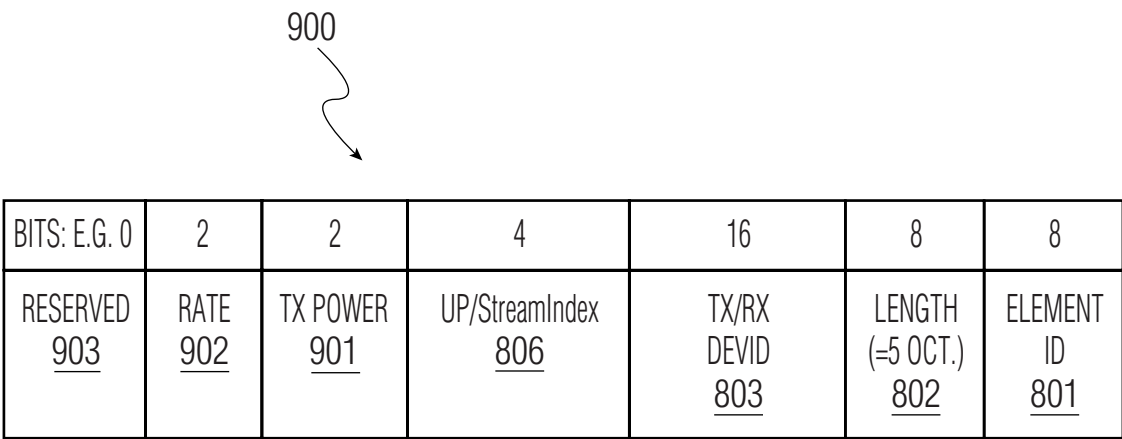


FIG. 9

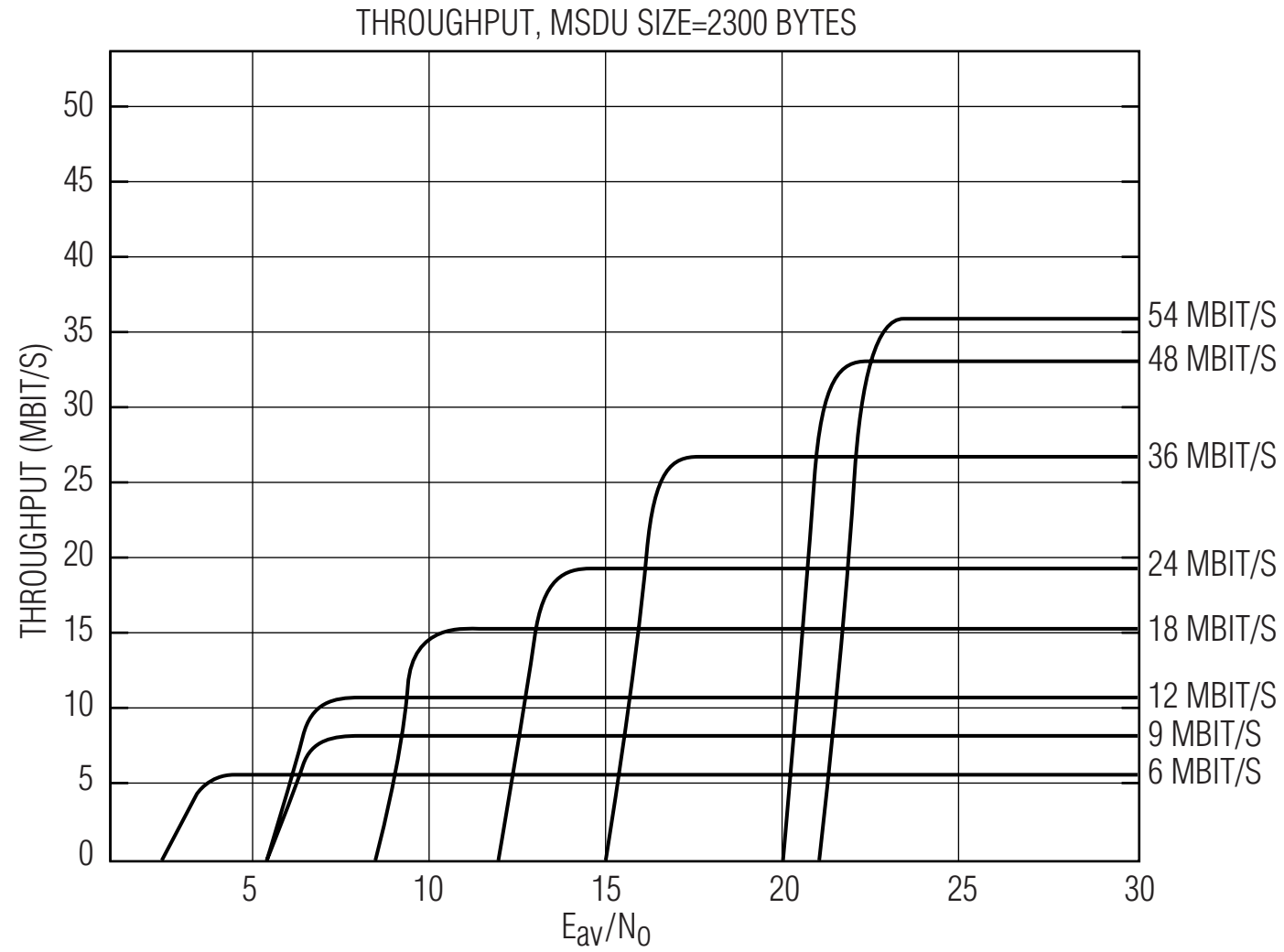


FIG. 10

800

OCTETS: 1	1	3		3
ELEMENT ID <u>801</u>	LENGTH(=2+5xN) <u>802</u>	LINK 1 <u>1100</u>	...	LINK N

FIG. 11

1100

BITS: B23-B20	B19-B16	B15-B0
DATA RATE <u>805</u>	TX POWER LEVEL CHANGE <u>804</u>	DevAddr <u>1200</u>

FIG. 12