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**PROTECTED THREE-PIN MOSGATED DEVICE POWER
SWITCH WITH SEPARATE RESET INPUT SIGNAL LEVEL**

ABSTRACT OF THE DISCLOSURE

A three-terminal Smart Power MOSgated device has short circuit, over-temperature and over-voltage protection and the like. The control circuit and the protective circuits are powered from an input or control pin connected to a microprocessor. The output of the microprocessor is connected to a resistive divider circuit such that three potentials can be selectively produced at the divider mode, and comprise an on-level signal, an off-level signal and a reset level signal respectively. The control circuit contains an R-S latch which turns on in the absence of a fault signal to connect the on and off signal levels to the MOSgated device. During a fault, the R-S latch disconnects the control circuit input from the MOSgated device, and is reset only by the reset level signal which is distinct from the off signal level which is incapable of resetting the R-S latch.

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