

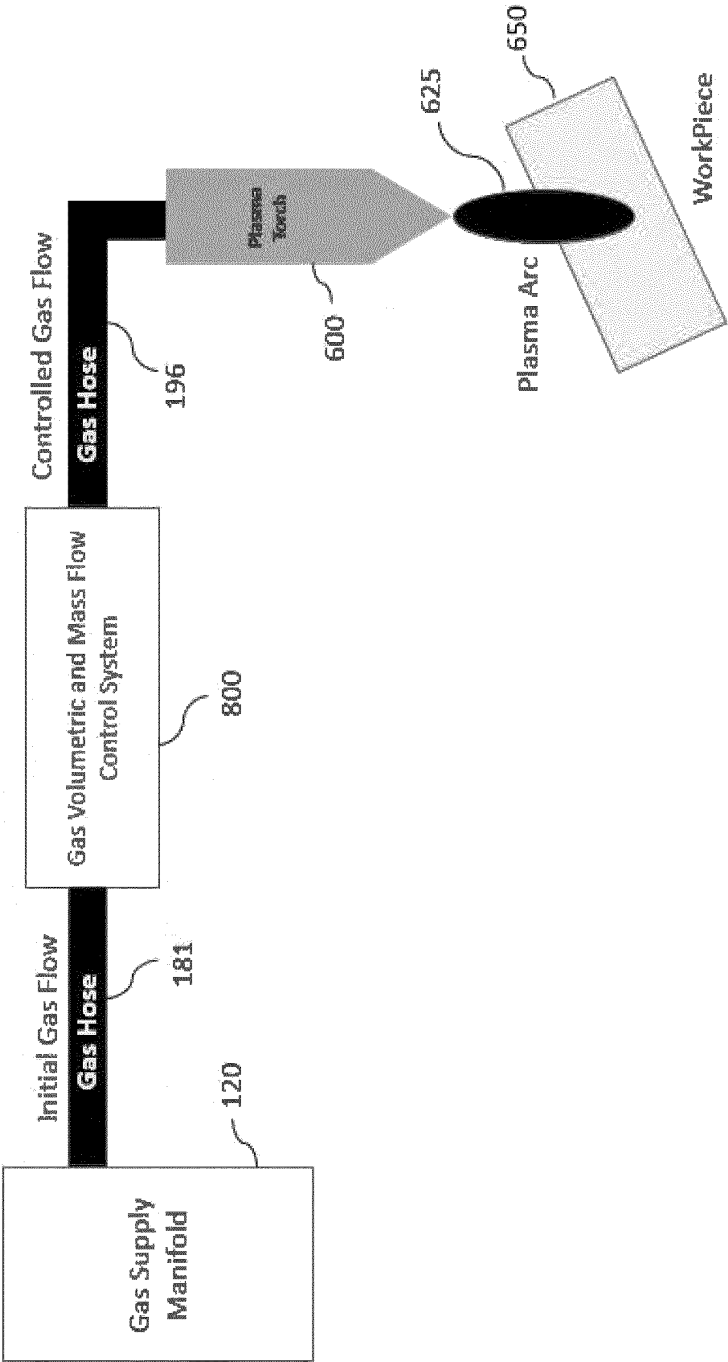


FIG. 1

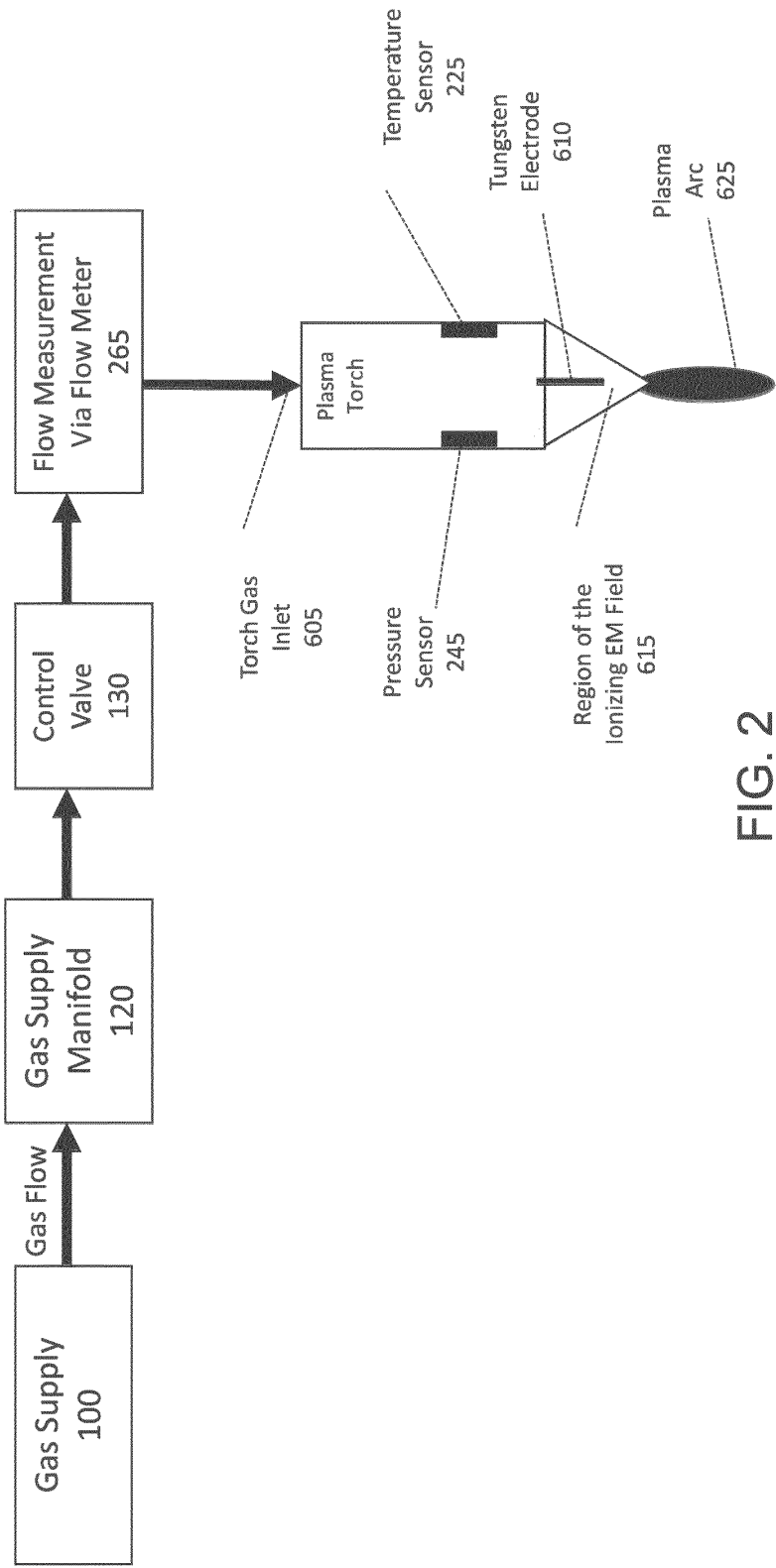


FIG. 2

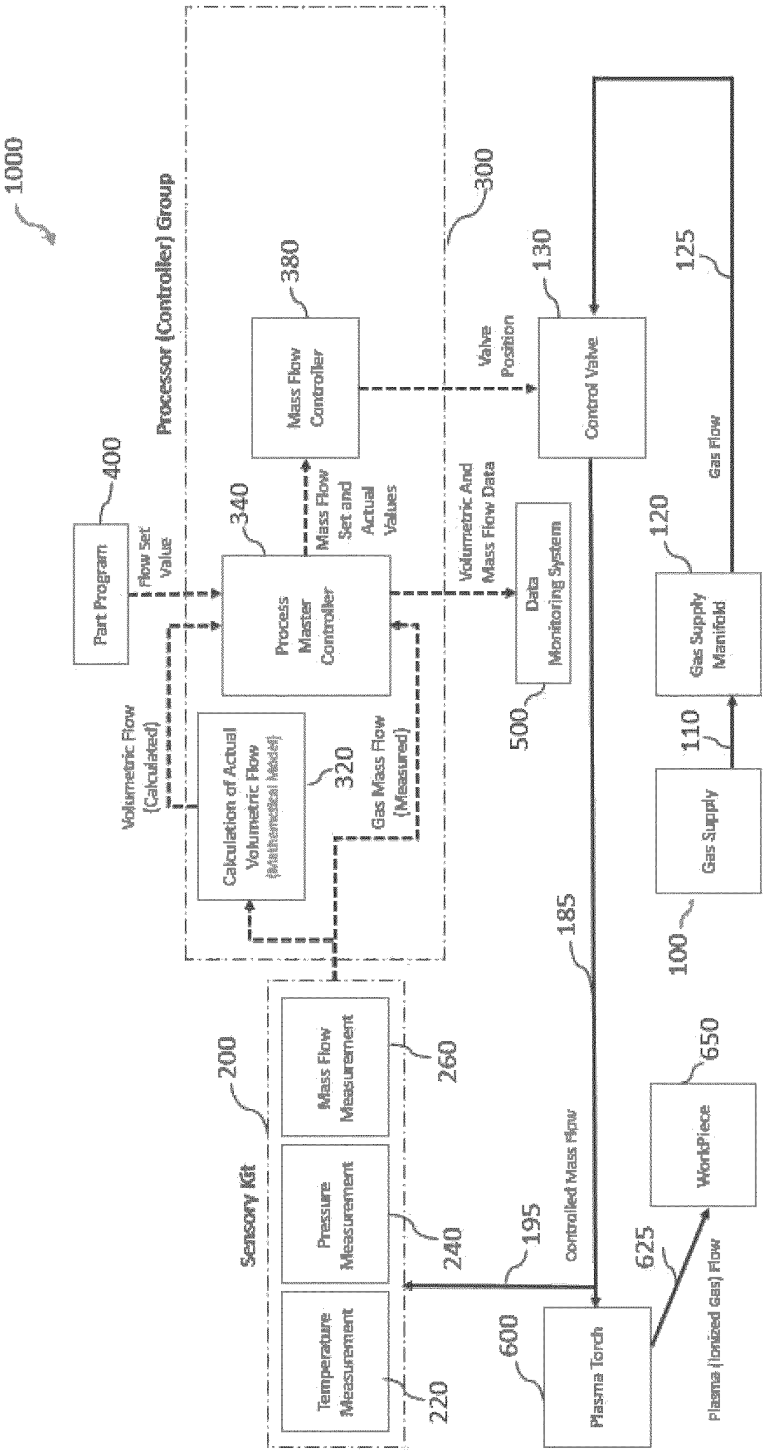


FIG. 3A

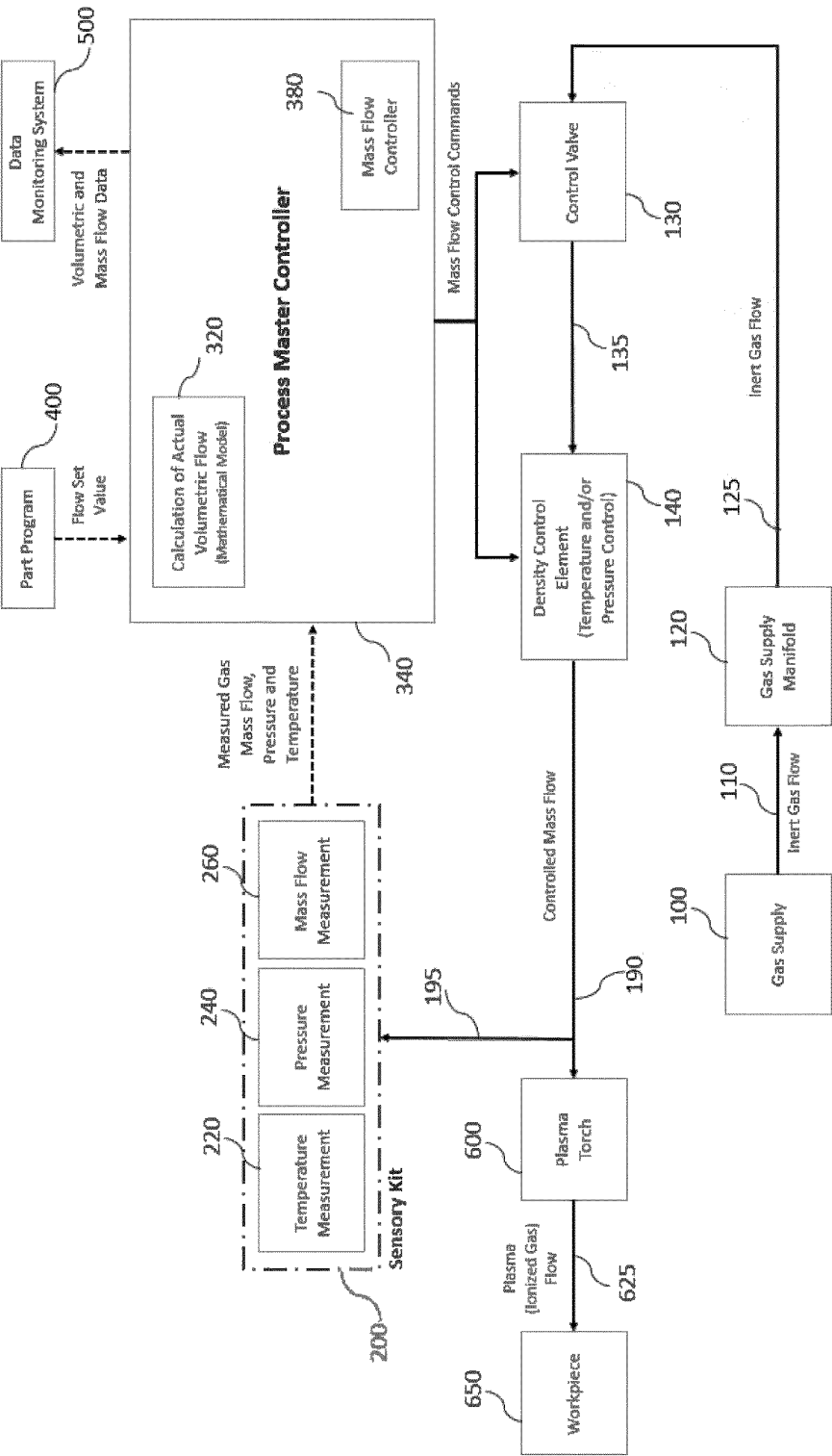


FIG. 3B

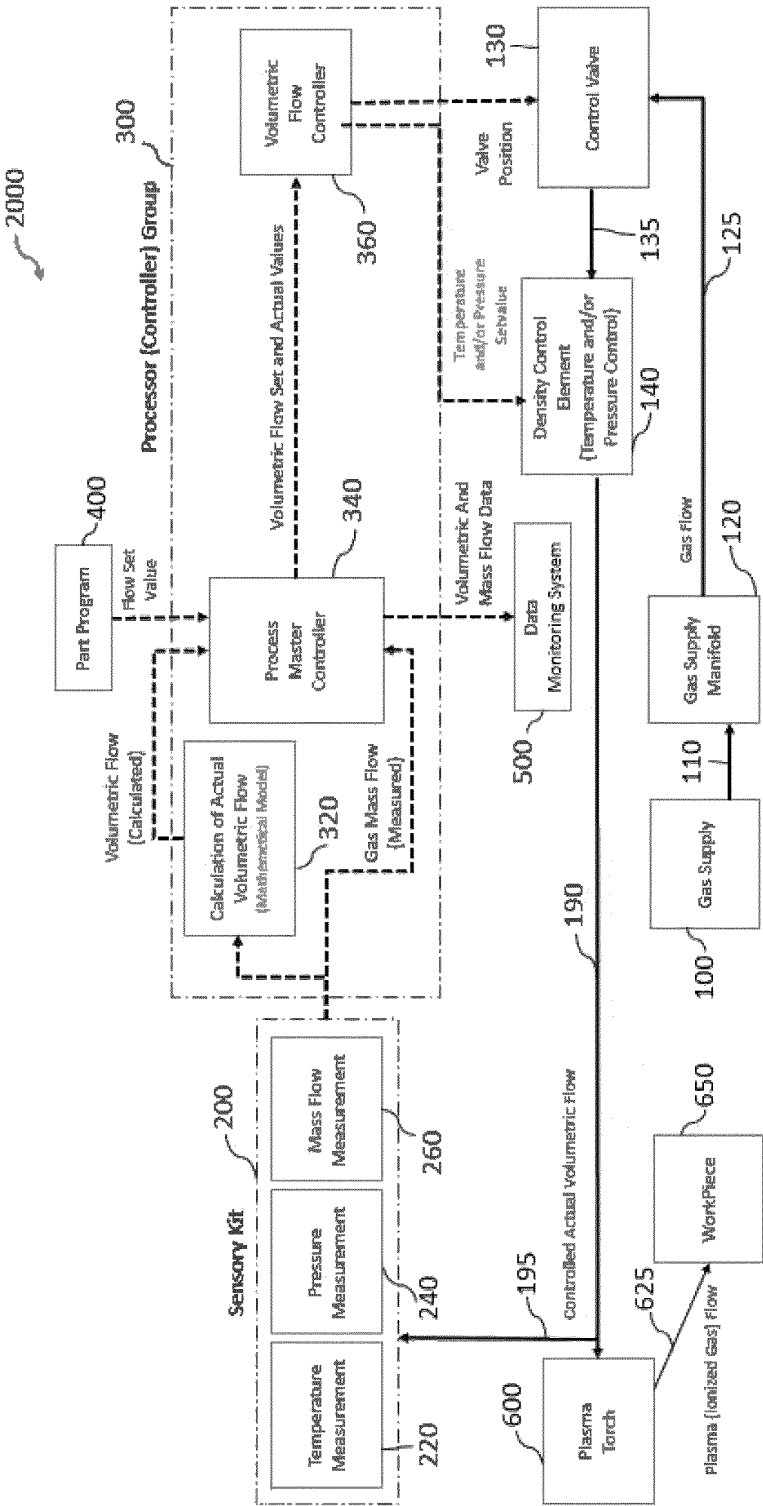


FIG. 4A

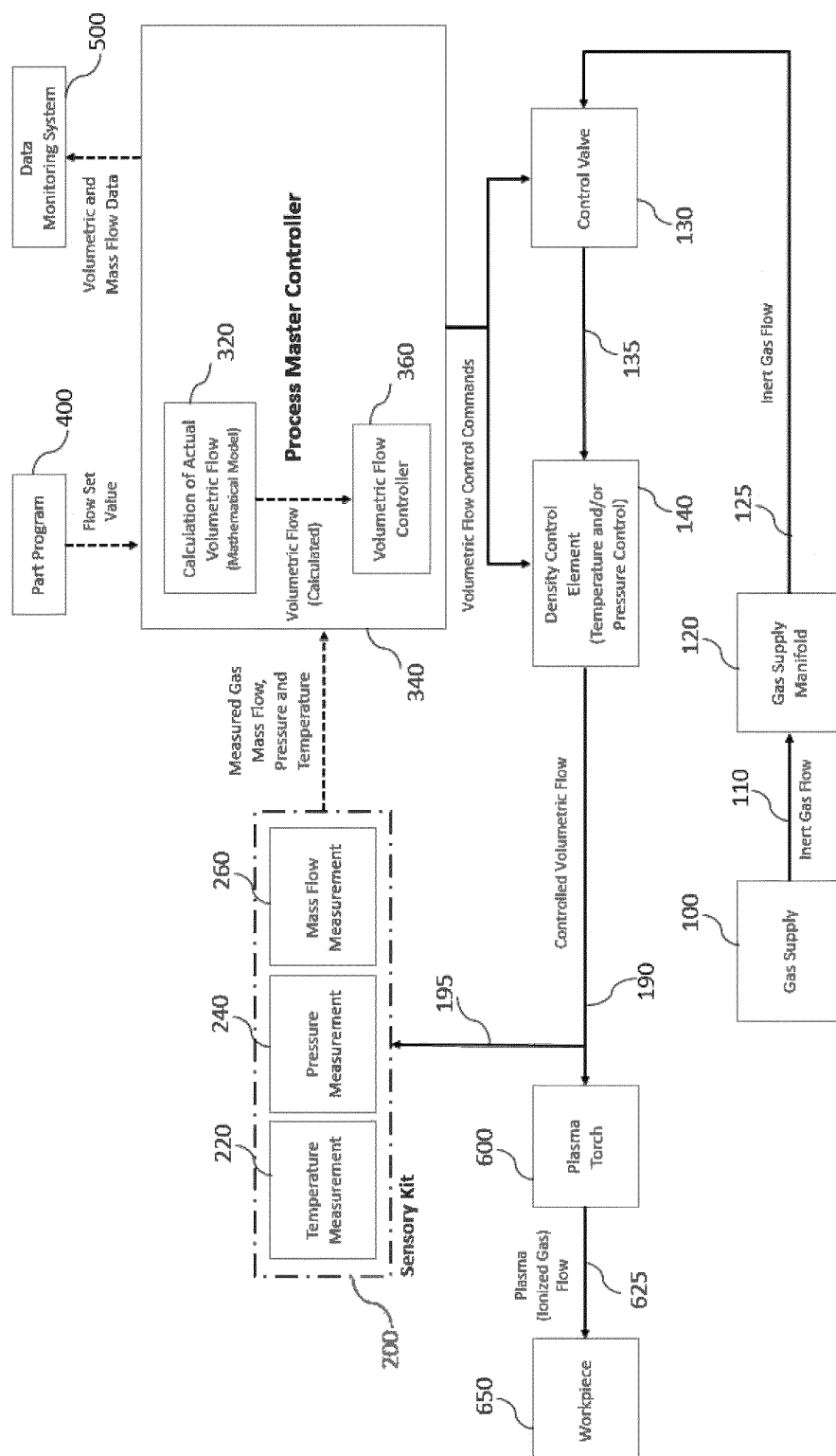


FIG. 4B

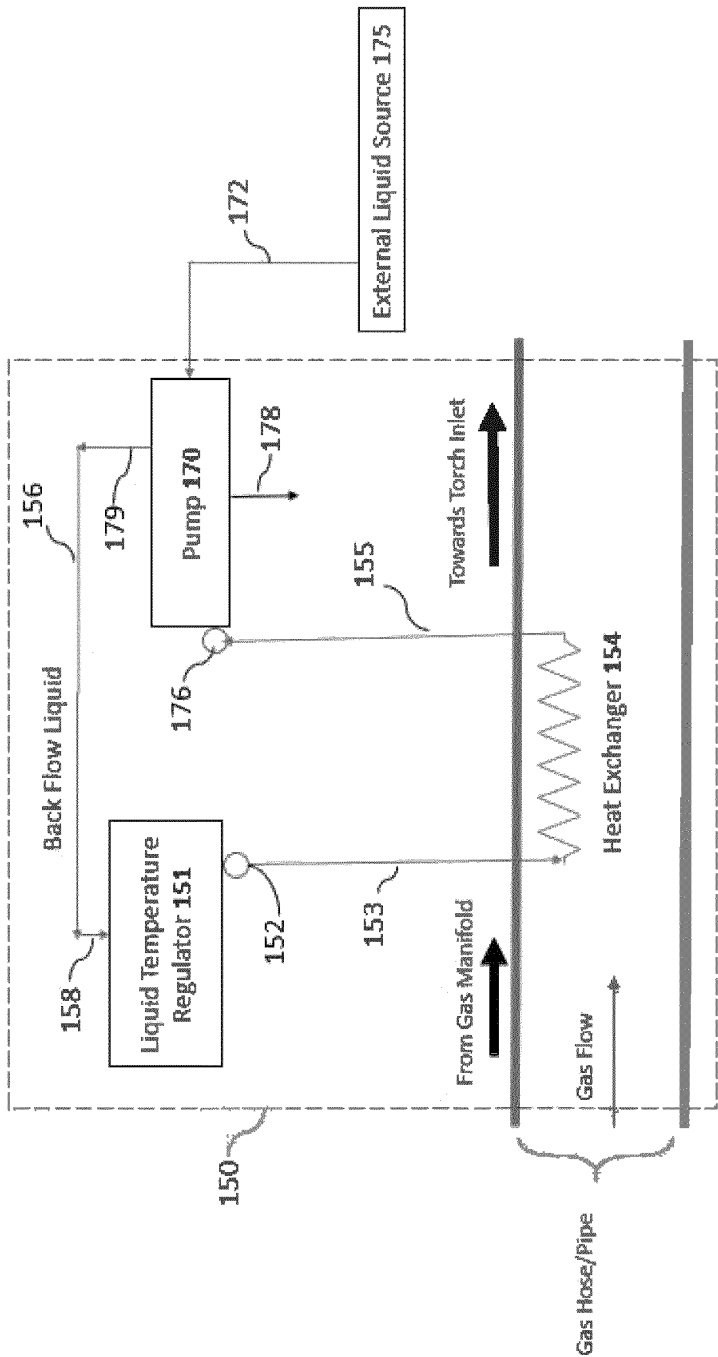


FIG. 5

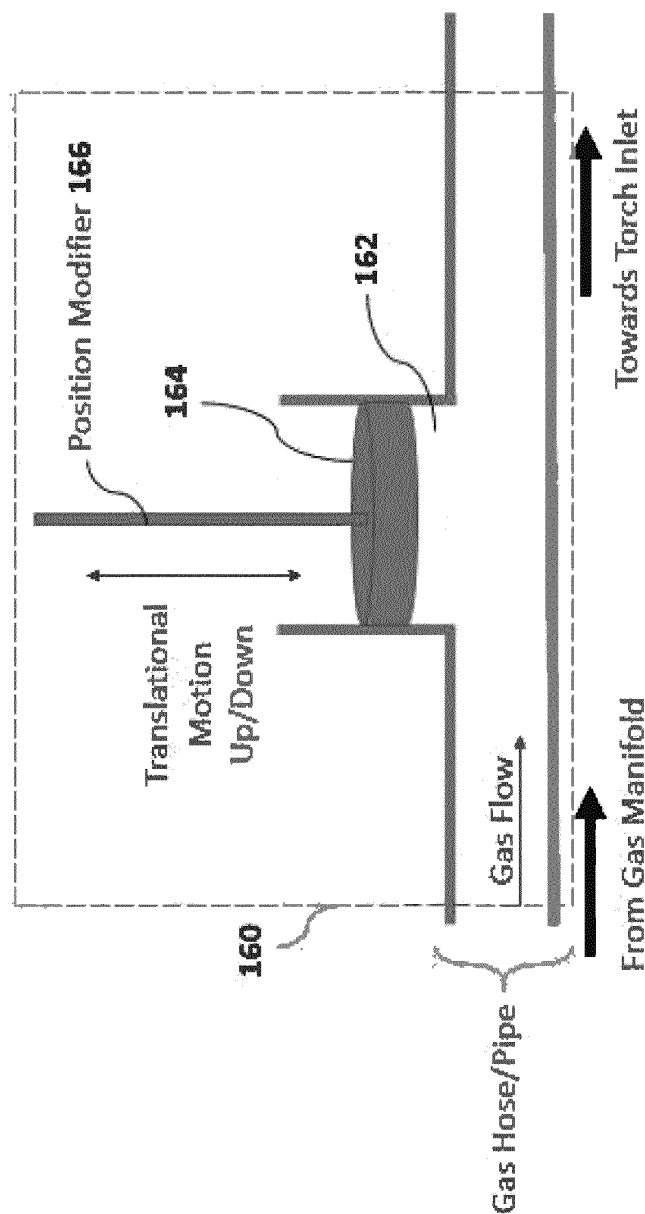


FIG. 6

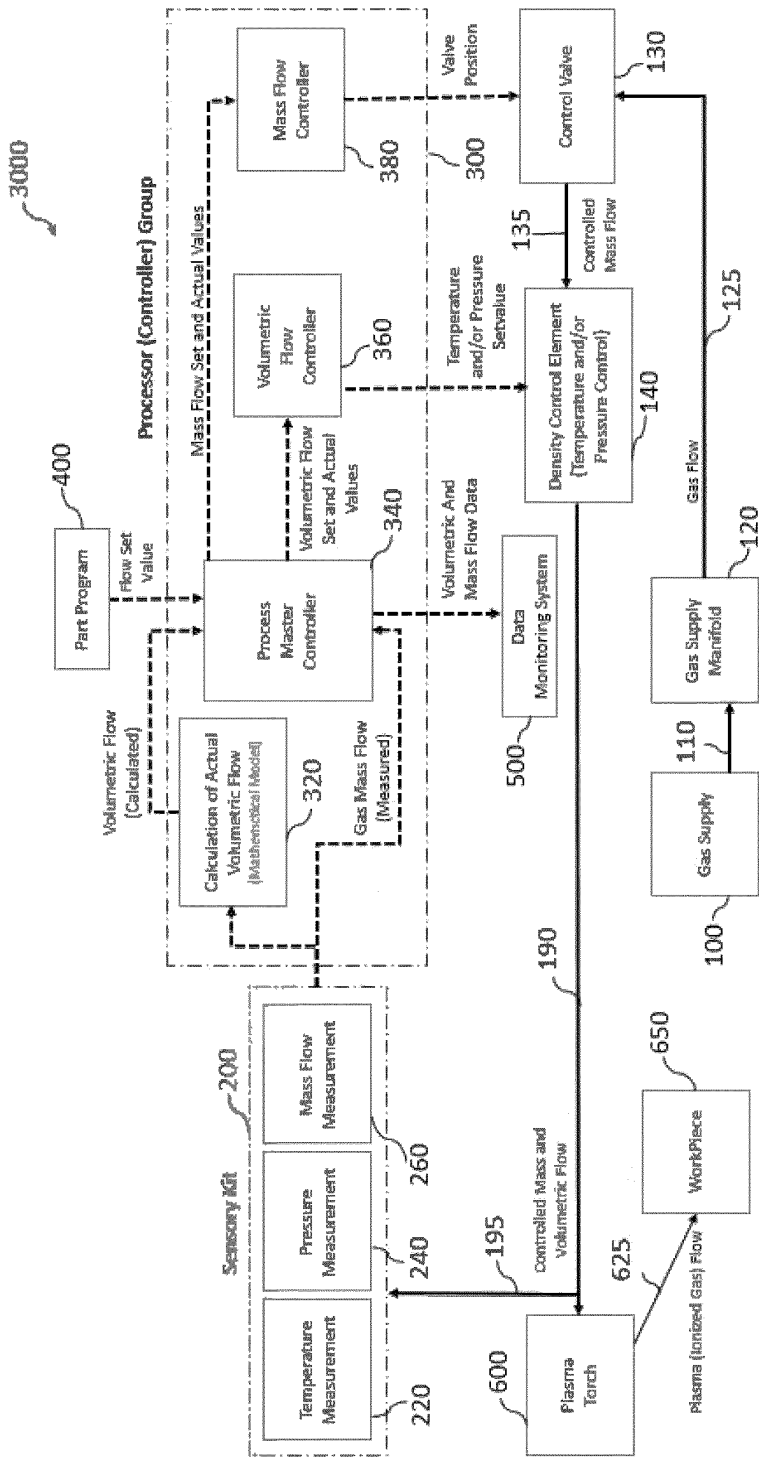


FIG. 7A

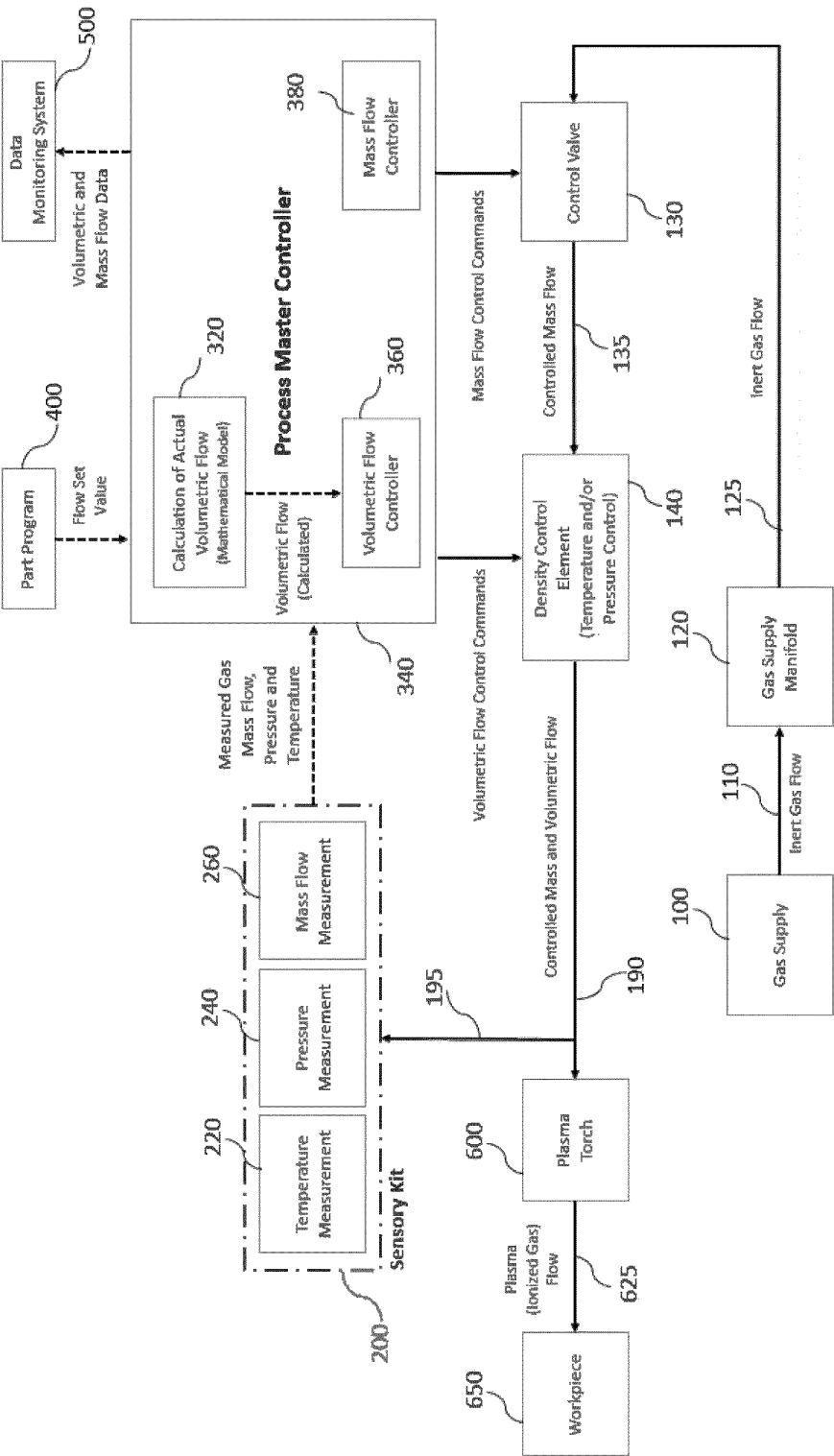


FIG. 7B

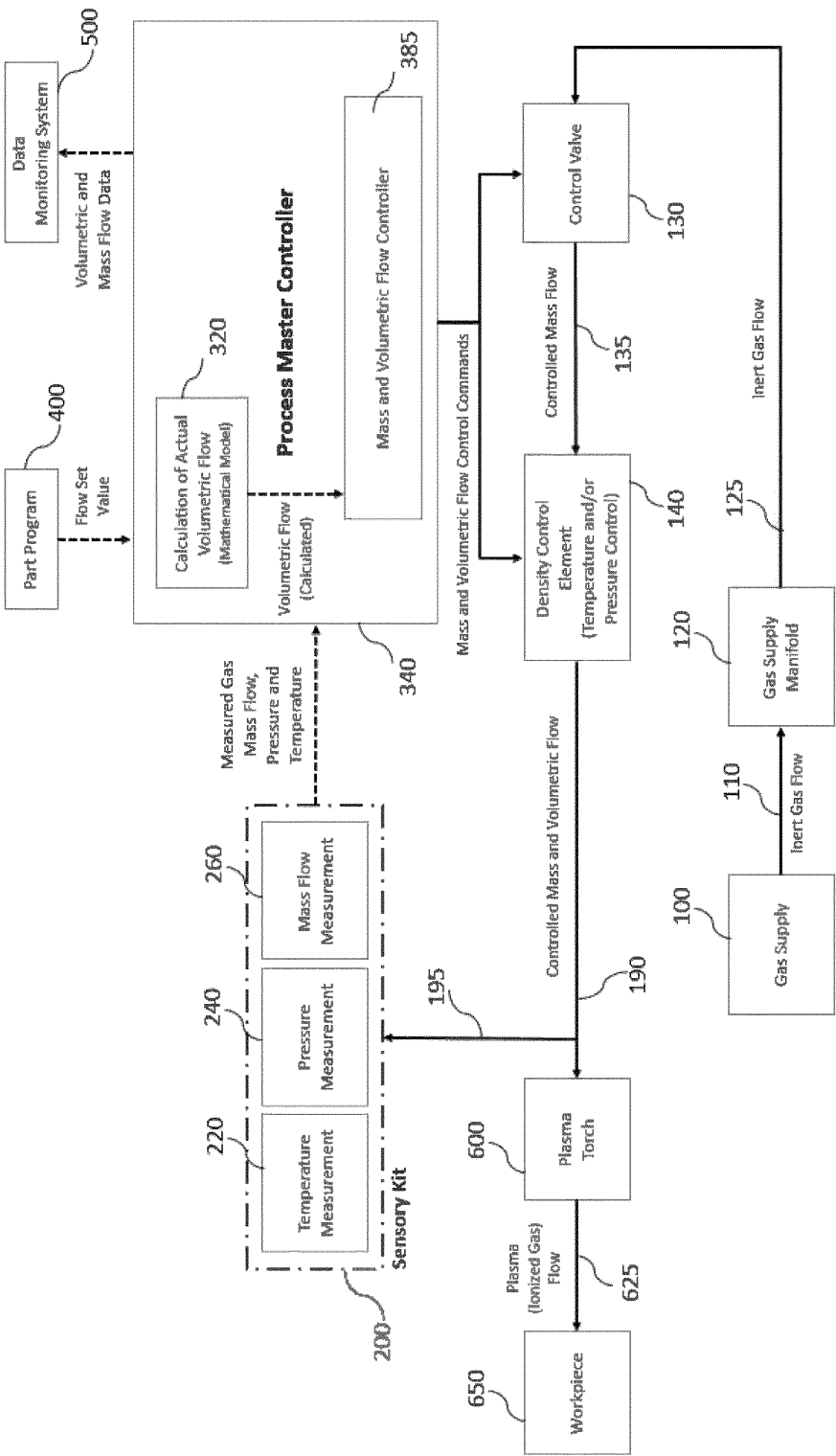


FIG. 7C

Two Parallel Single Input Single Output Controllers Ignoring the Coupling Terms

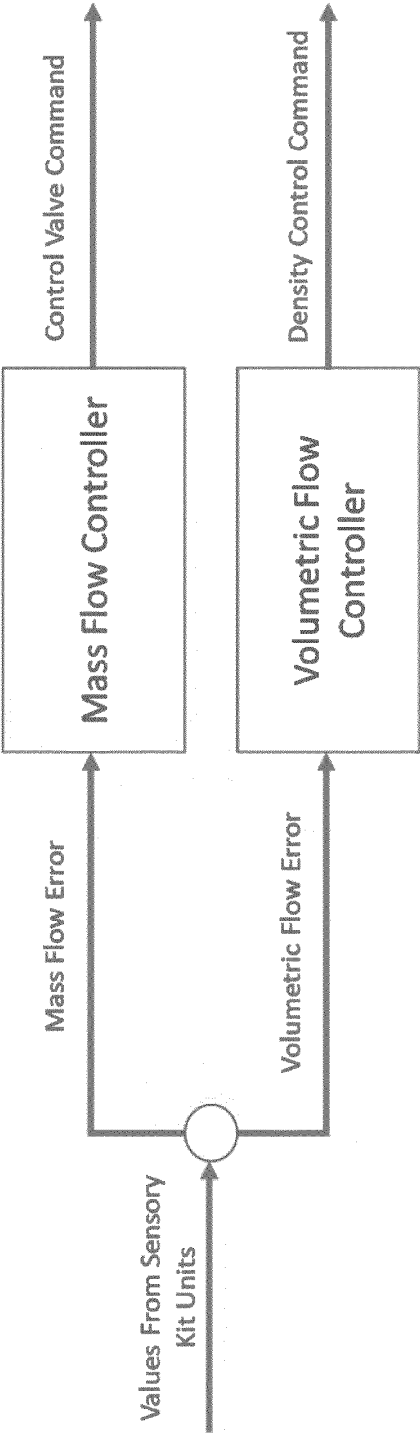


FIG. 8A

One Unified Multi Input Multi Output Controller Taking the Coupling Terms into Account

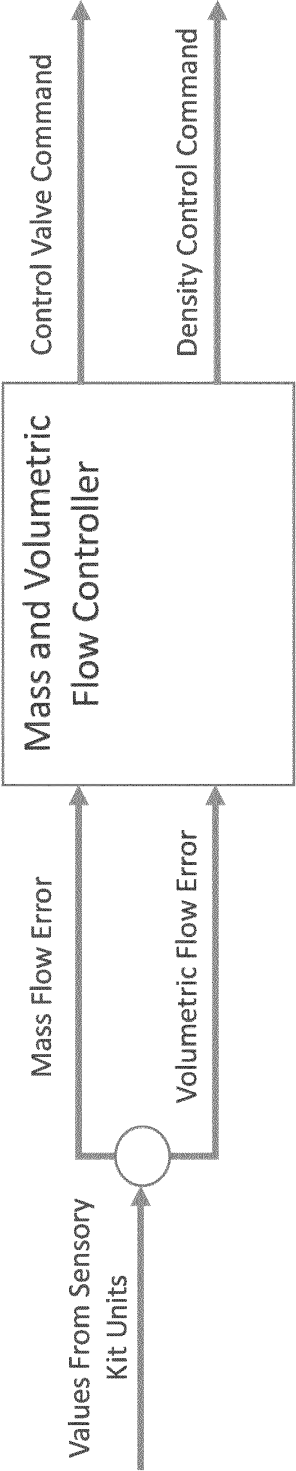


FIG. 8B