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ABSTRACT OF THE DISCLOSURE

One data processor (101) is provided with an interface means (119) for realizing connection with the other data processor (100), this interface means is provided with a function for connecting the other data processor as a bus master to an internal bus (108) of one data processor, and the relevant other data processor is capable of operating in direct peripheral functions memory mapped to the internal bus from an external side via said interface means. Accordingly, the data processor can utilize the peripheral functions of the other data processor without intermission of the program being executed. In short, one data processor can use in common the peripheral resources of the other data processor.



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