

POWER CONVERSION METHOD, APPARATUS, AND DEVICE, AND
MEDIUM

CROSS-REFERENCE TO RELATED DISCLOSURES

5 **[0001]** The present disclosure is based on and claims the priority to the Chinese Patent Application No. 201811456564.0 filed on November 30, 2018, which is hereby incorporated by reference in its entirety into the present disclosure.

10 TECHNICAL FIELD

[0002] The present disclosure relates to the field of power conversion, and in particular, to a power conversion method, apparatus, device, and medium.

15 BACKGROUND

[0003] Since systems such as photovoltaic power generation system, wind power generation system, solar power generation system, or AC power grid provided in the related art are configured very differently, and there are many corresponding power conversion apparatuses that cannot be used universally, power apparatuses used with the systems are complex and have poor universality and maintainability.

SUMMARY

[0004] At least some embodiments of the present disclosure provide a power conversion method, apparatus, device, and medium, so as to at least partially solve the problem of poor universality of the power conversion apparatus in the related art.

[0005] In an embodiment of the present disclosure, there is provided a power conversion apparatus comprising: a configurable input interface, a power conversion circuit, and a configurable output interface, wherein

the configurable input interface is arranged to configure a first electrical connection of an input power supply and the power conversion circuit, and is electrically connected with the input power supply and the power conversion circuit;

the configurable output interface is arranged to configure a second electrical connection of a load and the power conversion circuit, and is electrically connected with the load and the power conversion circuit; and

the power conversion circuit is arranged to perform corresponding power conversion according to parameters of the input power supply and parameters of the load.

[0006] In an embodiment of the present disclosure, there is further provided a power conversion method comprising:

configuring single-pole double-throw switches K1, K2, K3, and K4, according to parameters of an input power supply and parameters of a load; and

controlling on/off of triodes in switching groups according to rules corresponding to the parameters of the input power supply and

the parameters of the load.

[0007] In an embodiment of the present disclosure, there is further provided a power conversion device comprising: at least one processor, at least one memory, and computer program instructions stored in the memory, which when executed by the processor, implement the power conversion method described above.

[0008] In an embodiment of the present disclosure, there is further provided a computer-readable storage medium having stored thereon computer program instructions, which when executed by a processor, implement the power conversion method described above.

[0009] According to the power conversion method, apparatus, device, and medium provided by the embodiments of the present disclosure, the power conversion apparatus comprises: a configurable input interface, a power conversion circuit, and a configurable output interface, wherein the configurable input interface is arranged to configure a first electrical connection of the input power supply and the power conversion circuit, and is electrically connected with the input power supply and the power conversion circuit; the configurable output interface is arranged to configure a second electrical connection of a load and the power conversion circuit, and is electrically connected with the load and the power conversion circuit; and the power conversion circuit is arranged to perform corresponding power conversion according to parameters of the input power supply and parameters of the load, so that the problem of poor universality of the power conversion apparatus in the related art is solved, and the universality of the power conversion apparatus is improved.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] The accompanying drawings described herein are used for providing a further understanding of this disclosure and constitute a part of the present application, and the disclosed illustrative
5 embodiments, together with the description thereof, serve to explain but not to limit improperly the present disclosure. In the accompanying drawings:

[0011] Fig. 1 is a block diagram of a power conversion apparatus according to an embodiment of the present disclosure.

10 [0012] Fig. 2 is a circuit diagram of a power conversion apparatus according to a first alternative embodiment of the present disclosure.

[0013] Fig. 3 is a circuit diagram of a power conversion apparatus according to a second alternative embodiment of the present disclosure.

15 [0014] Fig. 4 is a circuit diagram of a power conversion apparatus according to a third alternative embodiment of the present disclosure.

[0015] Fig. 5 is a flow diagram of a power conversion method according to an embodiment of the present disclosure.

[0016] Fig. 6 is a schematic hardware diagram of a power conversion
20 device according to an embodiment of the disclosure.

DETAILED DESCRIPTION

[0017] Features and exemplary embodiments of various aspects of the present disclosure will be described in detail below, and in order to make objectives, technical solutions and advantages of the present disclosure more clear and explicit, the present disclosure will be further described in detail below in combination with the accompanying drawings and the embodiments. It should be understood that the specific embodiments described herein are merely illustrative of this disclosure and are not intended to limit it. It will be apparent to one skilled in the art that the present disclosure may be implemented without some of these specific details. The following description of the embodiments is merely intended to provide a better understanding of the present disclosure by illustrating examples thereof.

[0018] It should be noted that, in this disclosure, relational terms such as first and second, are used solely to distinguish one entity or operation from another, and do not necessarily require or imply any such actual relation or order between these entities or operations. Also, terms "including," "comprising," or any other variation thereof, are intended to cover a non-exclusive inclusion, such that a process, method, article, or device comprising a series of elements comprises not only those elements, but also other elements not expressly listed, or elements inherent in such a process, method, article, or device. Without further limitation, elements defined by a sentence "comprising ..." does not exclude that there are additional identical elements in the process, method, article, or device comprising the elements.

[0019] In this embodiment, there is provided a power conversion apparatus. Fig. 1 is a block diagram of a power conversion apparatus according to an embodiment of the present disclosure, and as shown in Fig. 1, the apparatus comprises: a configurable input interface 1, a power conversion circuit 2, and a configurable output interface 3, wherein

the configurable input interface 1 is arranged to configure a first electrical connection of an input power supply and the power conversion circuit 2, and is electrically connected with the input power supply and the power conversion circuit 2;

the configurable output interface 3 is arranged to configure a second electrical connection of a load and the power conversion circuit 2, and is electrically connected with the load and the power conversion circuit 2; and

the power conversion circuit 2 is arranged to perform corresponding power conversion according to parameters of the input power supply and parameters of the load.

[0020] By using the above power conversion apparatus, one configurable input interface and one configurable output interface are used for connecting with the power conversion circuit according to different electrical connections, and the power conversion circuit performs corresponding power conversion according to the parameters of the input power supply and the parameters of the load, so that the power conversion apparatus can adapt to various power conversion requirements of the input power supply and the load on AC voltage, DC voltage, step-up voltage and step-down voltage, which solves the problem of poor universality of a power conversion apparatus in the

related art, and improves the universality of the power conversion apparatus.

[0021] The above input power supply comprises, but is not limited to: a photovoltaic power generation network, a wind power generation network, an energy storage system, an AC power grid, etc., and the load comprises a DC load and an AC load.

[0022] Alternatively, the power conversion performed by the above power conversion circuit comprises at least two of the following: DC/DC step-up conversion, DC/DC step-down conversion, AC/DC conversion, DC/AC conversion, AC/AC conversion, or DC/DC step-up conversion first and then DC/AC conversion. These power conversion functions can be implemented by using a plurality of independent circuits, for example, a DC/DC step-up conversion circuit is a separate one, the DC/DC step-down conversion circuit is a separate one, etc., and these circuits are independent from each other except that their input and output ends are shared through the configurable input interface and the configurable output interface.

[0023] Preferably, the power conversion functions described above in the embodiment are implemented by one circuit. In this embodiment, one power conversion circuit will be adopted for implementing the above power conversion functions, which will be taken as an example for explanation. Fig. 2 is a circuit diagram of a power conversion apparatus according to a first alternative embodiment of the present disclosure, and this embodiment will be described and explained in conjunction with Fig. 2.

[0024] Alternatively, the configurable input interface 1 comprises: single-pole double-throw switches K1 and K2, wherein an immovable

end K1-1 of the single-pole double-throw switch K1 and an immovable end K2-1 of the single-pole double-throw switch K2 are respectively arranged to be electrically connected with the input power supply; and movable ends K1-2 and K1-3 of the single-pole double-throw switch K1, and movable ends K2-2 and K2-3 of the single-pole double-throw switch K2 are respectively arranged to be electrically connected with four input ends of the power conversion circuit, wherein the movable ends K1-2, K1-3, K2-2 and K2-3 are electrically connected with the input ends IN1, IN2, IN3 and IN4 of the power conversion circuit, respectively.

[0025] Alternatively, the configurable output interface 3 comprises: single-pole double-throw switches K3 and K4, wherein an immovable end K3-1 of the single-pole double-throw switch K3 and an immovable end K4-1 of the single-pole double-throw switch K4 are respectively arranged to be electrically connected with the load; and movable ends K3-2 and K3-3 of the single-pole double-throw switch K3, and movable ends K4-2 and K4-3 of the single-pole double-throw switch K4 are respectively arranged to be electrically connected with four output ends of the power conversion circuit, wherein the movable ends K3-2, K3-3, K4-2 and K4-3 are electrically connected with the output ends OUT1, OUT2, OUT3 and OUT4 of the power conversion circuit, respectively.

[0026] The single-pole double-throw switch is preferably a relay or contactor, so as to realize automatic circuit control.

[0027] Alternatively, the power conversion circuit 2 comprises: input ends IN1, IN2, IN3, and IN4, output ends OUT1, OUT2, OUT3, and OUT4, switching groups S1, S2, S3, S4, S5, S6, S7, and S8, inductors

L1 and L2, and a capacitor C, wherein the input end IN1 is electrically connected with the output end OUT1; the input end IN4 is electrically connected with the output end OUT4; the switching groups S1 and S2 that are connected in series in a same direction, the switching groups S3 and S4 that are connected in series in a same direction, the capacitor C, the switching groups S5 and S6 that are connected in series in a same direction, and the switching groups S7 and S8 that are connected in series in a same direction are connected in parallel between the input ends IN1 and IN4; the inductor L1 is connected in series between the input end IN2 and a series connection node of the switching groups S1 and S2; the inductor L2 is connected in series between the output end OUT2 and a series connection node of the switching groups S5 and S6; the input end IN3 is electrically connected with a serial connection node of the switching groups S3 and S4; the output end OUT3 is electrically connected with a serial connection node of the switching groups S7 and S8; wherein each switching group includes: a triode and a diode that are connected in anti-parallel.

[0028] With reference to Fig. 3, alternatively, the power conversion circuit 2 further comprises: a PWM control unit, arranged to control on/off of the triodes in the switching groups according to rules corresponding to the combination of the first electrical connection and the second electrical connection.

[0029] With reference to Fig. 4, alternatively, the power conversion circuit 2 further comprises other peripheral circuits, arranged to implement functions such as automatic control of the single-pole double-throw switches K1 to K4, and voltage and current sampling of the input interface and the output interface.

[0030] Through the above power conversion apparatus, the use of one circuit can realize a variety of power conversion, which greatly reduces the volume of the apparatus.

[0031] A control method of the above power conversion apparatus, i.e., a power conversion method, will be described and explained below.

[0032] Fig. 5 is a flow diagram of a power conversion method according to an embodiment of the present disclosure, and as shown in Fig. 5, the method comprises:

step S501, configuring single-pole double-throw switches K1, K2, K3 and K4, according to parameters of an input power supply and parameters of a load; and

step S502, controlling on/off of triodes in switching groups according to rules corresponding to the parameters of the input power supply and the parameters of the load.

[0033] With respect to different power conversion requirements, the single-pole double-throw switches K1 to K4 are configured differently, and on/off control rules of the triodes in the switching groups S1 to S8 are also different, which will be separately described with reference to Fig. 3.

[0034] Alternatively, under the condition that the input power supply is a DC power supply, the load is a DC load, and the voltage required by the load is higher than that of the input power supply, it is configured that the immovable end K1-1 of the single-pole double-throw switch K1 is connected with the movable end K1-3, the immovable end K2-1 of the single-pole double-throw switch K2 is connected with the movable end K2-3, the immovable end K3-1 of the single-pole

double-throw switch K3 is connected with the movable end K3-2, and the immovable end K4-1 of the single-pole double-throw switch K4 is connected with the movable end K4-3; and locking of the triodes in the switching groups S1, S3, S4, S5, S6, S7 and S8 is controlled, and on/off of the triodes in the switching group S2 is controlled according to a preset frequency. At this time, a DC step-up circuit is constituted.

[0035] Alternatively, under the condition that the input power supply is a DC power supply, the load is a DC load, and voltage required by the load is lower than that of the input power supply, it is configured that the immovable end K1-1 of the single-pole double-throw switch K1 is connected with the movable end K1-2, the immovable end K2-1 of the single-pole double-throw switch K2 is connected with the movable end K2-3, the immovable end K3-1 of the single-pole double-throw switch K3 is connected with the movable end K3-3, and the immovable end K4-1 of the single-pole double-throw switch K4 is connected with the movable end K4-3; locking of the triodes in the switching groups S1, S2, S3, S4, S6, S7 and S8 is controlled, and on/off of the triodes in the switching group S5 is controlled according to a preset frequency. At this time, a DC step-down circuit is constituted.

[0036] Alternatively, under the condition that the input power supply is an AC power supply and the load is a DC load, it is configured that the immovable end K1-1 of the single-pole double-throw switch K1 is connected with the movable end K1-3, the immovable end K2-1 of the single-pole double-throw switch K2 is connected with the movable end K2-2, the immovable end K3-1 of the single-pole

double-throw switch K3 is connected with the movable end K3-2, and the immovable end K4-1 of the single-pole double-throw switch K4 is connected with the movable end K4-3; locking of the triodes in the switching groups S5, S6, S7 and S8 is controlled, and on/off of the triodes in the switching groups S1, S2, S3 and S4 is controlled according to a preset frequency. At this time, a controllable rectifier circuit is constituted.

[0037] Alternatively, under the condition that the input power supply is a DC power supply and the load is an AC load, it is configured that the immovable end K1-1 of the single-pole double-throw switch K1 is connected with the movable end K1-2, the immovable end K2-1 of the single-pole double-throw switch K2 is connected with the movable end K2-3, the immovable end K3-1 of the single-pole double-throw switch K3 is connected with the movable end K3-3, and the immovable end K4-1 of the single-pole double-throw switch K4 is connected with the movable end K4-2; locking of the triodes in the switching groups S1, S2, S3 and S4 is controlled, and on/off of the triodes in the switching groups S5, S6, S7 and S8 is controlled according to a preset frequency. At this time, an inverter circuit is constituted.

[0038] Alternatively, under the condition that the input power supply is an AC power supply and the load is an AC load, it is configured that the immovable end K1-1 of the single-pole double-throw switch K1 is connected with the movable end K1-3, the immovable end K2-1 of the single-pole double-throw switch K2 is connected with the movable end K2-2, the immovable end K3-1 of the single-pole double-throw switch K3 is connected with the movable end K3-3, and

the immovable end K4-1 of the single-pole double-throw switch K4 is connected with the movable end K4-2; on/off of the triodes in the switching groups S1, S2, S3, S4, S5, S6, S7 and S8 is controlled according to a preset frequency. At this time, the switching groups S1, S2, S3, S4, and other elements constitute a controllable rectifier circuit, and the switching groups S5, S6, S7, S8, and other elements constitute an inverter circuit.

[0039] Alternatively, under the condition that the input power supply is a DC power supply, the load is an AC load, and voltage required by the load is step-up, it is configured that the immovable end K1-1 of the single-pole double-throw switch K1 is connected with the movable end K1-3, the immovable end K2-1 of the single-pole double-throw switch K2 is connected with the movable end K2-3, the immovable end K3-1 of the single-pole double-throw switch K3 is connected with the movable end K3-3, and the immovable end K4-1 of the single-pole double-throw switch K4 is connected with the movable end K4-2; locking of the triodes in the switching groups S1, S3 and S4 is controlled, and on/off of the triodes in the switching groups S2, S5, S6, S7 and S8 is controlled according to a preset frequency. At this time, the switching groups S1, S2, S3, S4, and other elements constitute a step-up circuit, and the switching groups, S5, S6, S7, S8, and other elements constitute an inverter circuit.

[0040] Alternatively, in order to realize automatic configuration of the configurable input interface and the configurable output interface, current sampling and voltage sampling of the configurable input interface and the configurable output interface can be performed; and the parameters of the input power supply and the

parameters of the load are determined according to the current sampling and the voltage sampling. The parameters of the power supply and the parameters of the load include: DC/AC type of the power supply and the load, and whether the voltage required by the load needs
5 step-up/step-down.

[0041] In addition, for a traditional power conversion apparatus, since its input and output have fixed configurable properties, it can be only provided that the input is connected to AC or DC and the output is AC or DC; and the input end and output end are strictly
10 configured and cannot be used universally, and a great number of skilled technicians are required to guide field installation and debugging. However, with respect to the power conversion apparatus with the circuit configuration shown in Fig. 2 of this embodiment, since the left and right circuit configuration thereof is completely
15 consistent, its input end and output end do not need to be defined compulsorily, and can be used universally.

[0042] Through the implementations described above, those skilled in the art can clearly understand that the method according to the foregoing embodiments can be implemented by means of software plus
20 necessary general hardware platforms, and of course, can also be implemented by means of hardware, but in many cases, the former is a better implementation. Based on such understanding, the technical solutions of the present disclosure in essence, or its part contributing to the prior art can be embodied in the form of a software
25 product, and the computer product is stored in a storage medium (such as a ROM/RAM, a magnetic disk, and an optical disk), and contains certain instructions for enabling a terminal device (which can be

a mobile phone, a computer, a server, or a network device, etc.) to perform the method according to the embodiments of the present disclosure.

[0043] In addition, the power conversion method according to the
5 embodiments of the present disclosure described in conjunction with
Fig. 5 can be implemented by a power conversion device. Fig. 6 shows
a schematic hardware diagram of a power conversion device provided
according to an embodiment of the present disclosure. As shown in
Fig. 6, the power conversion device can include a processor 61 and
10 a memory 62 having stored thereon computer program instructions.

[0044] Specifically, the processor 61 can include a Central
Processing Unit (CPU), or an Application Specific Integrated Circuit
(ASIC), or can be configured to be one or more integrated circuits
to implement the embodiments of the present disclosure.

15 **[0045]** The memory 62 can include a mass memory to **store** data or
instructions. By way of example but not limitation, the memory 62
can include a Hard Disk Drive (HDD), a floppy Disk Drive, a flash
memory, an optical disk, a magneto-optical disk, a magnetic tape,
or a Universal Serial Bus (USB) Drive or a combination of two or more
20 of these. The memory 62 can include removable or non-removable (or
fixed) media, where appropriate. The memory 62 can be internal or
external to a data processing apparatus, where appropriate. In a
particular embodiment, the memory 62 is a non-volatile solid-state
memory. In a particular embodiment, the memory 62 includes a Read
25 Only Memory (ROM). Where appropriate, the ROM can be a mask-programmed
ROM, Programmable ROM (PROM), Erasable PROM (EPROM), Electrically
Erasable PROM (EEPROM), Electrically Alterable ROM (EAROM), or flash

memory or a combination of two or more of these.

[0046] The processor 61 reads and executes the computer program instructions stored in the memory 62, to implement any of the power conversion methods in the above embodiments.

5 **[0047]** In one example, the power conversion device can further comprise a communication interface 63 and a bus 60. As shown in Fig. 6, the processor 61, the memory 62, and the communication interface 63 connect and communicate with each other via the bus 60.

[0048] The communication interface 63 is mainly arranged to implement
10 communication between modules, apparatuses, units and/or devices in the embodiments of the present disclosure.

[0049] The bus 60 includes hardware, software, or both, and couples components of the power conversion device with each other. By way of example but not limitation, a bus can include an Accelerated
15 Graphics Port (AGP) or other graphics bus, an Enhanced Industrial Standard Architecture (EISA) bus, a Front Side Bus (FSB), a Hyper Transport (HT) interconnect, an Industrial Standard Architecture (ISA) bus, an InfiniBand interconnect, a Low Pin Count (LPC) bus, a memory bus, a Micro Channel Architecture (MCA) bus, a Peripheral
20 Component Interconnect (PCI) bus, a PCI-Express (PCI-X) bus, a Serial Advanced Technology Attachment (SATA) bus, a Video Electronics Standards Association Local (VLB) bus, or other suitable buses, or a combination of two or more of these. The bus 60 can include one or more buses, where appropriate. Any suitable bus or interconnect
25 will be considered in this disclosure, although a particular bus is described and illustrated in the embodiments thereof.

[0050] The power conversion device can perform the power conversion

method in the embodiments of the present disclosure based on the acquired data, thereby implementing the power conversion method described in conjunction with Fig. 5.

[0051] In addition, in combination with the power conversion method
5 in the foregoing embodiments, the embodiments of the present disclosure can provide a computer-readable storage medium for implementing the method. The computer readable storage medium has stored thereon computer program instructions, which, when executed by a processor, implement the power conversion method in any of the
10 above embodiments.

[0052] The above description only describes the preferred embodiments of the present disclosure and is not intended to limit the present disclosure. For those skilled in the art, various modifications and variations can be made to the present disclosure.
15 Any modification, equivalent substitution, improvement, etc., made within the spirit and principle of the present disclosure should be contained within the protection scope of the present disclosure.

CLAIMS

1. A power conversion apparatus comprising a configurable input interface, a power conversion circuit, and a configurable output interface, wherein:

the configurable input interface is arranged to configure a first electrical connection of an input power supply and the power conversion circuit, and is electrically connected with the input power supply and the power conversion circuit;

the configurable output interface is arranged to configure a second electrical connection of a load and the power conversion circuit, and is electrically connected with the load and the power conversion circuit; and

the power conversion circuit is arranged to perform corresponding power conversion according to parameters of the input power supply and parameters of the load,

wherein the power conversion circuit comprises input ends IN1, IN2, IN3, and IN4, output ends OUT1, OUT2, OUT3, and OUT4, switching groups S1, S2, S3, S4, S5, S6, S7, and S8, inductors L1 and L2, and a capacitor C, wherein:

the input end IN1 is electrically connected with the output end OUT1;

the input end IN4 is electrically connected with the output end OUT4;

the switching groups S1 and S2 that are connected in series in a same direction, the switching groups S3 and S4 that are connected in series in a same direction, the capacitor C, the switching groups

S5 and S6 that are connected in series in a same direction, and the switching groups S7 and S8 that are connected in series in a same direction are connected in parallel between the input ends IN1 and IN4;

the inductor L1 is connected in series between the input end IN2 and a series connection node of the switching groups S1 and S2;

the inductor L2 is connected in series between the output end OUT2 and a series connection node of the switching groups S5 and S6;

the input end IN3 is electrically connected with a serial connection node of the switching groups S3 and S4;

the output end OUT3 is electrically connected with a serial connection node of the switching groups S7 and S8; and

each of the switching groups includes a triode and a diode that are connected in anti-parallel.

2. The apparatus according to claim 1, wherein power conversion performed by the power conversion circuit comprises at least two of the following:

DC/DC step-up conversion, DC/DC step-down conversion, AC/DC conversion, DC/AC conversion, AC/AC conversion, or DC/DC step-up conversion first and then DC/AC conversion.

3. The apparatus according to claim 1, wherein the configurable input interface comprises single-pole double-throw switches K1 and

K2, wherein:

an immovable end K1-1 of the single-pole double-throw switch K1 and an immovable end K2-1 of the single-pole double-throw switch K2 are respectively arranged to be electrically connected with the input power supply;

movable ends K1-2 and K1-3 of the single-pole double-throw switch K1, and movable ends K2-2 and K2-3 of the single-pole double-throw switch K2 are respectively arranged to be electrically connected with four input ends of the power conversion circuit; and

the movable ends K1-2, K1-3, K2-2 and K2-3 are electrically connected with the input ends IN1, IN2, IN3 and IN4 of the power conversion circuit, respectively.

4. The apparatus according to claim 1, wherein the configurable output interface comprises single-pole double-throw switches K3 and K4, wherein:

an immovable end K3-1 of the single-pole double-throw switch K3 and an immovable end K4-1 of the single-pole double-throw switch K4 are respectively arranged to be electrically connected with the load;

movable ends K3-2 and K3-3 of the single-pole double-throw switch K3, and movable ends K4-2 and K4-3 of the single-pole double-throw switch K4 are respectively arranged to be electrically connected with four output ends of the power conversion circuit; and

the movable ends K3-2, K3-3, K4-2 and K4-3 are electrically connected with the output ends OUT1, OUT2, OUT3 and OUT4 of the power conversion circuit, respectively.

5. The apparatus according to claim 1, wherein the power conversion circuit further comprises: a PWM control unit, arranged to control on/off of the triodes in the switching groups according to rules corresponding to the parameters of the input power supply and the parameters of the load.

6. A power conversion method, performed by the power conversion apparatus according to any of claims 1 to 5, comprising:

configuring single-pole double-throw switches K1, K2, K3, and K4, according to parameters of an input power supply and parameters of a load; and

controlling on/off of triodes in switching groups according to rules corresponding to the parameters of the input power supply and the parameters of the load.

7. The method according to claim 6, wherein under the condition that the input power supply is a DC power supply, the load is a DC load, and voltage required by the load is higher than that of the input power supply, the method comprises:

configuring an immovable end K1-1 of the single-pole double-throw switch K1 to be connected with a movable end K1-3, an immovable end K2-1 of the single-pole double-throw switch K2

to be connected with a movable end K2-3, an immovable end K3-1 of the single-pole double-throw switch K3 to be connected with a movable end K3-2, and an immovable end K4-1 of the single-pole double-throw switch K4 to be connected with a movable end K4-3; and

controlling locking of the triodes in the switching groups S1, S3, S4, S5, S6, S7 and S8, and controlling on/off of the triodes in the switching group S2 according to a preset frequency.

8. The method according to claim 6, wherein under the condition that the input power supply is a DC power supply, the load is a DC load, and voltage required by the load is lower than that of the input power supply, the method comprises:

configuring an immovable end K1-1 of the single-pole double-throw switch K1 to be connected with a movable end K1-2, an immovable end K2-1 of the single-pole double-throw switch K2 to be connected with a movable end K2-3, an immovable end K3-1 of the single-pole double-throw switch K3 to be connected with a movable end K3-3, and an immovable end K4-1 of the single-pole double-throw switch K4 to be connected with a movable end K4-3; and

controlling locking of the triodes in the switching groups S1, S2, S3, S4, S6, S7 and S8, and controlling on/off of the triodes in the switching group S5 according to a preset frequency.

9. The method according to claim 6, wherein under the condition

that the input power supply is an AC power supply and the load is a DC load, the method comprises:

configuring an immovable end K1-1 of the single-pole double-throw switch K1 to be connected with a movable end K1-3, an immovable end K2-1 of the single-pole double-throw switch K2 to be connected with a movable end K2-2, an immovable end K3-1 of the single-pole double-throw switch K3 to be connected with a movable end K3-2, and an immovable end K4-1 of the single-pole double-throw switch K4 to be connected with a movable end K4-3; and

controlling locking of the triodes in the switching groups S5, S6, S7 and S8, and controlling on/off of the triodes in the switching groups S1, S2, S3 and S4 according to a preset frequency.

10. The method according to claim 6, wherein under the condition that the input power supply is a DC power supply and the load is an AC load, the method comprises:

configuring an immovable end K1-1 of the single-pole double-throw switch K1 to be connected with a movable end K1-2, an immovable end K2-1 of the single-pole double-throw switch K2 to be connected with a movable end K2-3, an immovable end K3-1 of the single-pole double-throw switch K3 to be connected with a movable end K3-3, and an immovable end K4-1 of the single-pole double-throw switch K4 to be connected with a movable end K4-2; and

controlling locking of the triodes in the switching groups S1,

S2, S3 and S4, and controlling on/off of the triodes in the switching groups S5, S6, S7 and S8 according to a preset frequency.

11. The method according to claim 6, wherein under the condition that the input power supply is an AC power supply and the load is an AC load, the method comprises:

configuring an immovable end K1-1 of the single-pole double-throw switch K1 to be connected with a movable end K1-3, an immovable end K2-1 of the single-pole double-throw switch K2 to be connected with a movable end K2-2, an immovable end K3-1 of the single-pole double-throw switch K3 to be connected with a movable end K3-3, and an immovable end K4-1 of the single-pole double-throw switch K4 to be connected with a movable end K4-2; and

controlling on/off of the triodes of the switching groups S1, S2, S3, S4, S5, S6, S7 and S8 according to a preset frequency.

12. The method according to claim 6, wherein under the condition that the input power supply is a DC power supply, the load is an AC load, and voltage required by the load is step-up, the method comprises:

configuring an immovable end K1-1 of the single-pole double-throw switch K1 to be connected with a movable end K1-3, an immovable end K2-1 of the single-pole double-throw switch K2 to be connected with a movable end K2-3, an immovable end K3-1 of the single-pole double-throw switch K3 to be connected with a

movable end K3-3, and an immovable end K4-1 of the single-pole double-throw switch K4 to be connected with a movable end K4-2; and

controlling locking of the triodes in the switching groups S1, S3 and S4, and controlling on/off of the triodes in the switching group S2, S5, S6, S7 and S8 according to a preset frequency.

13. The method according to any of claims 6 to 12, further comprising:

performing current sampling and voltage sampling of the configurable input interface and the configurable output interface; and

determining the parameters of the input power supply and the parameters of the load according to the current sampling and the voltage sampling.

14. A power conversion device comprising: at least one processor, at least one memory, and computer program instructions stored in the memory, which when executed by the processor, implement the power conversion method according to any of claims 6 to 13.

15. A computer-readable storage medium having stored thereon computer program instructions, which when executed by a processor, implement the power conversion method according to any of claims 6 to 13.