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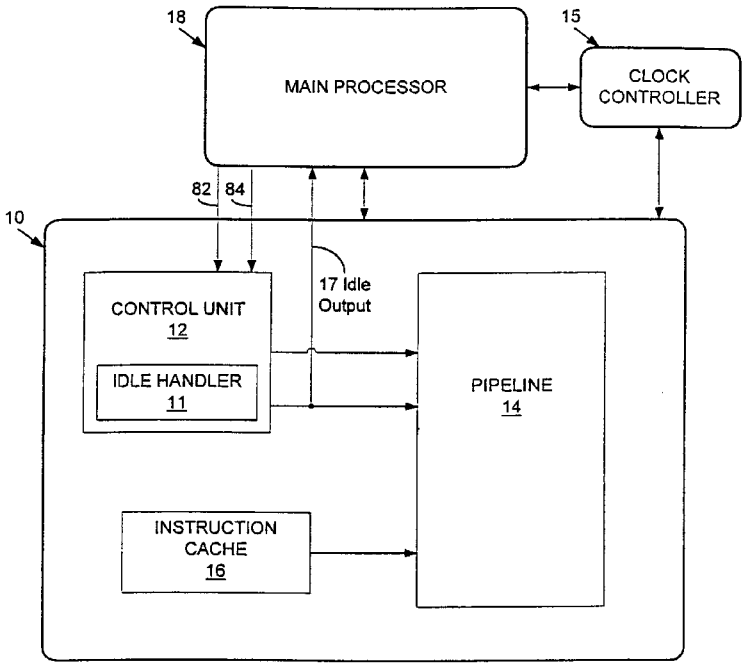
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(54) Title: PROCESSOR IDLE STATE




(57) Abstract: In one embodiment, a method is described herein for placing a programmable processor in a low-power idle state and returning the processor to an active state.

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