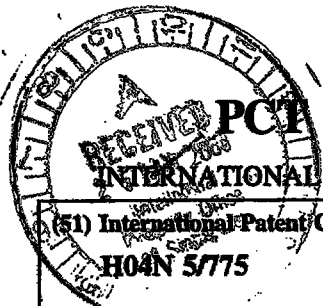




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(71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V.
[NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven
(NL).

(71) Applicant (for SE only): PHILIPS AB [SE/SE]; Kottbygatan 7,
Kista, S-164 85 Stockholm (SE).

(72) Inventors: UITTENBOGAARD, Teunis, H.; Prof. Holstlaan
6, NL-5656 AA Eindhoven (NL). BOEKESTEIN, Peter;
Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). VAN
DER WIJST, Hendricus, M.; Prof. Holstlaan 6, NL-5656
AA Eindhoven (NL).

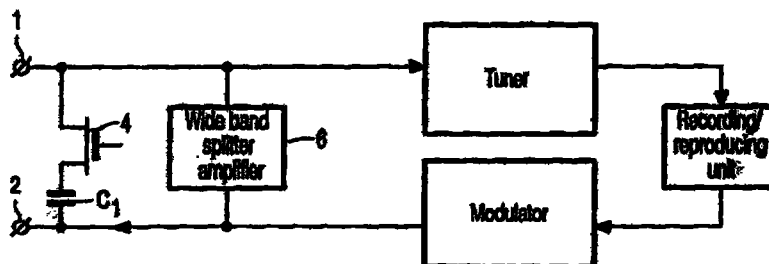
(74) Agent: VAN DER KRUK, Willem, L.; Prof. Holstlaan 6,
NL-5656 AA Eindhoven (NL).

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(57) Abstract

A video recorder/reproducer apparatus is provided with an antenna input terminal (1), an antenna output terminal (2), a splitter circuit (6) having an input coupled to the antenna input terminal and an output coupled to the antenna output terminal, a tuner circuit (tuner) having an input coupled to the antenna input terminal, and an output coupled to a recording/reproducing unit. A modulator unit is present, having an input coupled to the output of the recording/reproducing unit and an output which is coupled to the antenna output terminal (2). Supply voltage generator means (38) for generating a supply voltage for at least the splitter circuit (6) is present. In accordance with the invention, switching means (4, 8) are present having a first terminal coupled to the antenna input terminal (1) and a second terminal coupled to the antenna output terminal (2). The switching means realize an internal connection between its two terminals in response to the supply voltage generator means (38) being switched off and for realizing an open connection between its two terminals in response to the supply voltage generator means being switched on. The switching means comprise a FET (4), having its first and second main electrodes coupled to the first and second terminal, respectively, of the switching means.