

~~CLAIMS.~~

1. A textile motherboard that consists of at least one substrate layer on which at least one peripheral is incorporated and one central processing unit, wherein
5 the at least one peripheral is conformed of a textile switch, which consists of an isolating substrate with regions of null conductivity and regions of maximum conductivity.
- 10 2. Textile motherboard according to claim 1, wherein the textile motherboard incorporates a number of switches in a predetermined arrangement, which follows a pattern of logical gates.
- 15 3. Textile motherboard according to claim 2, wherein both the number and interconnections of textile switches are based on combinational logic.
- 20 4. Textile motherboard according to claim 3, wherein the number of switches is selected by employing the expression $2^N - 1$, where N is the number of available inputs.

~~5. Textile motherboard according to claim 4, wherein the~~
regions of maximum conductivity of the textile switch
are arranged following specific patterns, which
correspond to the perforations of the isolating
substrate.

6. Textile motherboard according to claim 4, wherein the
regions of minimum conductivity of the textile switch
are arranged following specific patterns.

7. A system to stimulate learning that consists of a
textile motherboard according to any of the claims 1
to 6, a textile intercommunications module, and an
electronic device configured to receive signals coming
from the textile motherboard, when the latter is
operated by a user.

8. System according to claim 7, wherein the textile
motherboard is incorporated into a garment and the
switches enable the command of the device.

9. A system to monitor that consists of a textile
motherboard according to any of the claims 1 to 6,
wherein the substrate is configured as a garment or a
bedroom textile or a bed linen.

10. A textile switch that consists of at least one
conductive textile substrate material and an isolating
substrate material, wherein the isolating material
5 incorporates patterns to enable regions of maximum
conductivity and regions of null conductivity.

11. Textile switch according to claim 10, wherein the
conductive substrate and the regions of maximum
10 conductivity of the isolating substrate coincide in
number.

12. Textile switch according to claim 11, wherein the
textile switch incorporates an additional conductive
15 substrate.

13. Textile switch according to claim 12, wherein the
isolating substrate is placed between the conductive
substrates.

14. Textile switch according to claim 13, wherein the
first substrate incorporates more than one conductive
20 region.

CLAIMS

1. A textile motherboard that consists of at least one textile substrate layer, on which at least one textile peripheral and one interchangeable central processing unit are incorporated, wherein the at least one textile peripheral comprises regions of null conductivity and regions of maximum conductivity;

the textile motherboard additionally consists of at least one textile peripheral configured as a first logic gate interconnected to another textile peripheral configured as a second logic gate; each of the peripherals is conformed of at least one pole and one throw, and the peripherals are configured such that the central processing unit may determine which peripheral has been activated by a user.

2. Textile motherboard according to claim 1, wherein both the number and interconnections of textile peripherals are based on combinational logic.

3. Textile motherboard according to claim 1, wherein the textile peripherals are textile switches.

4. Textile motherboard according to claim 2, wherein the number of textile switches is selected by employing the expression $2^N - 1$, where N is the number of available inputs, as well as the number of poles and throws.

5. Textile motherboard according to claim 4, wherein the regions of maximum conductivity of the textile switch correspond to the perforations of an isolating substrate.

6. Textile motherboard according to claim 5, wherein the regions of maximum conductivity are arranged following specific patterns.

7. A system to stimulate learning that consists of a textile motherboard according to any of the claims 1 to 6, a textile intercommunications module, and an electronic device configured to receive signals coming from the textile motherboard, when the latter is operated by a user.

8. System according to claim 7, wherein the textile motherboard is incorporated into a garment and the switches enable the command of the device.

9. A system to monitor that consists of a textile motherboard according to any of the claims 1 to 6, wherein the substrate is configured as a garment or a bedroom textile or a bed linen.

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10. A textile switch that consists of at least one conductive textile substrate material and an isolating substrate material, wherein the isolating material incorporates patterns to enable regions of maximum conductivity and regions of null conductivity, incorporates at least one pole and one throw, and may be configured as a logical gate.

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11. Textile switch according to claim 10, wherein the conductive substrate and the regions of maximum conductivity of the isolating substrate coincide in number.

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12. Textile switch according to claim 11, wherein the textile switch incorporates an additional conductive substrate.

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13. Textile switch according to claim 12, wherein the isolating substrate is placed between the conductive substrates.

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14. Textile switch according to claim 13, wherein the first substrate incorporates more than one conductive region.