CLAIMS

1. A chip module, comprising a plurality of first ports and a first control module, wherein

at least some of the plurality of first ports are first selection ports, and the first selection port acts as a read port or a write port; and

the first control module controls, based on a transmit/receive requirement of the chip module, the first selection port to be switched to a read port or a write port, to match the transmit/receive requirement of the chip module.

wherein the first control module is further configured to: obtain a required ratio of write ports to read ports as a target ratio based on the transmit/receive requirement of the chip module, and determine a current ratio of write ports to read ports in the current chip module; and

when the current ratio is different from the target ratio, the first control module is configured to control, based on a result of comparison between the target ratio and the current ratio, the first selection port to be switched to a read port or a write port, until the ratio of read ports to write ports of the chip module is consistent with the target ratio.

2. The chip module according to claim 1, wherein the first control module is further configured to: obtain a required quantity and locations of write ports and a required quantity and locations of read ports based on the transmit/receive requirement of the chip module; and compare the required quantity and the locations of write ports and the required quantity and the locations of read ports with a current quantity and locations of write ports and a current quantity and locations of read ports, to determine a first selection port that needs to be switched.

3. The chip module according to claim 2, wherein the first control module is further configured to: determine the first selection port that needs to be switched, and switch the determined first selection port based on the transmit/receive requirement of the chip module.

4. The chip module according to claim 3, wherein the first control module is further configured to: when the current ratio is less than the target ratio, control at least some of the first selection ports acting as read ports to be switched to write ports; or

when the current ratio is greater than the target ratio, control at least some of the first selection ports acting as write ports to be switched to read ports.

5. The chip module according to any one of claims 1 to 4, wherein each first selection port comprises: a first connection port, a first receiving circuit, a first sending circuit, and a first selector switch; the first connection port is connected to one of the first receiving circuit and the first sending circuit by using the first selector switch;

when the first receiving circuit is connected to the first connection port, the first selection port acts as a write

port; and

when the first sending circuit is connected to the first connection port, the first selection port acts as a read port.

6. The chip module according to any one of claims 1 to 5, wherein the first control module is further configured to: send a switching indication message to a peer chip module paired with the chip module, and receive a switching completion indication message fed back by the peer chip module.

7. A communication system, comprising a first chip module and a second chip module, wherein the first chip module is the chip module according to any one of claims 1 to 6, wherein

first ports of the first chip module and second ports of the second chip module are connected in a one-to-one correspondence.

8. The communication system according to claim 7, wherein the second chip module

comprises a plurality of second ports and a second control module, wherein

at least some of the plurality of second ports are second selection ports; and

the second control module controls, based on a transmit/receive requirement of a peer chip module paired with the chip module, the second selection port to be switched to a read port or a write port;.

the first ports of the first chip module and second ports of the second chip module are connected in a one-to-one correspondence.

9. A port allocation method, comprising at least the following steps:

obtaining a transmit/receive requirement of a chip module; and

switching a first selection port of the chip module to a read port or a write port based on the obtained transmit/receive requirement of the chip module, wherein the switching a first selection port to a read port or a write port based on the obtained transmit/receive requirement of the chip module is specifically:

obtaining a required ratio of write ports to read ports as a target ratio based on the transmit/receive requirement of the chip module;

determining a current ratio of write ports to read ports in the current chip module; and

when the current ratio is different from the target ratio, controlling, based on a result of comparison between the target ratio and the current ratio, the first selection port to be switched to a read port or a write port, until the ratio of read ports to write ports of the chip module is consistent with the target ratio.

10. The port allocation method according to claim 9, further comprising:

determining a first selection port that needs to be switched, and switching the determined first selection port based on the transmit/receive requirement of the chip module.

11. The port allocation method according to claim 10, further comprising:

sending a switching indication message to a peer chip module paired with the chip module; and

receiving a switching completion indication message fed back by the peer chip module.

12. A mobile terminal, comprising the chip module according to any one of claims 1 to 6 and a peer chip module connected by using a signal.