Abstract

An interstitial line generator for an interlaced scan to non-interlaced scan video signal converter includes circuitry (52,54,56) for examining a plurality of lines spatially and temporally disposed about an interstitial line location and excludes (58,60,62, 64,66,70) signals having amplitudes representing the relative extremes of the plurality of lines. The remaining signals are combined (68) in predetermined proportions to generate the interstitial line.

This Sheet Has Been Replaced

