

DESCRIPTION

DUPLEXER, COMMUNICATION MODULE, AND COMMUNICATION DEVICE

Technical Field

5 [0001] The disclosure of the present application relates to a duplexer, a communication module, and a communication device.

Background Art

[0002] From the viewpoint of achieving high-capacity data communication at a
10 higher speed, there has been a trend in recent years in mobile terminals for high frequency communication toward an increase in the transmission signal power. In a mobile terminal, an increase in the signal power may result in deterioration of isolation of the duplexer. Theoretically, it is possible to improve the isolation of the duplexer by configuring a transmitting filter or receiving filter used in the duplexer to
15 have an increased attenuation in the opposite band (the receiving band for the transmitting filter and the transmitting band for the receiving filter). In reality, however, the isolation becomes saturated at a certain level. The reason for this is because not only a current that flows through the filters but also a current that flows through the duplexer via space serve as components having an influence on the
20 isolation. In order to achieve a high level of isolation, it is desirable to study measures against the current that flows through the duplexer via space.

[0003] Examples of effective configurations in removing the influence of the current that flows through the duplexer via space, in other words, the influence of electromagnetic coupling components include increasing the physical distance
25 between a source and a target to be coupled and placing an electromagnetic shielding structure. Patent Document 1 discloses a surface acoustic wave device in which a shield electrode is disposed between two surface acoustic wave filter patterns. Patent Document 2 discloses a surface acoustic wave device in which an input external terminal and an output external terminal are disposed apart from each other
30 diagonally.

Prior Art Documents

Patent Documents

[0004] Patent Document 1: JP 2006-60747 A

Patent Document 2: JP 2002-76829 A

5 Disclosure of Invention

Problem to be Solved by the Invention

[0005] In the case of the configuration disclosed in Patent Document 1, adequate improvements in the isolation may be achieved by increasing the size of the shielding member. However, an increase in the size of the shielding member may impair a downsizing of the duplexer. Further, in the case of the configuration disclosed in Patent Document 2, adequate improvements in the isolation may be achieved by increasing the distance between the input external terminal and the output external terminal. However, an increase in the distance between the terminals may impair a downsizing of the duplexer. That is, with the configurations disclosed in Patent Documents 1 and 2, it is difficult to achieve both the downsizing of the duplexer and improvements in the isolation.

Means for Solving Problem

[0006] The duplexer disclosed in the present application includes: an unbalanced transmitting filter; a balanced receiving filter; a transmitting port connected to the transmitting filter; a primary receiving port connected to the receiving filter; a secondary receiving port connected to the receiving filter; and an electrostatic coupling portion connected between the transmitting port and the primary receiving port and between the transmitting port and the secondary receiving port. The electrostatic coupling portion allows phase-adjusted electric power to flow from the transmitting port to the primary receiving port and from the transmitting port to the secondary receiving port.

Effects of the Invention

[0007] According to the disclosure of the present application, it is possible to achieve a simple duplexer with good isolation.

Brief Description of the Drawings

[0008] [FIG. 1] FIG. 1 is a block diagram of a duplexer.

[FIG. 2] FIG. 2 is a graph showing a phase difference between currents that flow to two receiving ports from a transmitting port.

5 [FIG. 3A] FIG. 3A is a graph showing SEI of the duplexer.

[FIG. 3B] FIG. 3B is a graph showing SEI of the duplexer.

[FIG. 3C] FIG. 3C is a graph showing DI of the duplexer.

[FIG. 4] FIG. 4 is a block diagram schematically showing the occurrence of an electromagnetic coupling in a duplexer.

10 [FIG. 5A] FIG. 5A is a graph showing electric power transmission characteristics of the paths from the transmitting port to the two receiving ports in terms of phase.

[FIG. 5B] FIG. 5B is a graph showing electric power transmission characteristics of the paths from the transmitting port to the two receiving ports in
15 terms of phase difference.

[FIG. 6A] FIG. 6A is a graph showing DI of the duplexer.

[FIG. 6B] FIG. 6B is a graph showing SEI of the duplexer.

[FIG. 6C] FIG. 6C is a graph showing SEI of the duplexer.

[FIG. 7] FIG. 7 is a graph showing a phase difference between currents that
20 flow to the two receiving ports from the transmitting port.

[FIG. 8A] FIG. 8A is a graph showing SEI from the transmitting port and the receiving port.

[FIG. 8B] FIG. 8B is a graph showing SEI from the transmitting port and the receiving port.

25 [FIG. 8C] FIG. 8C is a graph showing DI of the duplexer.

[FIG. 9] FIG. 9 is a circuit diagram of a ladder filter.

[FIG. 10] FIG. 10 is a circuit diagram of a double-mode filter.

[FIG. 11A] FIG. 11A is a plan view of a film bulk acoustic resonator.

[FIG. 11B] FIG. 11B is a cross-sectional view of the A-A portion in FIG. 11A.

30 [FIG. 12A] FIG. 12A is a plan view of an acoustic wave resonator.

[FIG. 12B] FIG. 12B is a cross-sectional view of the A-A portion in FIG. 12A.

[FIG. 13] FIG. 13 is a circuit diagram showing a specific configuration of the

duplexer.

[FIG. 14A] FIG. 14A is a graph showing electric power transmission characteristics of the paths from the transmitting port to the two receiving ports in terms of phase.

5 [FIG. 14B] FIG. 14B is a graph showing electric power transmission characteristics of the paths from the transmitting port to the two receiving ports in terms of phase difference.

[FIG. 15A] FIG. 15A is a graph showing DI of the duplexer.

[FIG. 15B] FIG. 15B is a graph showing SEI from the transmitting port to the
10 receiving port.

[FIG. 15C] FIG. 15C is a graph showing SEI from the transmitting port to the receiving port.

[FIG. 16] FIG. 16 is a schematic diagram showing a specific configuration of a ladder filter.

15 [FIG. 17] FIG. 17 is a schematic diagram showing a specific configuration of a double-mode filter.

[FIG. 18A] FIG. 18A is a schematic diagram showing a specific configuration of the upper layer of a duplexer.

[FIG. 18B] FIG. 18B is a schematic diagram showing a specific configuration
20 of the internal layer of the duplexer.

[FIG. 18C] FIG. 18C is a schematic diagram showing a specific configuration of the lower layer of the duplexer.

[FIG. 18D] FIG. 18D is a cross-sectional view of the duplexer shown in FIG. 18A.

25 [FIG. 19] FIG. 19 is a circuit diagram showing a specific circuit configuration of a duplexer.

[FIG. 20A] FIG. 20A is a graph showing electric power transmission characteristics of the paths from the transmitting port to the two receiving ports in terms of phase.

30 [FIG. 20B] FIG. 20B is a graph showing electric power transmission characteristics of the paths from the transmitting port to the two receiving ports in terms of phase difference.

[FIG. 21A] FIG. 21A is a graph showing DI of the duplexer.

[FIG. 21B] FIG. 21B is a graph showing SEI from the transmitting port to the receiving port.

5 [FIG. 21C] FIG. 21C is a graph showing SEI from the transmitting port to the receiving port.

[FIG. 22] FIG. 22 is a circuit diagram showing a specific configuration of a duplexer.

[FIG. 23A] FIG. 23A is a schematic diagram showing a specific configuration of the upper layer of the duplexer.

10 [FIG. 23B] FIG. 23B is a schematic diagram showing a specific configuration of the internal layer of the duplexer.

[FIG. 23C] FIG. 23C is a schematic diagram showing a specific configuration of the lower layer of the duplexer.

15 [FIG. 23D] FIG. 23D is a cross-sectional view of the duplexer shown in FIG. 18A.

[FIG. 24A] FIG. 24A is a schematic diagram showing a specific configuration of the upper layer of the duplexer.

[FIG. 24B] FIG. 24B is a schematic diagram showing a specific configuration of the first internal layer of the duplexer.

20 [FIG. 24C] FIG. 24C is a schematic diagram showing a specific configuration of the second internal layer of the duplexer.

[FIG. 24D] FIG. 24D is a schematic diagram showing a specific configuration of the lower layer of the duplexer.

25 [FIG. 25A] FIG. 25A is a schematic diagram showing a specific configuration of the upper layer of the duplexer.

[FIG. 25B] FIG. 25B is a schematic diagram showing a specific configuration of the first internal layer of the duplexer.

[FIG. 25C] FIG. 25C is a schematic diagram showing a specific configuration of the second internal layer of the duplexer.

30 [FIG. 25D] FIG. 25D is a schematic diagram showing a specific configuration of the lower layer of the duplexer.

[FIG. 26] FIG. 26 is a block diagram of a communication module.

[FIG. 27] FIG. 27 is a block diagram of a communication device.

Description of the Invention

[0009] (Embodiment 1)

5 [1. Configuration of Duplexer]

FIG. 1 is a block diagram of a balanced-unbalanced duplexer using film bulk acoustic resonators or acoustic wave resonators. As shown in FIG. 1, the duplexer includes a transmitting filter 1, a receiving filter 2, a transmitting port 3, and receiving ports 4a and 4b. The duplexer shown in FIG. 1 is of a
 10 balanced-unbalanced type and includes one transmitting port 3 and two receiving ports 4a and 4b.

[0010] As shown in FIG. 1, in the balanced-unbalanced duplexer, a current I_t inputted to the transmitting port 3 partially flows into an antenna port Ant and also flows into the receiving filter 2. The receiving filter 2 is of an unbalanced
 15 input-balanced output type, and is configured to phase-shift one of the balanced outputs by $+90^\circ$ and the other of the balanced outputs by -90° . Thus, a current I_1 that flows into the receiving port 4a and a current I_2 that flows into the receiving port 4b have a phase difference of 180° . Thus, to the receiving ports 4a and 4b, currents having opposite phases are outputted in the receiving band (hereinafter referred to as
 20 the Rx band) and in-phase currents are outputted in other frequency bands.

[0011] FIG. 2 shows the relationship between currents that flow into the two receiving ports 4a and 4b in the duplexer shown in FIG. 1. As shown in FIG. 2, currents having a phase difference of 180° are outputted from the receiving filter 2 in the Rx band.

[0012] FIG. 3A and 3B each show isolation with respect to a single path (hereinafter referred to as single end isolation, and SEI for short) of a duplexer having the current characteristic as shown in FIG. 2. FIG. 3A shows the isolation from the transmitting port 3 to the receiving port 4a. FIG. 3B shows the isolation from the transmitting port 3 to the receiving port 4b. FIG. 3C shows differential isolation (hereinafter
 25 referred to as DI for short) of the duplexer having the current characteristic as shown in FIG. 2. The differential isolation is obtained by synthesizing the isolations of the two paths with a balun. As shown in FIG. 3C, SEI and DI have a small level

difference in the Rx band where currents having opposite phases are outputted. The reason for this is that currents having opposite phases are outputted in the Rx band and they do not cancel each other even if they pass through a balun in the receiving filter 2. On the other hand, DI shows better characteristics than those of SEI in the Tx band where in-phase currents are outputted. The reason for this is that in-phase currents cancel each other in the differential operation, i.e., when they pass through the balun.

[0013] However, these are only ideal cases. In reality, a coupling may occur through space as described above. As described above, there are components that propagate to the receiving ports 4a and 4b from the transmitting port 3 due to an electrostatic/electromagnetic coupling. Hereinafter, such a electrostatic/electromagnetic coupling will be referred to as an “electromagnetic coupling.”

[0014] FIG. 4 is a diagram for explaining the principles of an electromagnetic coupling between the transmitting port 3 and the receiving port 4a and that between the transmitting port 3 and the receiving port 4b in the duplexer. As shown in FIG. 4, when the duplexer is in the operating state, an electromagnetic coupling occurs between the transmitting port 3 and the receiving port 4a spatially as indicated by the arrow A, so that a current I11 flows to the receiving port 4a from the transmitting port 3. Further, an electromagnetic coupling occurs between the transmitting port 3 and the receiving port 4b spatially as indicated by the arrow B, so that a current I12 flows to the receiving port 4b from the transmitting port 3. In this way, if the transmitting port 3 and the receiving port 4a and the transmitting port 3 and the receiving port 4b are electromagnetically coupled, the isolation deteriorates.

[0015] FIG. 5A shows electric power characteristics of the paths from the transmitting port 3 to the receiving ports 4a and 4b in the duplexer shown in FIG. 4 in terms of phase. FIG. 5B shows electric power characteristics of the paths from the transmitting port 3 to the receiving ports 4a and 4b in the duplexer shown in FIG. 4 in terms of phase difference. As shown in FIG. 5B, a phase difference between electric power transmitted from the transmitting port 3 to the receiving port 4a and electric power transmitted from the transmitting port 3 to the receiving port 4b is roughly 0° in the Tx band. However, in the Rx band, the phase difference deviates from 180° as

an ideal value. The phase difference deviation occurs in the Rx band because there is a possibility of an electromagnetic coupling between the transmitting port 3 and the receiving port 4a and an electromagnetic coupling between the transmitting port 3 and the receiving port 4b.

5 [0016] FIG. 6A shows DI characteristics of the duplexer shown in FIG. 4. FIG. 6B shows SEI characteristics between the transmitting port 3 and the receiving port 4a in the duplexer shown in FIG. 4. FIG. 6C shows SEI characteristics between the transmitting port 3 and the receiving port 4b in the duplexer shown in FIG. 4. When there is a possibility of an electromagnetic coupling between the transmitting port 3 and the receiving port 4a and an electromagnetic coupling between the transmitting port 3 and the receiving port 4b in the duplexer as shown in FIG. 4, DI (see FIG. 6A) deteriorates more so than SEI (see FIGS. 6B and 6C) in the Rx band.

[0017] A major characteristic of the present embodiment is to control currents that flow into the two receiving ports from the transmitting port such that the currents become close to in-phase (the phase difference is close to 0°) in the Rx band as shown in FIG. 7. FIG. 8A shows SEI from the transmitting port 3 to the receiving port 4a. FIG. 8B shows SEI from the transmitting port 3 to the receiving port 4b. FIG. 8C shows differential isolation (DI), which is obtained by synthesizing the isolations of the two paths with a balun. FIGS. 8A to 8C each show ideal characteristics. By adjusting currents to be in-phase in the Rx band, the level of DI (FIG. 8C) can be improved more so than that of SEI (FIGS. 8A, 8B) as shown in FIG. 7 even if the transmitting port and the two receiving ports are electromagnetically coupled spatially. That is, it is possible to improve the isolation in the Rx band as indicated by the arrow D in FIG. 8C as well as the isolation in the Tx band as indicated by the arrow C.

[0018] At this time, the phases in the Rx band in which SEI becomes better than DI are preferably 0° or 360° as perfect in-phase achieved by the principles of common-mode rejection by a balun, 0 to 90° as a range shifted from 0° by $1/4$ wavelength or 270 to 360° as a range shifted from 360° by $1/4$ wavelength.

30 [0019] (Example 1)

FIG. 9 is a circuit diagram of a transmitting filter included in the duplexer according to the present embodiment. The transmitting filter shown in FIG. 9

includes serial resonators RES1 to RES4, parallel resonators RES11 to RES14, and inductors L1 to L3 (given that the Q value is “10”). The serial resonators RES1 to RES4 are connected to serial arms. The parallel resonator RES11 is connected to a parallel arm connected to a node between the serial resonators RES1 and RES2.

5 The parallel resonator RES12 is connected to a parallel arm connected to a node between the serial resonators RES2 and RES3. The parallel resonator RES13 is connected to a parallel arm connected to a node between the serial resonators RES3 and RES4. The parallel resonator RES14 is connected to a parallel arm connected to a node between the serial resonator RES4 and an output terminal OUT. The
10 inductor L1 is connected to the parallel resonator RES11. The inductor L2 is connected to the parallel resonators RES12 and RES13. The inductor L3 is connected to the parallel resonator RES14. Surface acoustic wave resonators (SAW resonators) or film bulk acoustic resonators (FBARs) can be used to achieve the serial resonators RES1 to RES4 and the parallel resonators RES11 to RES14.

15 [0020] FIG. 10 is a circuit diagram of a receiving filter included in the duplexer according to the present embodiment. The receiving filter shown in FIG. 10 is an unbalanced input-balanced output double mode SAW (DMS) filter. The receiving filter shown in FIG. 10 includes an input resonator RES21 connected to an input terminal IN, an output resonator RES22 connected to an output terminal OUT1, an
20 output resonator RES23 connected to an output terminal OUT2, and reflectors REF21 and REF22. The input resonator RES21 is excited based on electric signals inputted to the input terminal IN, and oscillations of the input resonator RES21 propagate toward the output resonators RES22 and RES23, thereby oscillating the output resonators RES22 and RES23. As a result, electric signals are outputted
25 from the output terminals OUT1 and OUT2.

[0021] For example, film bulk acoustic resonators as shown in FIGS. 11A and 11B can be used to achieve the resonators. FIG. 11A is a plan view of a film bulk acoustic resonator. FIG. 11B is a cross-sectional view of the A-A portion in FIG. 11A. The film bulk acoustic resonator includes a substrate 11, an upper electrode 12, a lower
30 electrode 13, and a piezoelectric film 14. The upper electrode 12, the lower electrode 13, and the piezoelectric film 14 are laminated on the substrate 11. The substrate 11 includes a cavity 15 at a portion overlapping an area (membrane area) 16 in which at

least the upper electrode 12 and the lower electrode 13 oppose each other. As a result of this, the piezoelectric film 14 can oscillate.

[0022] For example, acoustic wave resonators as shown in FIG. 12A and 12B can be used to achieve the resonators. FIG. 12A is a plan view of an acoustic wave resonator.

5 FIG. 12B is a cross-sectional view of the A-A portion in FIG. 12A. The acoustic wave resonator includes a substrate 21, a pair of comb electrodes 22 (Interdigital Transducer : IDT), reflectors 23a and 23b, an input terminal 24 and an output terminal 25. The input terminal 24 is connected to one of the comb electrodes 22. The output terminal 25 is connected to the other of the comb electrodes 22. One of
10 the comb electrodes 22 is excited based on electric signals inputted to the input terminal 24. Oscillations of one of the comb electrodes 22 propagate to the other of the comb electrodes 22, and electric signals are outputted to the output terminal 25.

[0023] Note that the resonators are not limited to film bulk acoustic resonators and acoustic wave resonators, and other resonators such as acoustic boundary wave
15 resonators may be used to achieve the resonators.

[0024] FIG. 13 is a circuit diagram of the duplexer according to the present embodiment. The duplexer shown in FIG. 13 includes a transmitting filter 31, a receiving filter 32, a transmitting port 33, receiving ports 34a and 34b, an antenna port 35, and an electrostatic coupling portion 36. A ladder filter having the same
20 configuration as that shown in FIG. 9 is used as the transmitting filter 31. A DMS filter having the same configuration as that shown in FIG. 10 is used as the receiving filter 32. The electrostatic coupling portion 36 includes capacities C1 and C2. The capacities C1 and C2 are connected to each other in series, and they are connected between the receiving ports 34a and 34b. A node between the capacities C1 and C2
25 is connected to the transmitting port 33. The capacities C1 and C2 each are configured to have such a constant that electric power transmission characteristics of the path from the transmitting port 33 to the receiving port 34a and those of the path from the transmitting port 33 to the receiving port the 34b have a phase difference of 0 to 90° in the Rx band except at a phase inversion point as shown in FIGS. 14A and
30 14B. As an example, each constant in the present embodiment is set such that the phase difference becomes about 40° as shown in the Rx band in FIG. 14B. FIG. 14A shows electric power transmission characteristics of the paths from the transmitting

port 33 to the receiving port 34a and 34b in terms of phase. FIG. 14B shows electric power transmission characteristics of the paths from the transmitting port 33 to the receiving ports 34a and 34b in terms of phase difference.

[0025] By configuring the duplexer in this way, DI can have characteristics as shown in FIG. 15A and SEI can have characteristics as shown in FIGS. 15B and 15C. FIG. 15A shows DI of the duplexer shown in FIG. 13. FIG. 15B shows SEI from the transmitting port 33 to the receiving port 34a shown in FIG. 13. FIG. 15C shows SEI from the transmitting port 33 to the receiving port 34b shown in FIG. 13. A comparison between FIG. 15A and FIGS. 15B and 15C shows that DI is at a lower level than SEI in the Rx band.

[0026] FIG. 16 shows a specific example of the transmitting filter 31 included in the duplexer shown in FIG. 13. The transmitting filter shown in FIG. 16 includes, on a substrate 41, signal terminals 42a and 42b, ground terminals 43a, 43b, and 43c, a dummy pattern 44, serial resonators RES1 to RES4, and parallel resonators RES11 to RES14. For convenience of explanation, the same reference numerals as those in FIG. 9 are used to denote the serial resonators and the parallel resonators.

[0027] FIG. 17 shows a specific example of the receiving filter 32 included in the duplexer shown in FIG. 13. The receiving filter shown in FIG. 17 includes, on a substrate 51, signal terminals 52a, 52b, and 52c, ground terminals 53a, 53b, and 53c, and resonators RES 21, RES22, and RES23. The signal terminal 52a is a terminal for receiving electric signals, and the signal terminals 52b and 52c are terminals for outputting electric signals.

[0028] FIGS. 18A to 18D show a specific example of the duplexer. As shown in FIGS. 18A to 18D, the duplexer may have a form in which the transmitting filter and the receiving filter as described above are mounted face down on a multilayer ceramics substrate through the use of bumps. Specifically, FIG. 18A is a plan view showing the configuration of the upper layer of the ceramics substrate, FIG. 18B is a plan view showing the configuration of the internal layer of the ceramics substrate, and FIG. 18C is a plan view showing the configuration of the backside of the ceramics substrate. FIG. 18D is a cross-sectional view of the A-A portion in FIG. 18A.

[0029] As shown in FIG. 18A, the transmitting filter 40 and the receiving filter 50 are patterned on the upper layer 61. Of the transmitting filter 40, the same

components as those shown in FIG. 16 are denoted by the same reference numerals. Of the receiving filter 50, the same components as those shown in FIG. 17 are denoted by the same reference numerals. A signal terminal 42a of the transmitting filter 40 is connected to a through hole 61a via a signal line 61m. A signal terminal 42b of the transmitting filter 40 is connected to a through hole 61b via a signal line 61p. A ground terminal 43a of the transmitting filter 40 is connected to a through hole 61e. A ground terminal 43b of the transmitting filter 40 is connected to a through hole 61g. A ground terminal 43c of the transmitting filter 40 is connected to a through hole 61f. A signal terminal 52a of the receiving filter 50 is connected to the through hole 61a via a signal line 61n. A signal terminal 52b of the receiving filter 50 is connected to a through hole 61d via a signal line 61r. A signal terminal 52c of the receiving filter 50 is connected to a through hole 61c via a signal line 61q. A ground terminal 53a of the receiving filter 50 is connected to a through hole 61h. The through hole 61a is connected to an antenna terminal 63a on a lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61b is connected to a transmitting port 63b on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61c is connected to a receiving port 63c on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61d is connected to a receiving port 63d on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61e is connected to a ground terminal 63e on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61f is connected to the ground terminal 63f on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61g is connected to a ground terminal 63g on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B. The through hole 61h is connected to a ground terminal 63h on the lower layer 63 shown in FIG. 18C through the internal layer 62 shown in FIG. 18B.

[0030] (Example 2)

FIG. 19 is a circuit diagram showing the configuration of a duplexer of Example 2 according to the present embodiment. Since the basic components shown in FIG. 19 can be the same as those in FIG. 13, the description thereof will not be

repeated. The capacities C1 and C2 have equal constants. In the present embodiment, the capacities C1 and C2 each have a constant of, for example, 5pF. The term “equal constants” as used herein means that not only the capacities C1 and C2 have completely the same constant but also have constants different in some

5 degree due to production variation or the like, so that their constants are not limited to completely coinciding constants. In this way, when the capacities C1 and C2 are configured to have equal constants, electric power transmission characteristics of the paths from the transmitting port to the two receiving ports have a phase difference of about 10 to 20° in the Rx band except at a phase inversion point as shown in FIGS.

10 20A and 20B. Thus, the phase relation more idealistic than the phase relation shown in FIG. 14B can be achieved. FIG. 20A shows electric power transmission characteristics of the paths from the transmitting port 33 to the receiving ports 34a and 34b in terms of phase. FIG. 20B shows electric power transmission characteristics of the paths from the transmitting port 33 to the receiving ports 34a

15 and 34b in terms of phase difference.

[0031] By configuring the duplexer in this way, DI will have characteristics as shown in FIG. 21A and SEI will have characteristics as shown in FIGS. 21B and 21C. FIG. 21A shows DI of the duplexer shown in FIG. 19. FIG. 21B shows SEI from the transmitting port 33 to the receiving port 34a shown in FIG. 19. FIG. 21C shows

20 SEI from the transmitting port 33 to the receiving port 34b shown in FIG. 19. A comparison between FIG. 21A and FIGS. 21B and 21C shows that DI is at a lower level than SEI in the Rx band.

[0032] (Example 3)

FIG. 22 is a circuit diagram showing the configuration of a duplexer of

25 Example 3 according to the present embodiment. The duplexer shown in FIG. 22 includes a transmitting filter 31, a receiving filter 32, a transmitting port 33, receiving ports 34a and 34b, an antenna port 35 and an electromagnetic coupling portion 36. A ladder filter having the same configuration as that shown in FIG. 9 can be used as the transmitting filter 31. A DMS filter having the same configuration as that shown in

30 FIG. 10 can be used as the receiving filter 32. The electromagnetic coupling portion 36 includes inductors L31, L32, and L33. The inductor L31 is connected between the receiving filter 32 and the receiving port 34a. The inductor L32 is connected between

the receiving filter 32 and the receiving port 34b. The inductor L33 is connected between the transmitting port 33 and the ground. The inductors L31 and L32 are disposed adjacent to each other and are electromagnetically coupled to develop a capacity in the space therebetween. The inductors L32 and L33 are disposed
 5 adjacent to each other and are electromagnetically coupled to develop a capacity in the space therebetween. The electromagnetic coupling portion 36 is configured to have such a constant that the paths from the transmitting port 33 to the two receiving ports 34a and 34b have a phase difference of roughly 0 to 90° in the Rx band. By configuring the duplexer in this way, DI can be at a lower level than SEI in the Rx
 10 band.

[0033] (Example 4)

FIGS. 23A to 23D are schematic diagrams showing the specific configuration of the duplexer according to the present embodiment. In FIGS. 23A to 23D, the same components as those shown in FIGS. 18A to 18D are denoted by the same reference
 15 numerals, and the description thereof will not be repeated.

[0034] The duplexer of Example 4 includes a conductor 64. As shown in FIG. 23A, the conductor 64 is disposed along the perimeter of the upper layer 61 of the ceramics substrate in a loop. As shown in FIG. 23B, the conductor 64 is connected to through holes 61b, 61c and 61d in the internal layer 62 of the ceramics substrate. Since the
 20 through hole 61b is connected to the transmitting port 63b shown in FIG. 23C, the through hole 61c is connected to the receiving port 63c shown in FIG. 23C, and the through hole 61d is connected to the receiving port 63d shown in FIG. 23C, the transmitting port 33 and the receiving port 34a and the transmitting port 33 and the receiving port 34b can be electrostatically coupled.

[0035] In this way, by simply adding the conductor 64 to the duplexer shown in FIGS. 18A to 18D, it is possible to achieve the duplexer according to the present embodiment shown in FIG. 13 and the like. That is, this duplexer can be achieved without making significant changes to the structure of the duplexer shown in FIGS. 18A to 18D. Thus, the duplexer can be achieved without substantially increasing the size.

[0036] In Example 4, the conductor 64 is provided on the upper layer 61 of the ceramics substrate. However, the conductor 64 may be formed on the internal layer as shown in FIGS. 24A to 24D. FIG. 24A is a plan view of the upper layer 61 of the
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ceramics substrate. FIG. 24B is a plan view of a first internal layer 62. FIG. 24C is a plan view of a second internal layer 65. FIG. 24D is a plan view of the lower layer 63. In the ceramics substrate, the upper layer 61, the first internal layer 62, the second internal layer 65 and the lower layer 63 are laminated in this order. As shown in FIG. 24B, the conductor 64 is disposed close to the perimeter of the first internal layer 62. Further, as shown in FIG. 24C, the conductor 64 is connected to the through holes 61b, 61c and 61d. Since the through hole 61b is connected to the transmitting port 63b shown in FIG. 24D, the through hole 61c is connected to the receiving port 63c shown in FIG. 24D, and the through hole 61d is connected to the receiving port 63d shown in FIG. 24D, the transmitting port 33 and the receiving port 34a and the transmitting port 33 and the receiving port 34b can be electrostatically coupled.

[0037] Further, the conductor 64 may be partially grounded. Further, a part of the conductor 64 may be on a different layer from the layer on which other part is disposed. Further, the conductor 64 may form an additional electrostatic coupling at least one part of the paths from the transmitting port 33 to the receiving ports 34a and 34b.

[0038] As long as the transmitting port 33 and the receiving port 34a and the transmitting port 33 and the receiving port 34b can be electrostatically coupled, the conductor 64 does not need to be in a loop. FIG. 25A is a plan view of the upper layer 61 of the ceramics substrate. FIG. 25B is a plan view of the first internal layer 62. FIG. 25C is a plan view of the second internal layer 65. FIG. 25D is a plan view of the lower layer 63. In the ceramics substrate, the upper layer 61, the first internal layer 62, the second internal layer 65 and the lower layer 63 are laminated in this order. As shown in FIG. 25B, a conductor 66 is disposed substantially in an L shape near a part of the perimeter of the first internal layer 62. As shown in FIG. 25C, the conductor 66 is connected to the through holes 61b, 61c and 61d. The through hole 61b is connected to the transmitting port 63b shown in FIG. 25D, the through hole 61c is connected to the receiving port 63c shown in FIG. 25D, and the through hole 61d is connected to the receiving port 63d shown in FIG. 25D. Thus, the transmitting port 33 and the receiving port 34a and the transmitting port 33 and the receiving port 34b can be electrostatically coupled.

[0039] [2. Configuration of Communication Module]

FIG. 26 shows an example of a communication module including the duplexer according to the present embodiment. As shown in FIG. 26, a duplexer 92 includes a receiving filter 92a and a transmitting filter 92b. Further, to the receiving filter 92a, receiving terminals 93a and 93b compatible with balanced output are connected, for example. Further, the transmitting filter 92b is connected to a transmitting terminal 95 through a power amplifier 94. Here, the duplexer according to the present embodiment can be used as the duplexer 92.

[0040] Of reception signals inputted through an antenna terminal 91, only signals in a certain frequency band are allowed to pass through the receiving filter 92a during the reception operation, and they are outputted externally from the receiving terminals 93a and 93b. Further, of transmission signals inputted from the transmitting terminal 95 and amplified by the power amplifier 94, only signals in a certain frequency band are allowed to pass through the transmitting filter 92b during the transmission operation, and they are outputted externally from the antenna terminal 91.

[0041] By incorporating the duplexer according to the present embodiment into the communication module, the communication module with improved isolation can be achieved without substantially increasing the size.

[0042] Note that the configuration of the communication module shown in FIG. 26 is merely an example. Even if the duplexer according to the present embodiment is incorporated into a communication module in other form, the same effects can be achieved.

[0043] [3. Configuration of Communication Device]

FIG. 27 shows an RF block of a mobile phone terminal as an example of a communication device including the duplexer according to the present embodiment or the communication module as described above. The communication device whose configuration is shown in FIG. 27 can be a mobile phone terminal compatible with the GSM (Global System for Mobile Communications) communication system and the W-CDMA (Wideband Code Division Multiple Access) communication system. The GSM communication system in the present embodiment can support 850 MHz, 950 MHz, 1.8 GHz and 1.9 GHz bands. In addition to the components shown in FIG. 27,

the mobile phone terminal also includes a microphone, a speaker, a liquid crystal display and the like but these components are not illustrated because they are irrelevant to the description of the present embodiment. Here, the duplexer according to the present embodiment is used as a duplexer 73.

- 5 [0044] First, an antenna switching circuit 72 selects an LSI to be operated based on the communication system of reception signals inputted thereto through an antenna 71, i.e., based on whether the communication system is W-CDMA or GSM. When the inputted reception signals correspond to the W-CDMA communication system, switching is performed to output the reception signals to the duplexer 73. The
10 reception signals inputted to the duplexer 73 are limited to those in a certain frequency band through a receiving filter 73a, so that balanced reception signals are outputted to a LNA 74. The LNA 74 amplifies the inputted reception signals and outputs the amplified reception signals to an LSI 76. Based on the inputted reception signals, the LSI 76 decodes the signals into audio signals and controls the
15 operation of each portion of the mobile phone terminal.
- [0045] On the other hand, when transmitting signals, the LSI 76 produces transmission signals. The transmission signals produced are amplified by the power amplifier 75 and are inputted to the transmitting filter 73b. Of the transmission signals inputted to the transmitting filter 73b, only signals in a certain frequency
20 band are allowed to pass therethrough. The transmission signals outputted from the transmitting filter 73b are outputted externally from the antenna 71 through the antenna switching circuit 72.
- [0046] Further, when the inputted reception signals are signals corresponding to the GSM communication system, the antenna switching circuit 72 selects one of receiving
25 filters 77 to 80 based on the frequency band of the signals, and outputs the reception signals to the selected receiving filter. The reception signals whose bands are limited by one of the receiving filters 77 to 80 are inputted to an LSI 83. Based on the inputted reception signals, the LSI 83 decodes the signals into audio signals and controls the operation of each portion of the mobile phone terminal. On the other
30 hand, when transmitting signals, the LSI 83 produces transmission signals. The transmission signals produced are amplified by a power amplifier 81 or 82 and are outputted externally from the antenna 71 through the antenna switching circuit 72.

[0047] By incorporating the duplexer or the communication module according to the present embodiment into the communication device, the communication device with improved isolation can be achieved without substantially increasing the size.

[0048] Note that the configuration of the communication device shown in FIG. 27 is merely an example. Even if the duplexer according to the present embodiment is incorporated into a communication device in other form, the same effects can be achieved.

[0049] [4. Effects of Embodiment, etc.]

According to the present embodiment, the isolation between the transmitting filter and the receiving filter can be improved by electrostatically coupling the transmitting port and the receiving port. Further, since an electrostatic coupling between the transmitting port and the receiving ports can be achieved by, for example, electrically connecting the transmitting port and the receiving ports through an element such as a capacity, an inductor or the like, a duplexer can be achieved without substantially increasing the size.

[0050] The present invention is not limited to the specific examples described above in detail. The present invention may be changed or modified within the scope of the appended claims.

[0051] The following notes are disclosed with respect to the present embodiment.

[0052] (Note 1)

A duplexer including: an unbalanced transmitting filter; a balanced receiving filter; a transmitting port connected to the transmitting filter; a primary receiving port connected to the receiving filter; a secondary receiving port connected to the receiving filter; and an electrostatic coupling portion connected between the transmitting port and the primary receiving port and between the transmitting port and the secondary receiving port, the electrostatic coupling portion allowing phase-adjusted electric power to flow from the transmitting port to the primary receiving port and from the transmitting port to the secondary receiving port.

[0053] According to the above configuration, it is possible to cancel the deterioration of the isolation caused by a coupling between the transmitting port and the receiving ports through space. Therefore, a simple duplexer with high isolation can be achieved without increasing the space between the transmitting port and the

receiving ports or additionally providing an electromagnetic shielding structure.

[0054] (Note 2)

The duplexer according to Note 1, wherein the electrostatic coupling portion adjusts the phase of electric power such that electric power that flows from the transmitting port to the primary receiving port and electric power that flows from the transmitting port to the secondary receiving port have an absolute phase difference in a range of 0 to 90° or 270 to 360° in the receiving band.

[0055] (Note 3)

The duplexer according to Note 1 or 2, wherein the electrostatic coupling portion provides an electrostatic coupling between the transmitting port and the primary receiving port and an electrostatic coupling between the transmitting port and the secondary receiving port at the same degree.

[0056] According to the above configurations, it is possible to achieve a duplexer with higher isolation.

15 [0057] (Note 4)

The duplexer according to any one of Note 1 to 3, wherein the electrostatic coupling portion is composed of a conductor.

[0058] According to the above configuration, it is possible to achieve a duplexer with higher isolation without making significant changes to the structure of the duplexer.

20 [0059] (Note 5)

The duplexer according to any one of Notes 1 to 3, wherein the electrostatic coupling portion includes a capacitative element.

[0060] According to the above configuration, it is possible to achieve a duplexer with higher isolation without making significant changes to the structure of the duplexer.

25 [0061] (Note 6)

The duplexer according to any one of Notes 1 to 3, wherein the electrostatic coupling portion includes an impedance-matching inductor.

[0062] According to the above configuration, it is possible to achieve a duplexer with higher isolation without making significant changes to the structure of the duplexer.

30 [0063] (Note 7)

A communication module including a duplexer, the duplexer including: an unbalanced transmitting filter; a balanced receiving filter; a transmitting port

connected to the transmitting filter; a primary receiving port connected to the receiving filter; a secondary receiving port connected to the receiving filter; and an electrostatic coupling portion connected between the transmitting port and the primary receiving port and between the transmitting port and the secondary receiving port, the electrostatic coupling portion allowing phase-adjusted electric power to flow from the transmitting port to the primary receiving port and from the transmitting port to the secondary receiving port.

[0064] According to the above configuration, it is possible to cancel the deterioration of the isolation caused by a coupling between the transmitting port and the receiving ports through space. Therefore, a simple duplexer with high isolation can be achieved without increasing the space between the transmitting port and the receiving ports or additionally providing an electromagnetic shielding structure.

[0065] (Note 8)

A communication device including a duplexer, the duplexer including: an unbalanced transmitting filter; a balanced receiving filter; a transmitting port connected to the transmitting filter; a primary receiving port connected to the receiving filter; a secondary receiving port connected to the receiving filter; and an electrostatic coupling portion connected between the transmitting port and the primary receiving port and between the transmitting port and the secondary receiving port, the electrostatic coupling portion allowing phase-adjusted electric power to flow from the transmitting port to the primary receiving port and from the transmitting port to the secondary receiving port.

[0066] According to the above configuration, it is possible to cancel the deterioration of the isolation caused by a coupling between the transmitting port and the receiving ports through space. Therefore, a simple duplexer with high isolation can be achieved without increasing the space between the transmitting port and the receiving ports or additionally providing an electromagnetic shielding structure.

Industrial Applicability

[0067] The present application is useful for a duplexer, a communication module, and a communication device.

Description of Reference Numerals

5	[0068]	31	transmitting filter
		32	receiving filter
		33	transmitting port
		34a, 34b	receiving port
		36	electrostatic coupling portion

CLAIMS

1. A duplexer comprising:
an unbalanced transmitting filter;
a balanced receiving filter;
5 a transmitting port connected to the transmitting filter;
a primary receiving port connected to the receiving filter;
a secondary receiving port connected to the receiving filter; and
an electrostatic coupling portion connected between the transmitting port and
the primary receiving port and between the transmitting port and the secondary
10 receiving port, the electrostatic coupling portion allowing phase-adjusted electric
power to flow from the transmitting port to the primary receiving port and from the
transmitting port to the secondary receiving port.
2. The duplexer according to claim 1, wherein the electrostatic coupling portion
15 adjusts a phase of electric power such that electric power that flows from the
transmitting port to the primary receiving port and electric power that flows from the
transmitting port to the secondary receiving port have an absolute phase difference in
a range of 0 to 90° or 270 to 360° in a receiving band.
- 20 3. The duplexer according to claim 1 or 2, wherein the electrostatic coupling
portion provides an electrostatic coupling between the transmitting port and the
primary receiving port and an electrostatic coupling between the transmitting port
and the secondary receiving port at the same degree.
- 25 4. The duplexer according to any one of claims 1 to 3, wherein the electrostatic
coupling portion is composed of a conductor.
5. A communication module comprising a duplexer,
the duplexer comprising:
30 an unbalanced transmitting filter;
a balanced receiving filter;
a transmitting port connected to the transmitting filter;

- a primary receiving port connected to the receiving filter;
a secondary receiving port connected to the receiving filter; and
an electrostatic coupling portion connected between the transmitting port and
the primary receiving port and between the transmitting port and the secondary
5 receiving port, the electrostatic coupling portion allowing phase-adjusted electric
power to flow from the transmitting port to the primary receiving port and from the
transmitting port to the secondary receiving port.
6. A communication device comprising a duplexer,
10 the duplexer comprising:
an unbalanced transmitting filter;
a balanced receiving filter;
a transmitting port connected to the transmitting filter;
a primary receiving port connected to the receiving filter;
15 a secondary receiving port connected to the receiving filter; and
an electrostatic coupling portion connected between the transmitting port and
the primary receiving port and between the transmitting port and the secondary
receiving port, the electrostatic coupling portion allowing phase-adjusted electric
power to flow from the transmitting port to the primary receiving port and from the
20 transmitting port to the secondary receiving port.