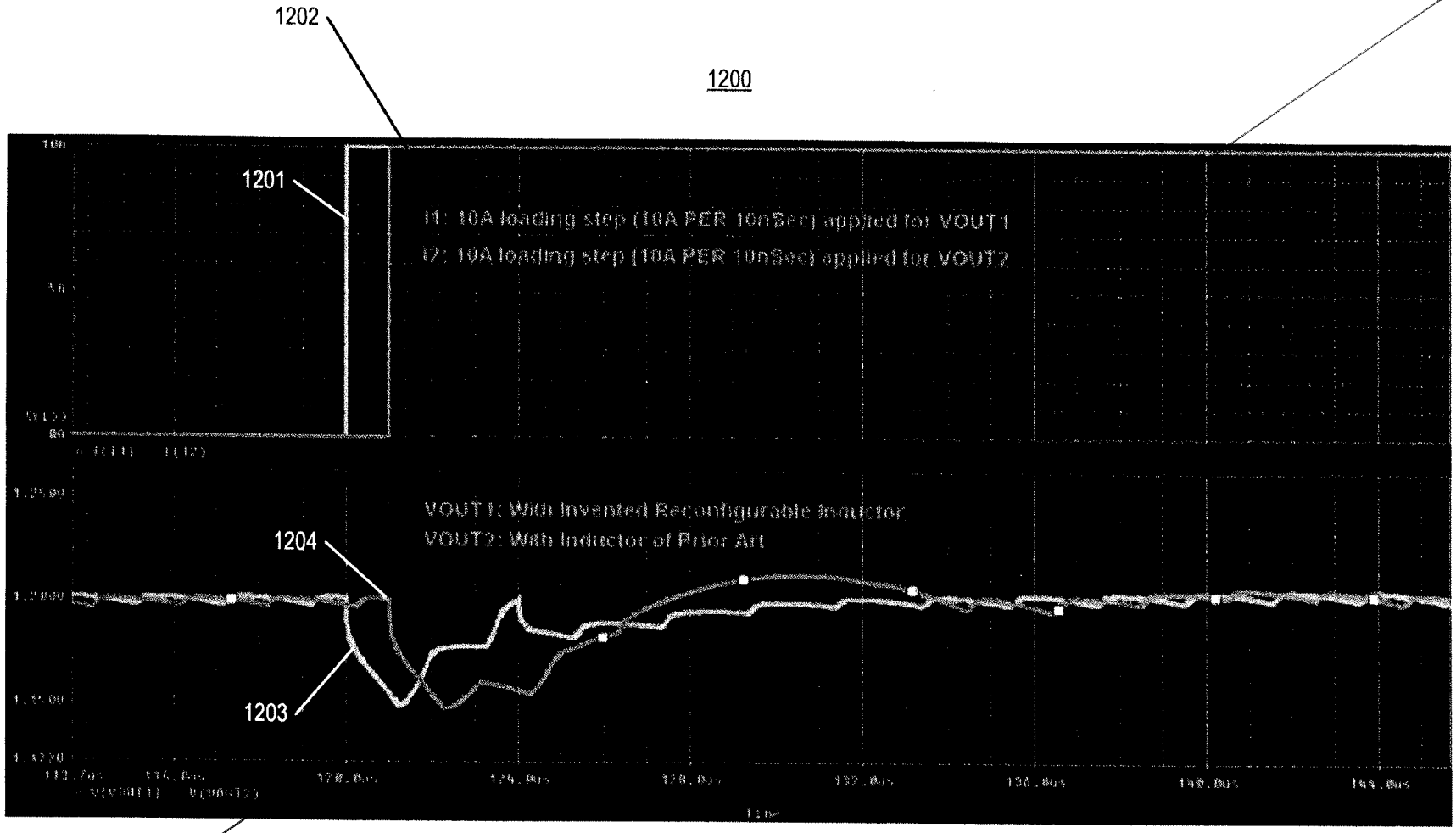

1202
1200
1201
I1: 10A loading step (10A PER 10nSec) applied for VOUT1
I2: 10A loading step (10A PER 10nSec) applied for VOUT2
VOUT1: With Invented Reconfigurable Inductor
VOUT2: With Inductor of Prior Art
1204
1203

FIG. 12

1300

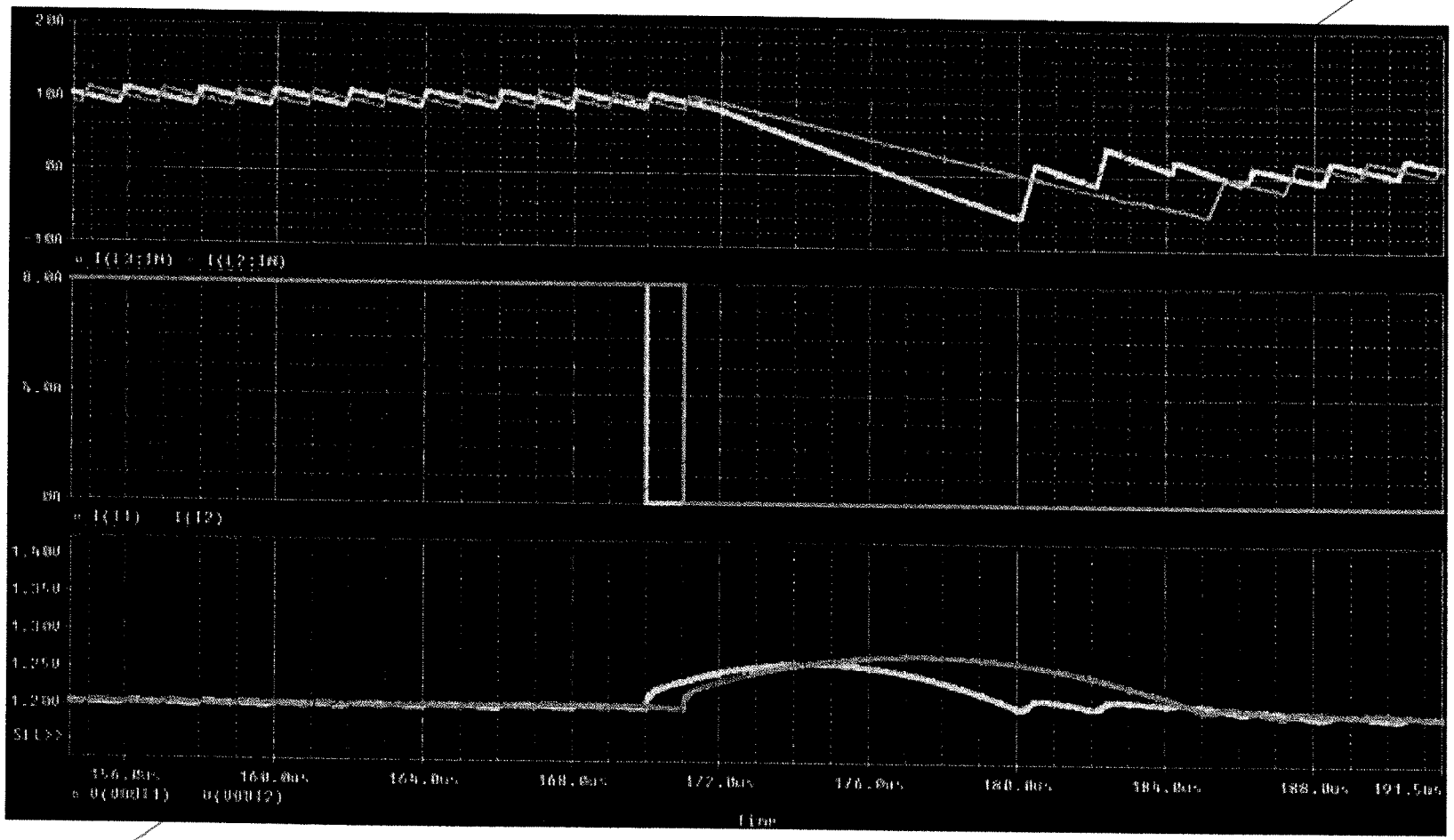

200
100
0A
-100A
I(L3:1N) - I(L2:1N)
8.0A
4.0A
0A
I(I1) I(I2)
1.400U
1.350U
1.300U
1.250U
1.200U
SEL>>
156.0us
160.0us
164.0us
168.0us
172.0us
176.0us
180.0us
184.0us
188.0us
191.5us
V(U0011) V(U0012)
Time

FIG. 13

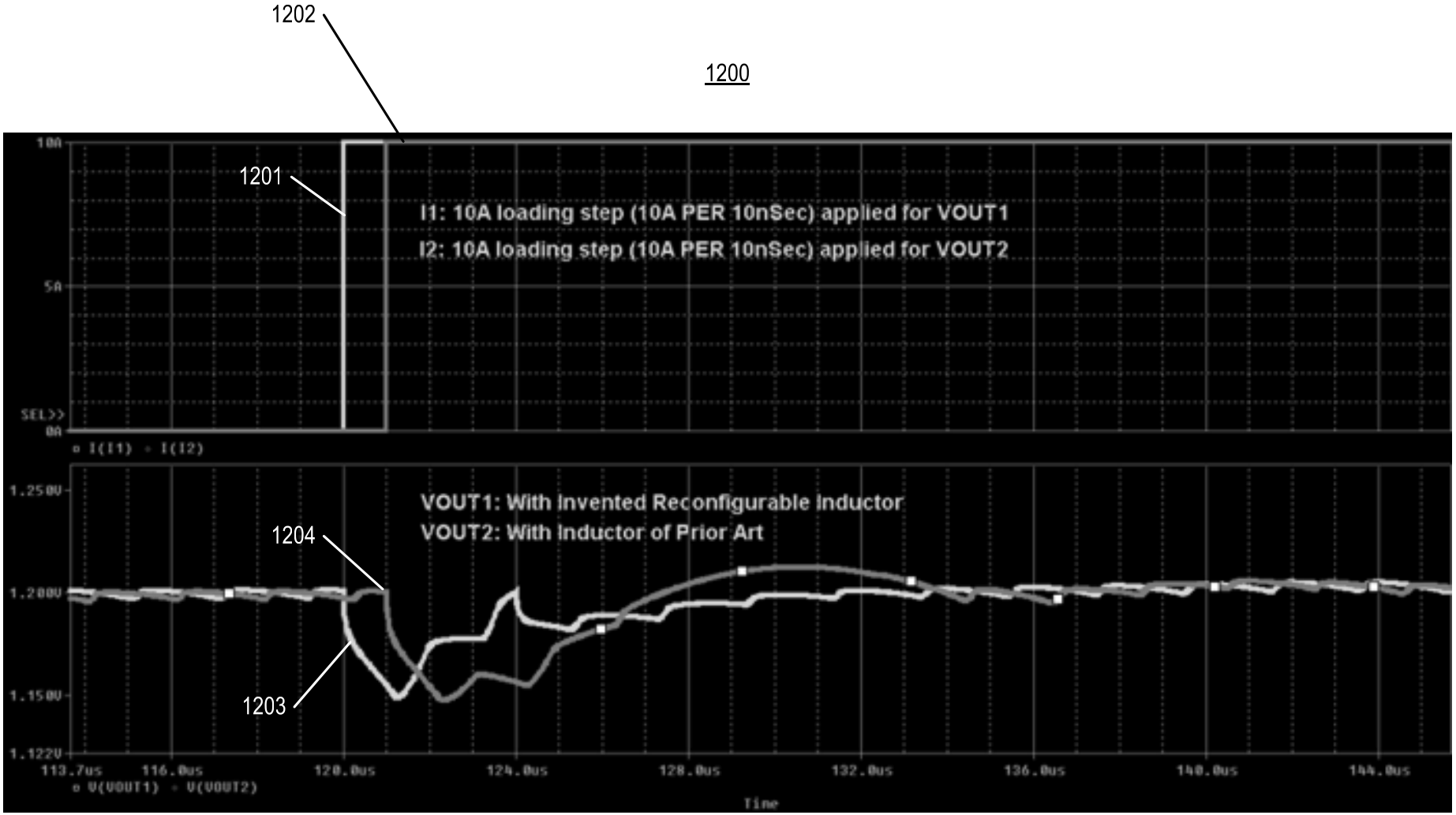

1202
1200
1201
I1: 10A loading step (10A PER 10nSec) applied for VOUT1
I2: 10A loading step (10A PER 10nSec) applied for VOUT2
10A
5A
SEL>>
0A
I(I1) I(I2)
VOUT1: With Invented Reconfigurable Inductor
VOUT2: With Inductor of Prior Art
1204
1203
1.250V
1.200V
1.150V
1.122V
113.7us
116.0us
120.0us
124.0us
128.0us
132.0us
136.0us
140.0us
144.0us
V(VOUT1) V(VOUT2)
Time

FIG. 12

1300

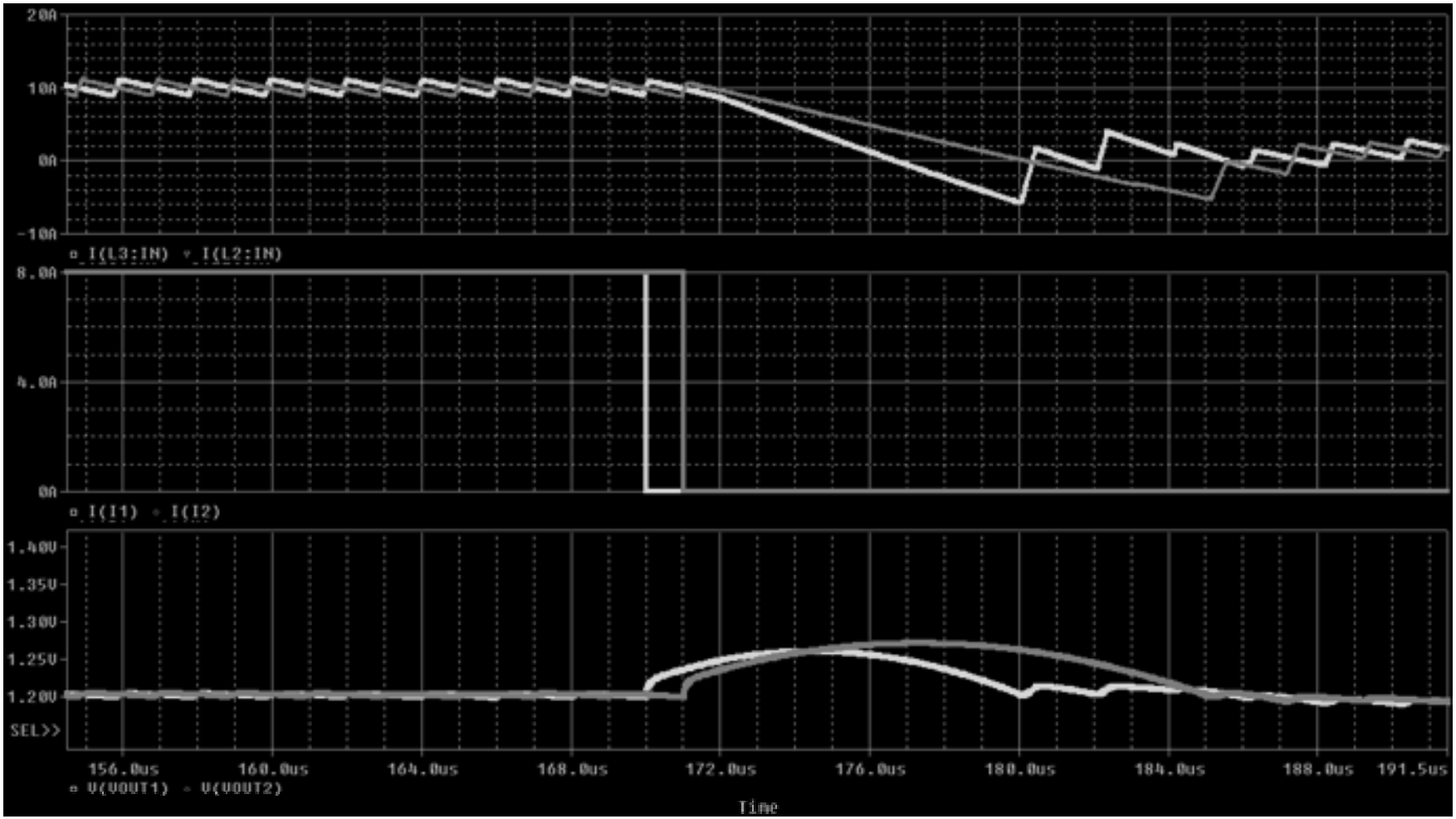

20A
10A
0A
-10A
I(L3:IN)
I(L2:IN)
8.0A
4.0A
0A
I(I1)
I(I2)
1.40V
1.35V
1.30V
1.25V
1.20V
SEL>>
156.0us
160.0us
164.0us
168.0us
172.0us
176.0us
180.0us
184.0us
188.0us
191.5us
V(VOUT1)
V(VOUT2)
Time

FIG. 13