

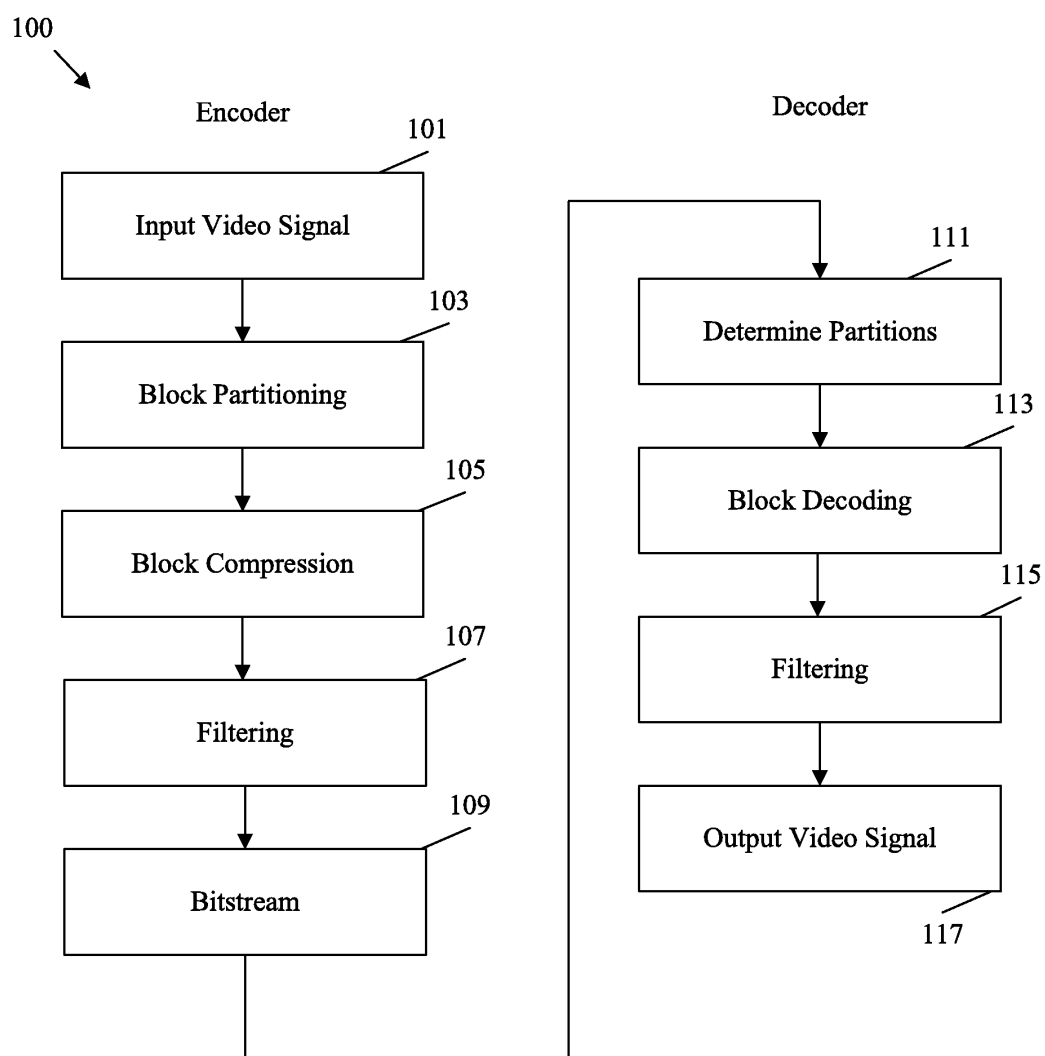


FIG. 1

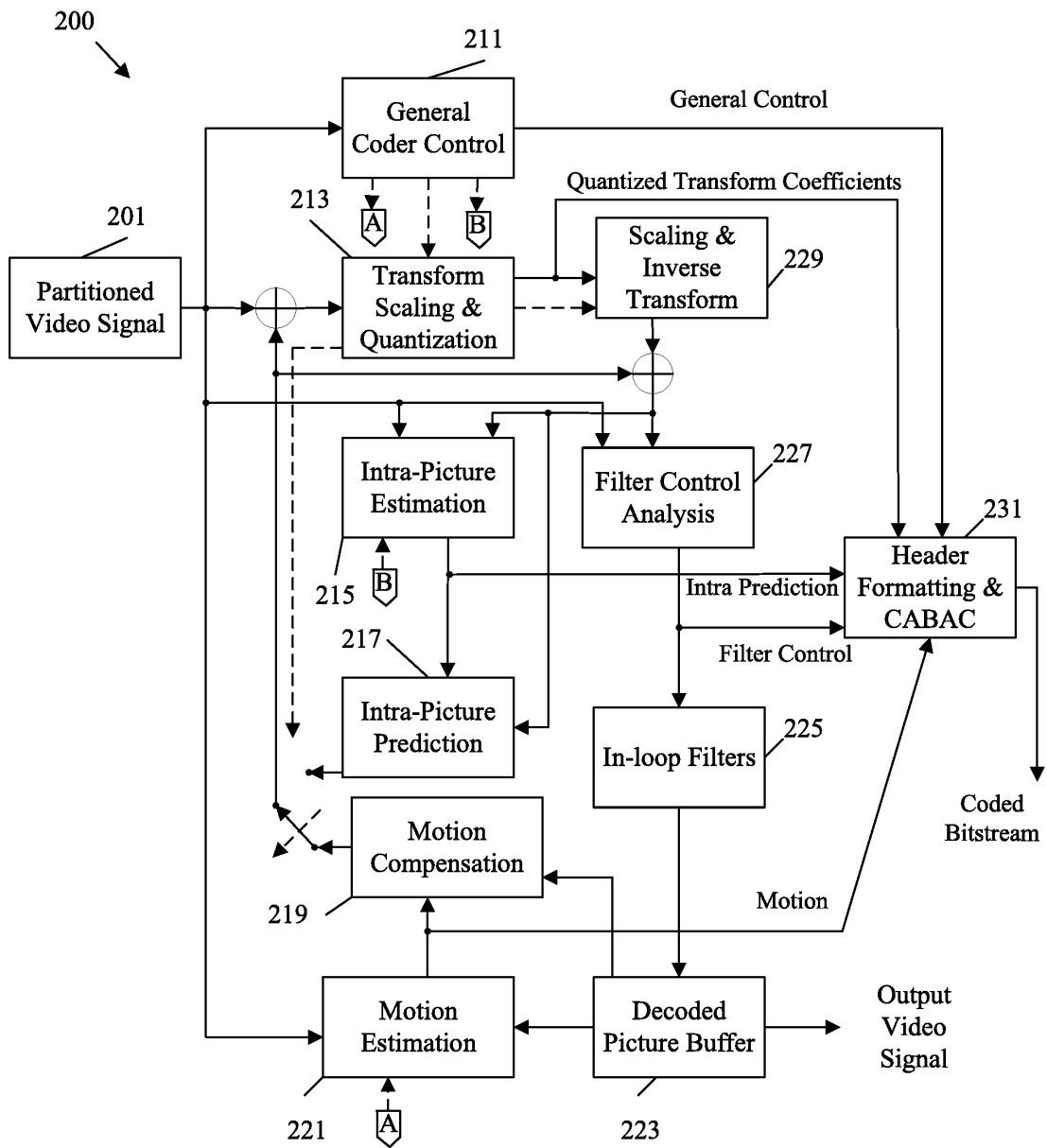


FIG. 2

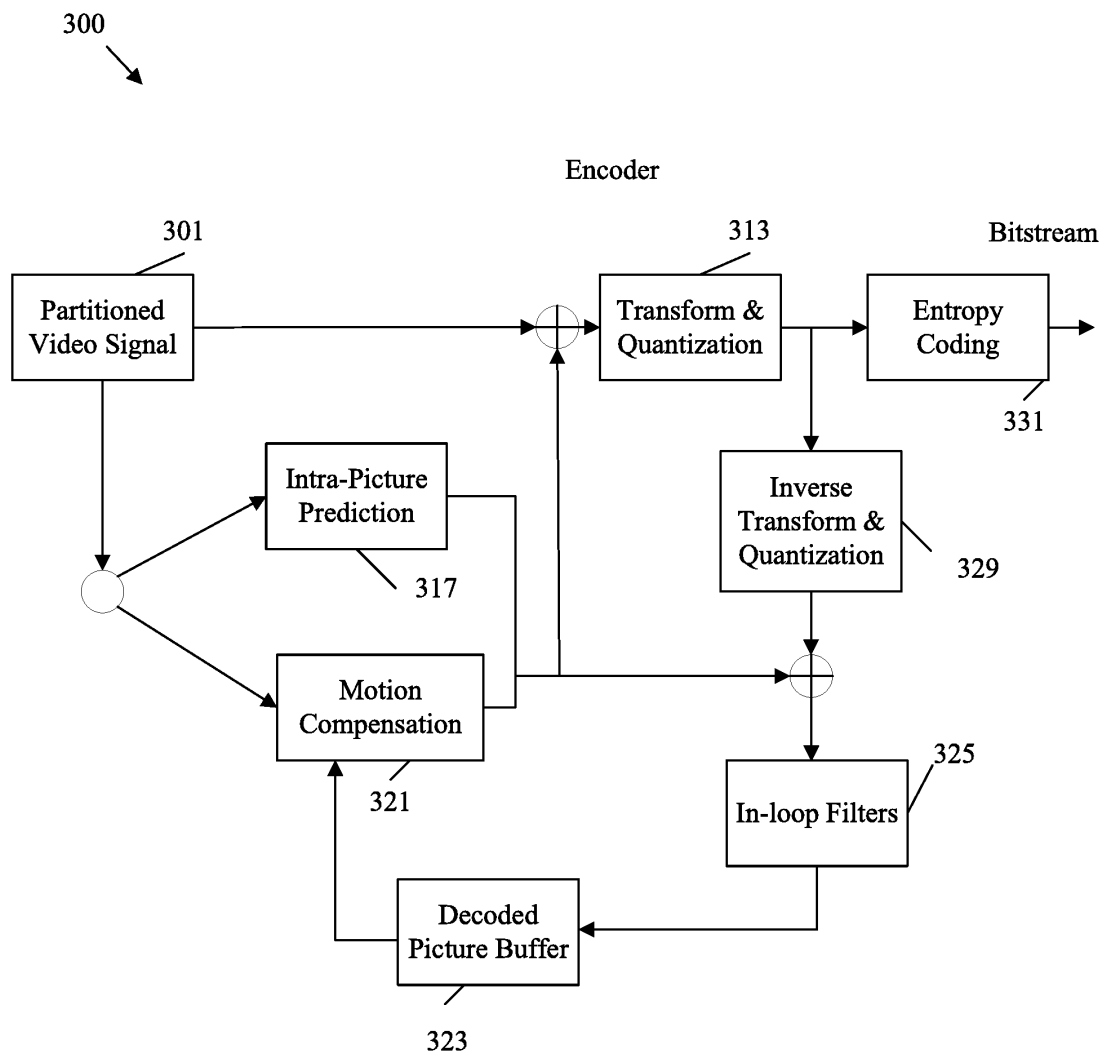


FIG. 3

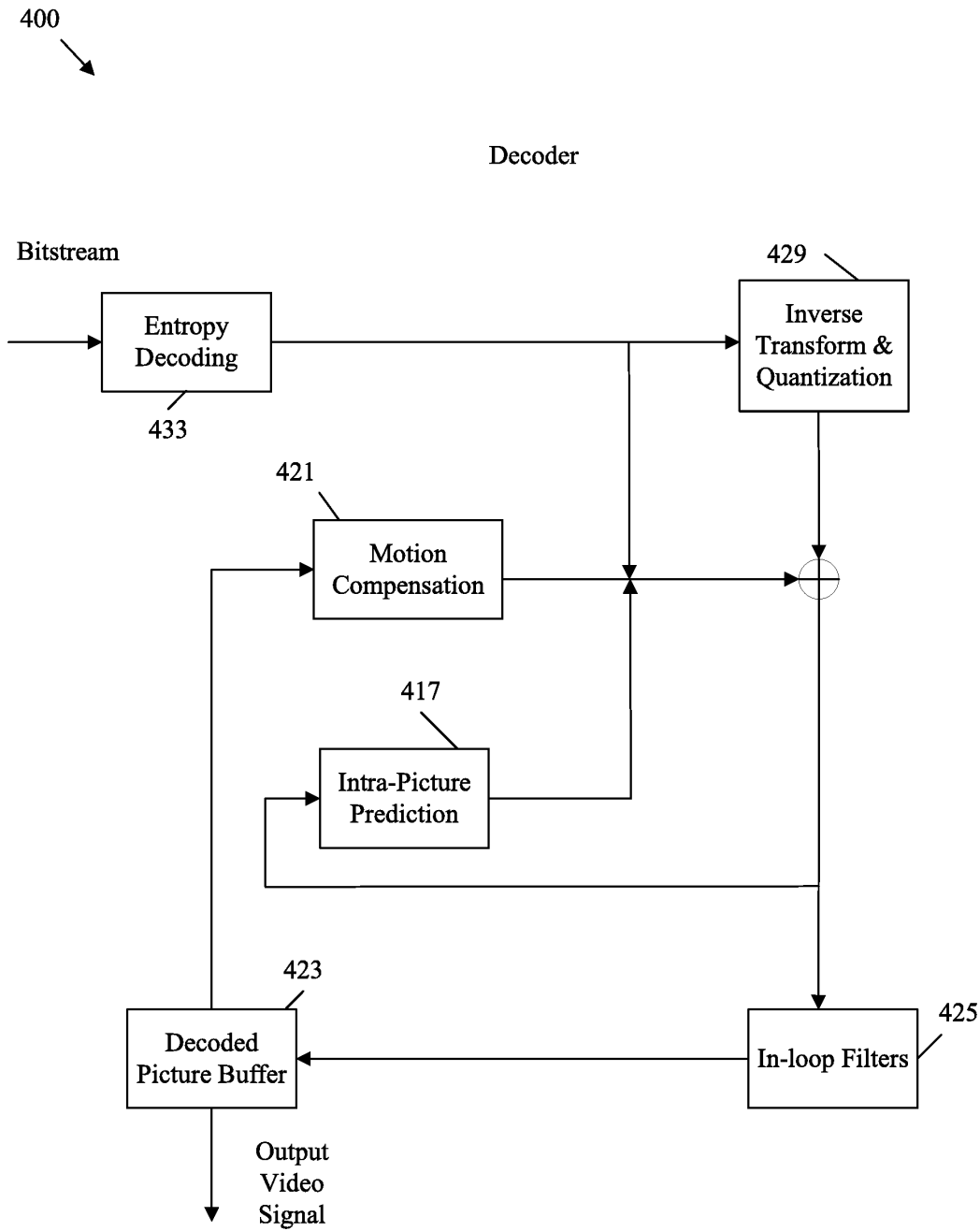


FIG. 4

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Hypothetical Reference Decoder

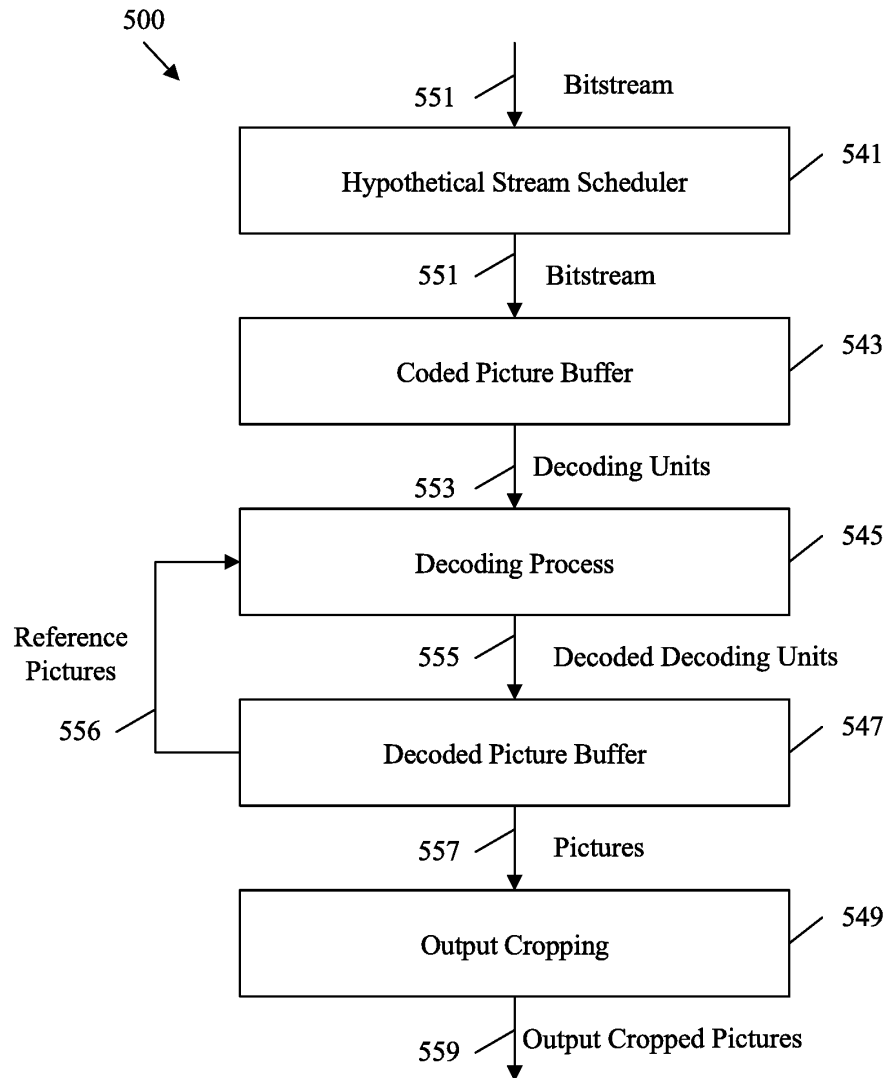


FIG. 5

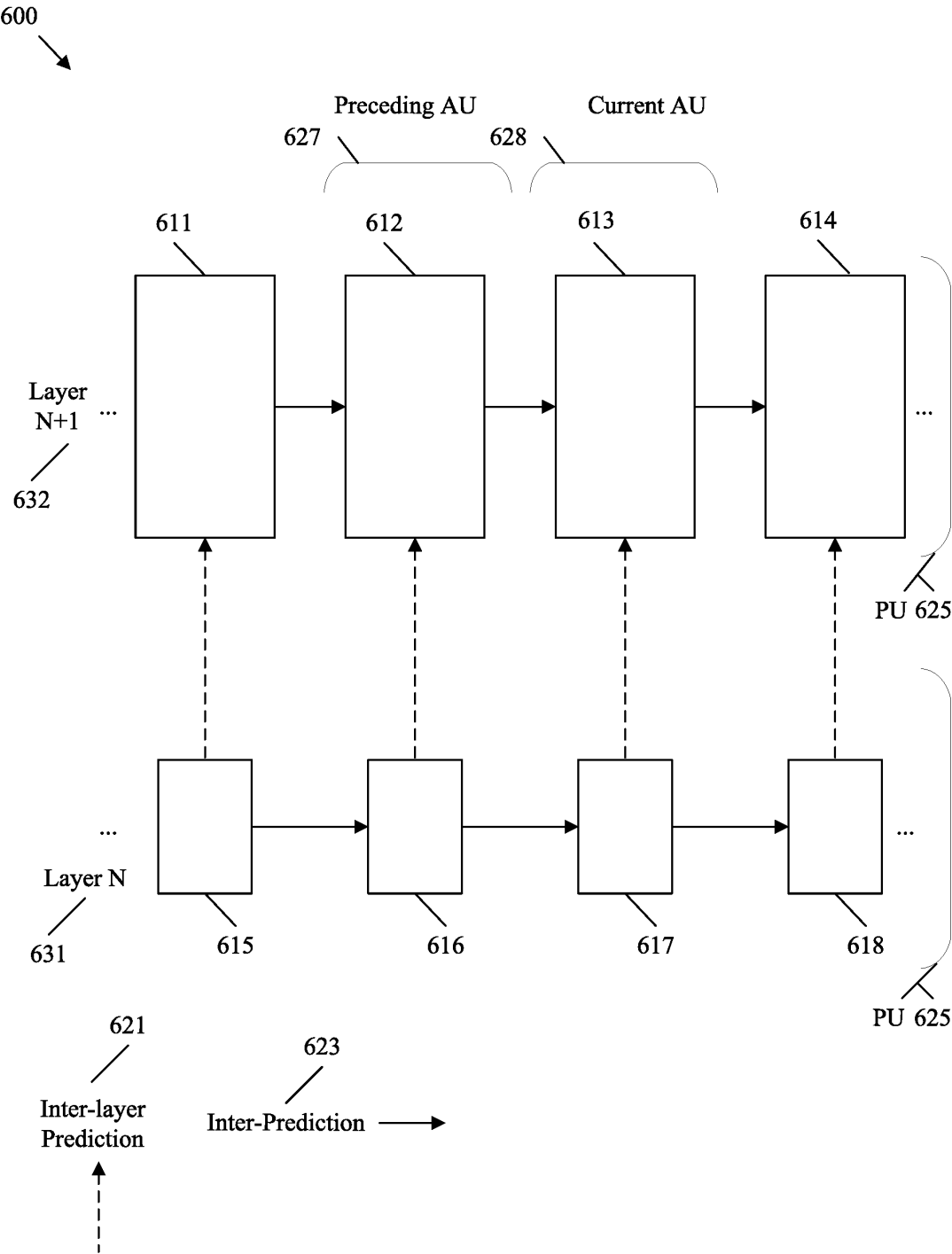


FIG. 6

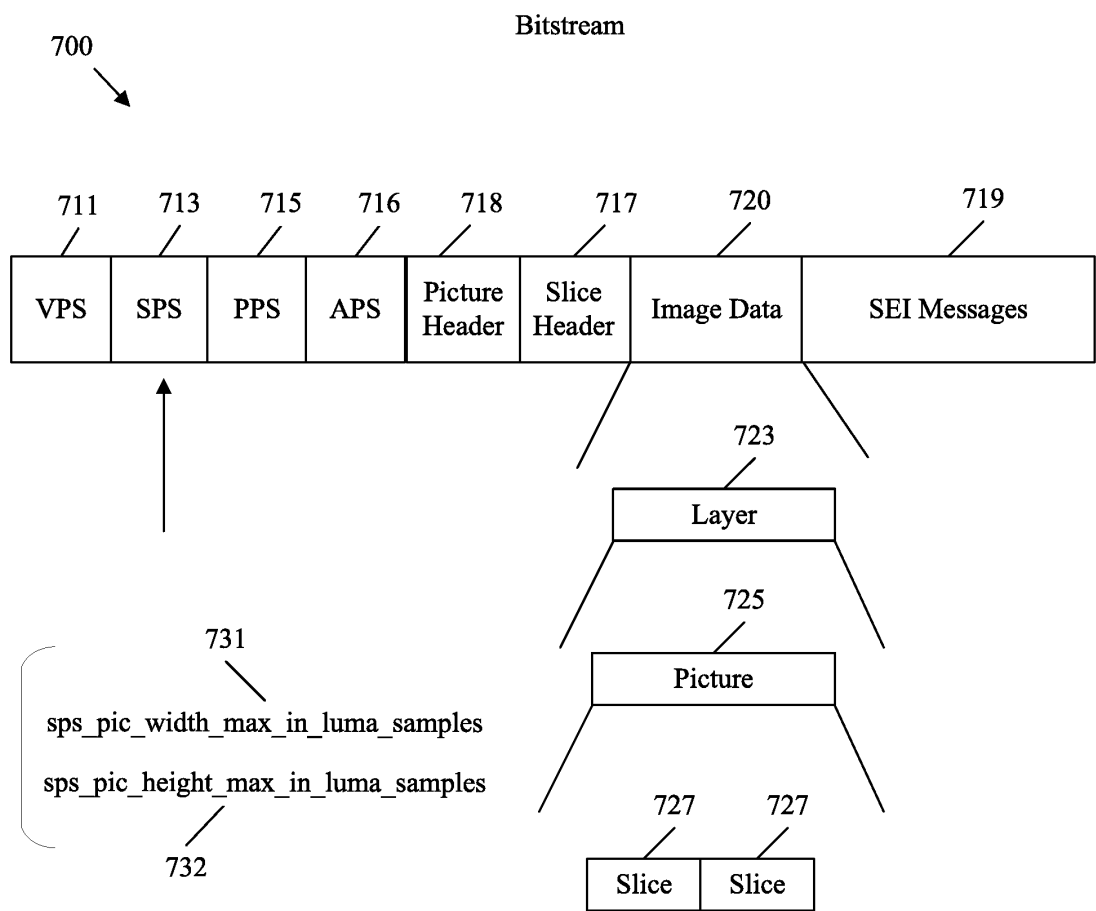


FIG. 7

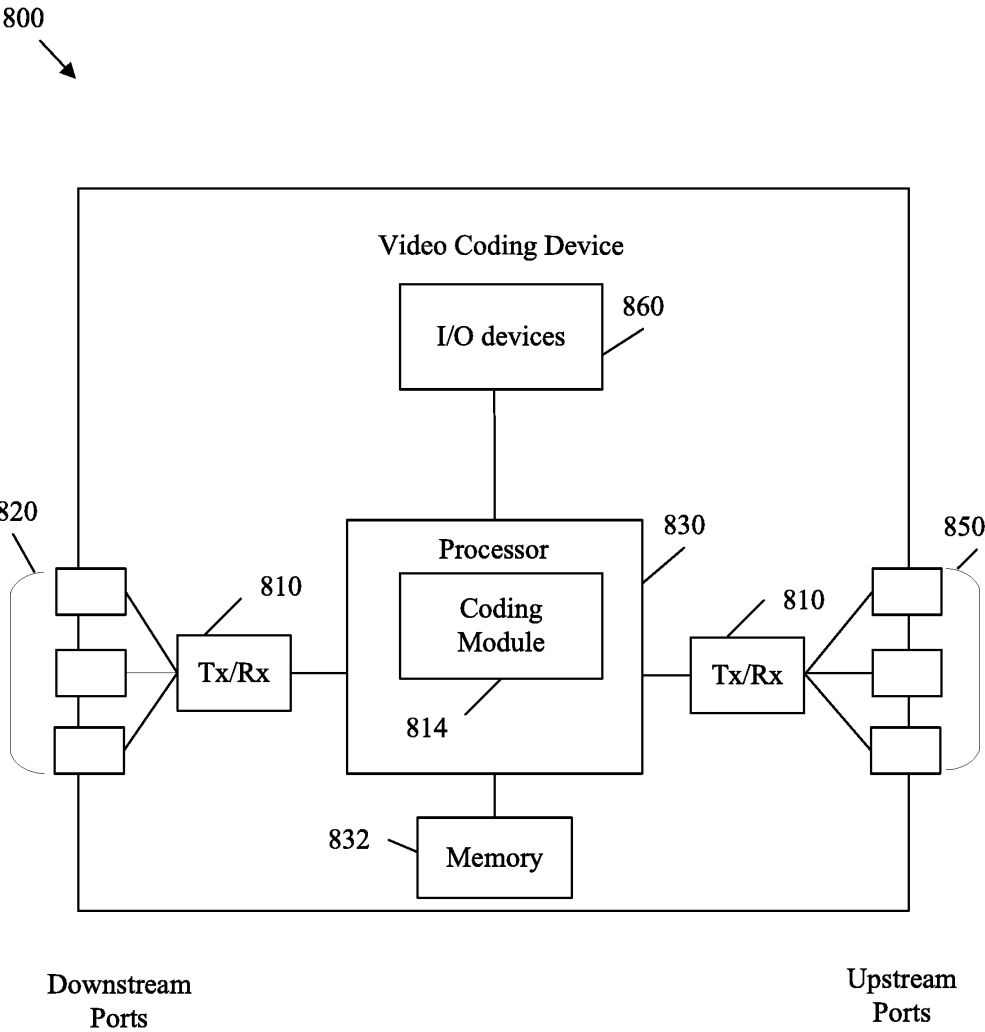


FIG. 8

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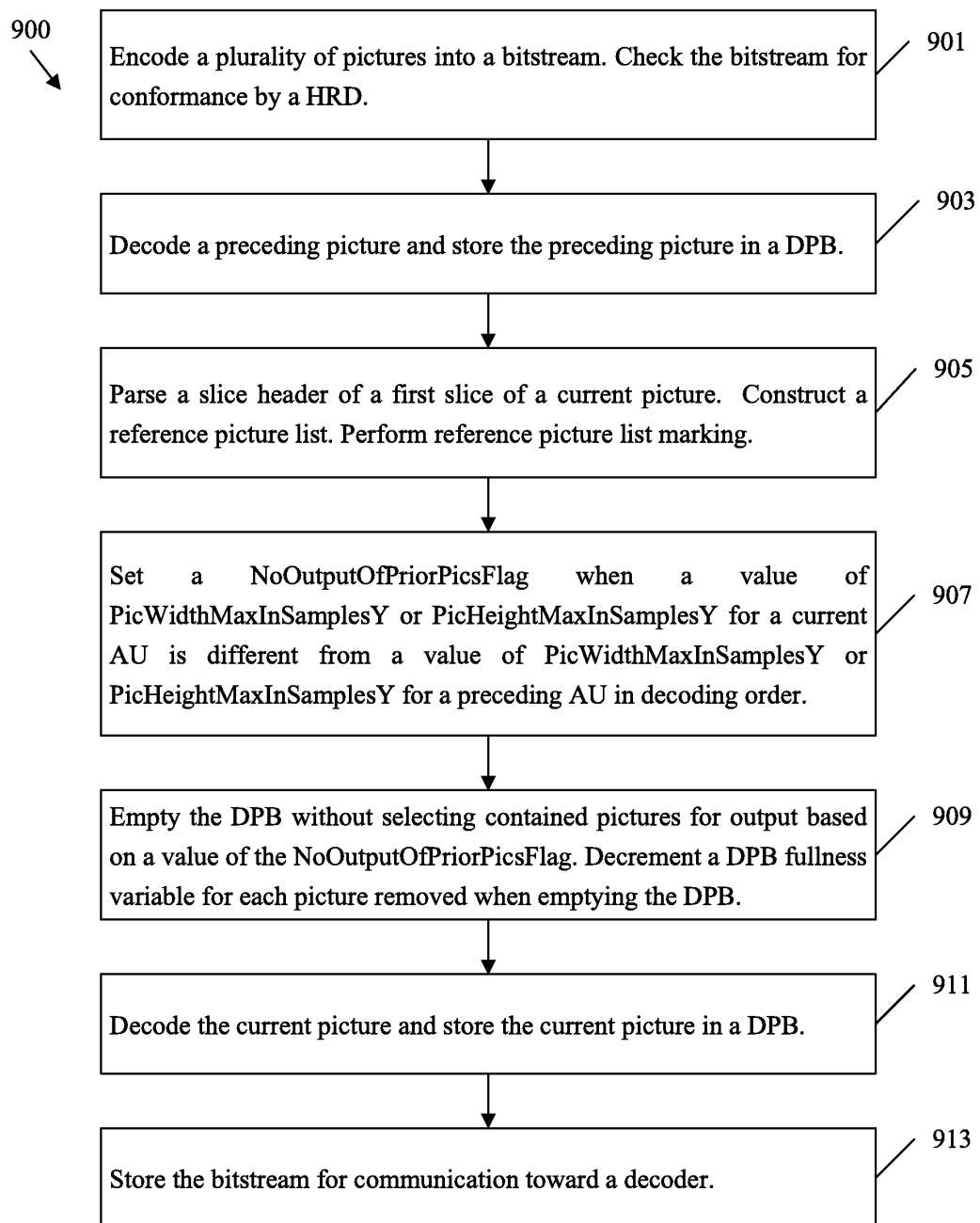


FIG. 9

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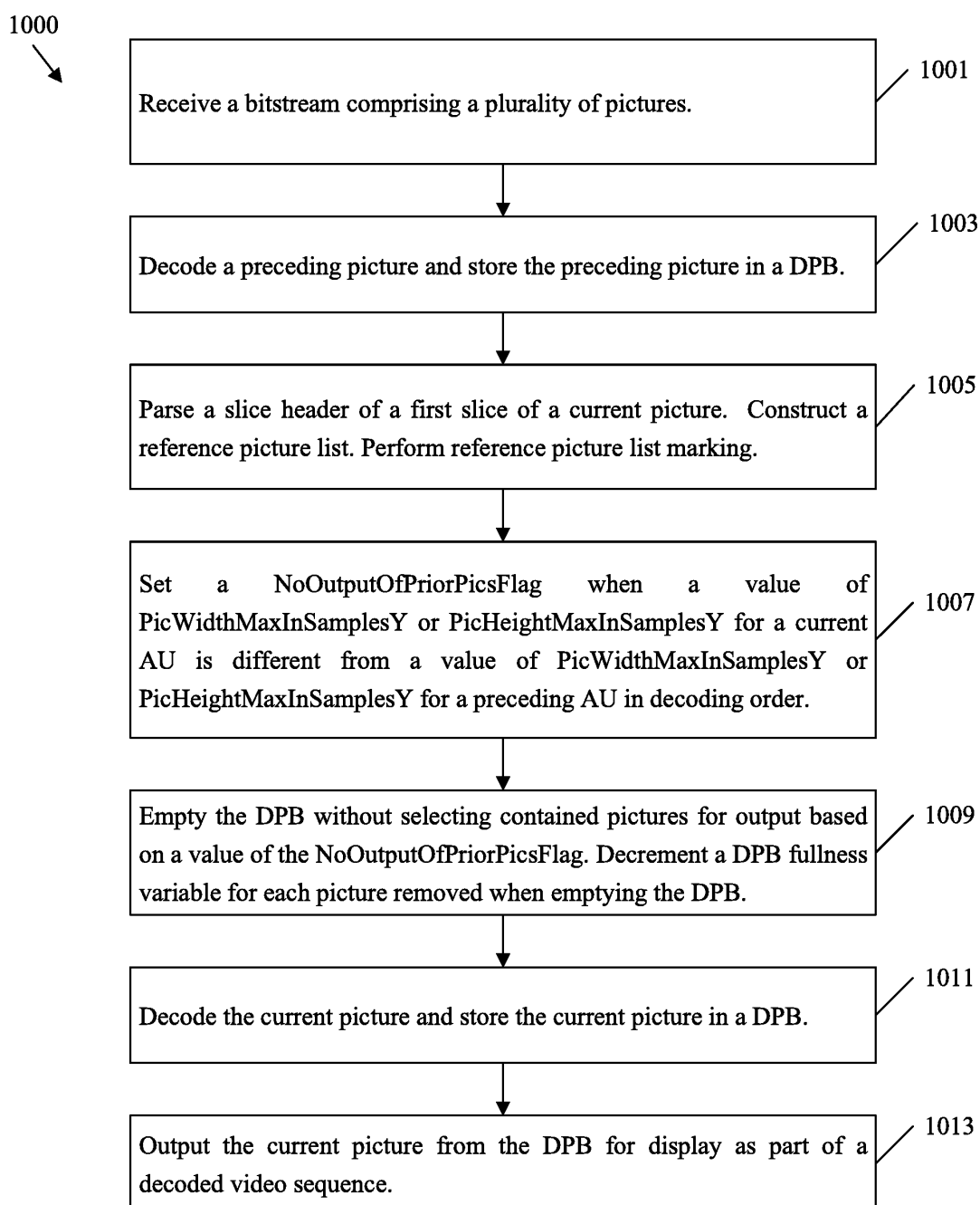


FIG. 10

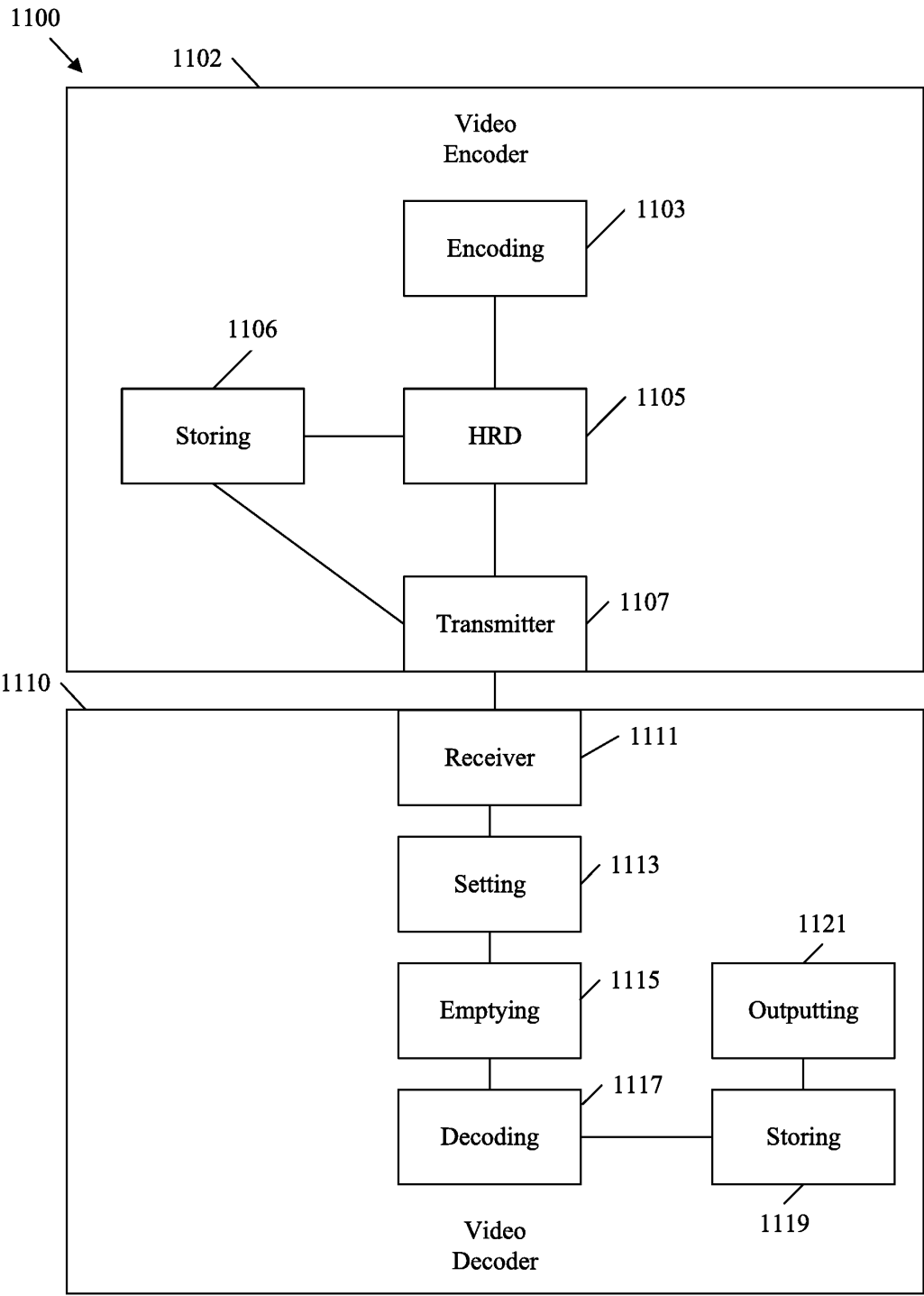


FIG. 11