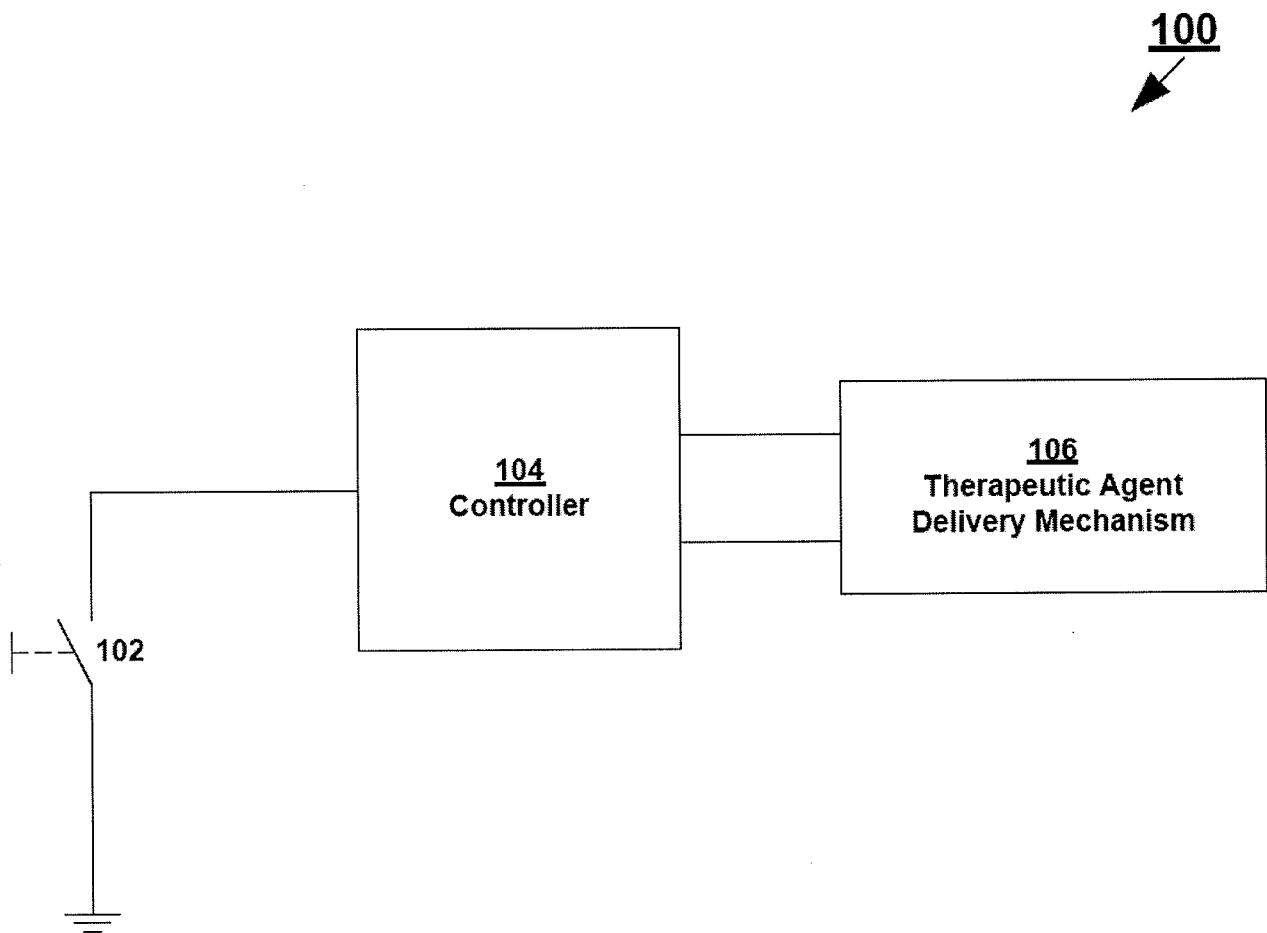
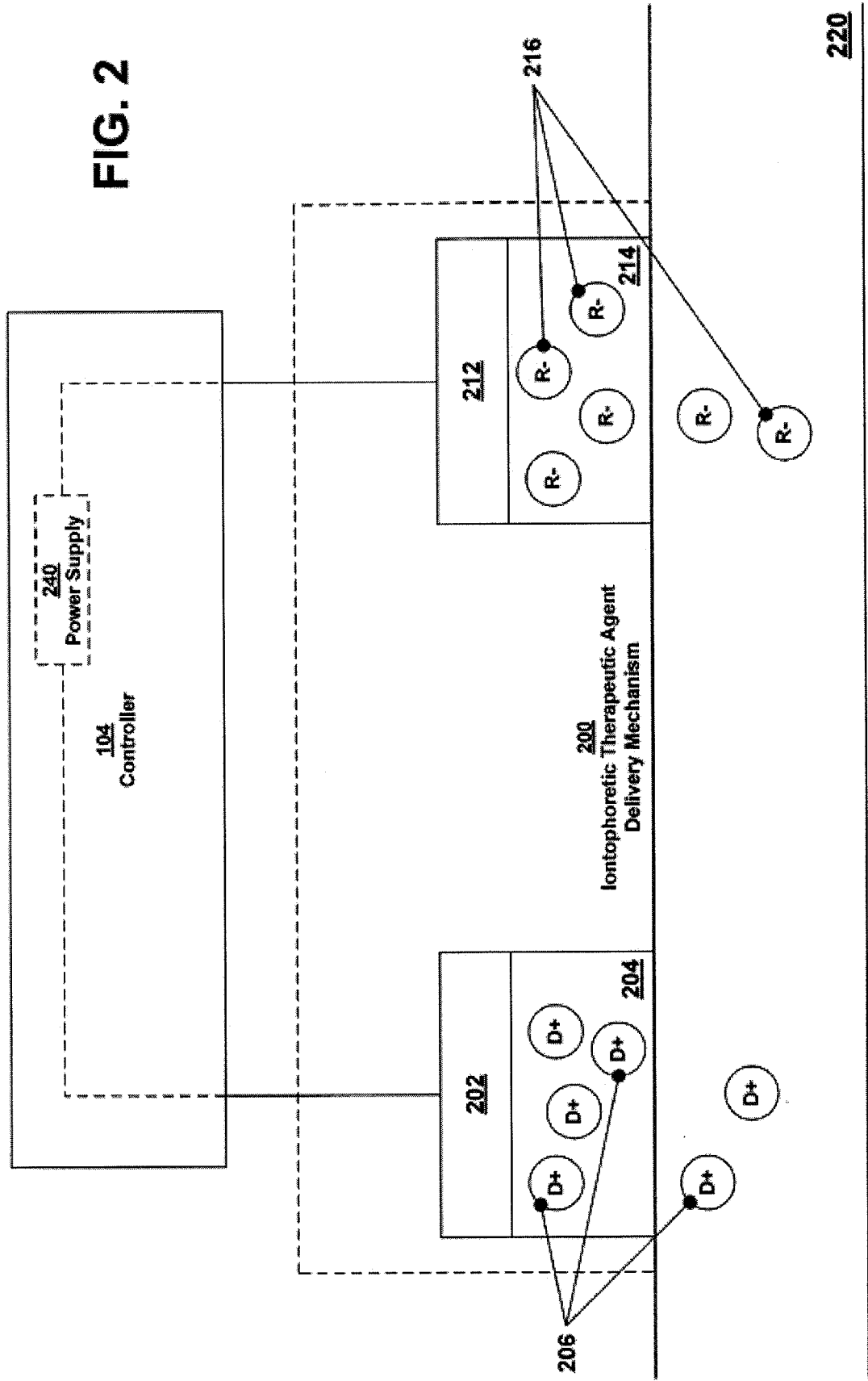


**FIG. 1**



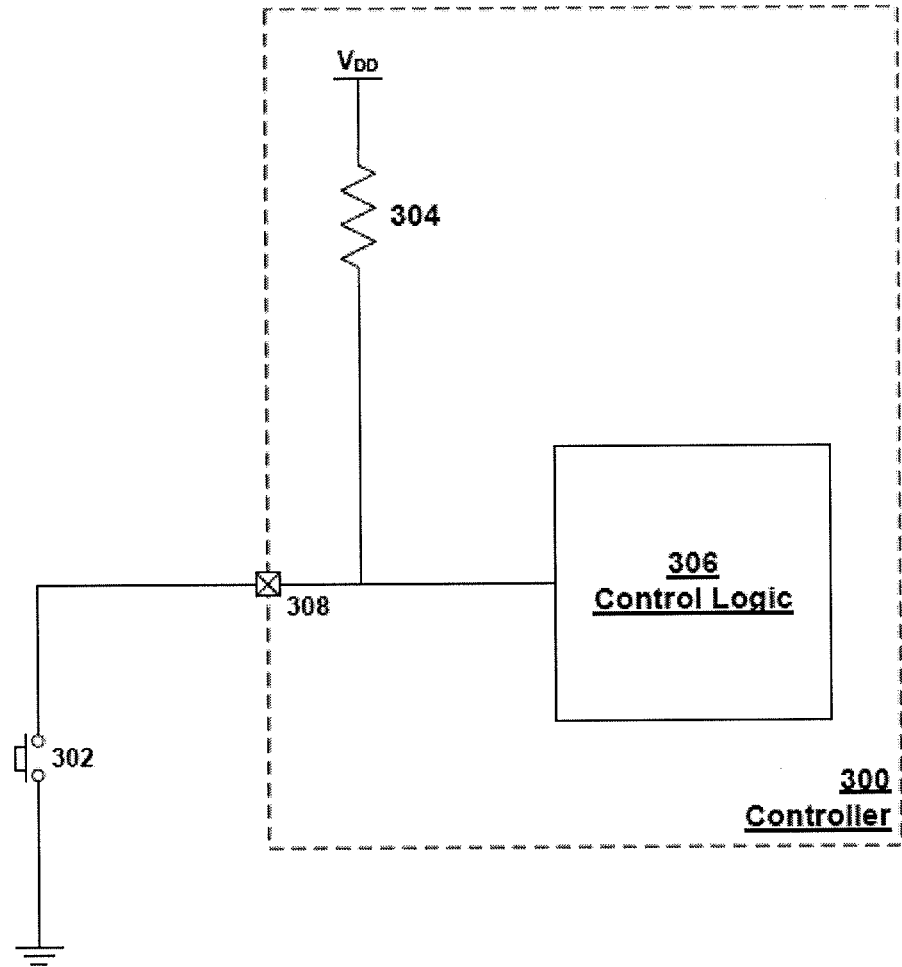


FIG. 3

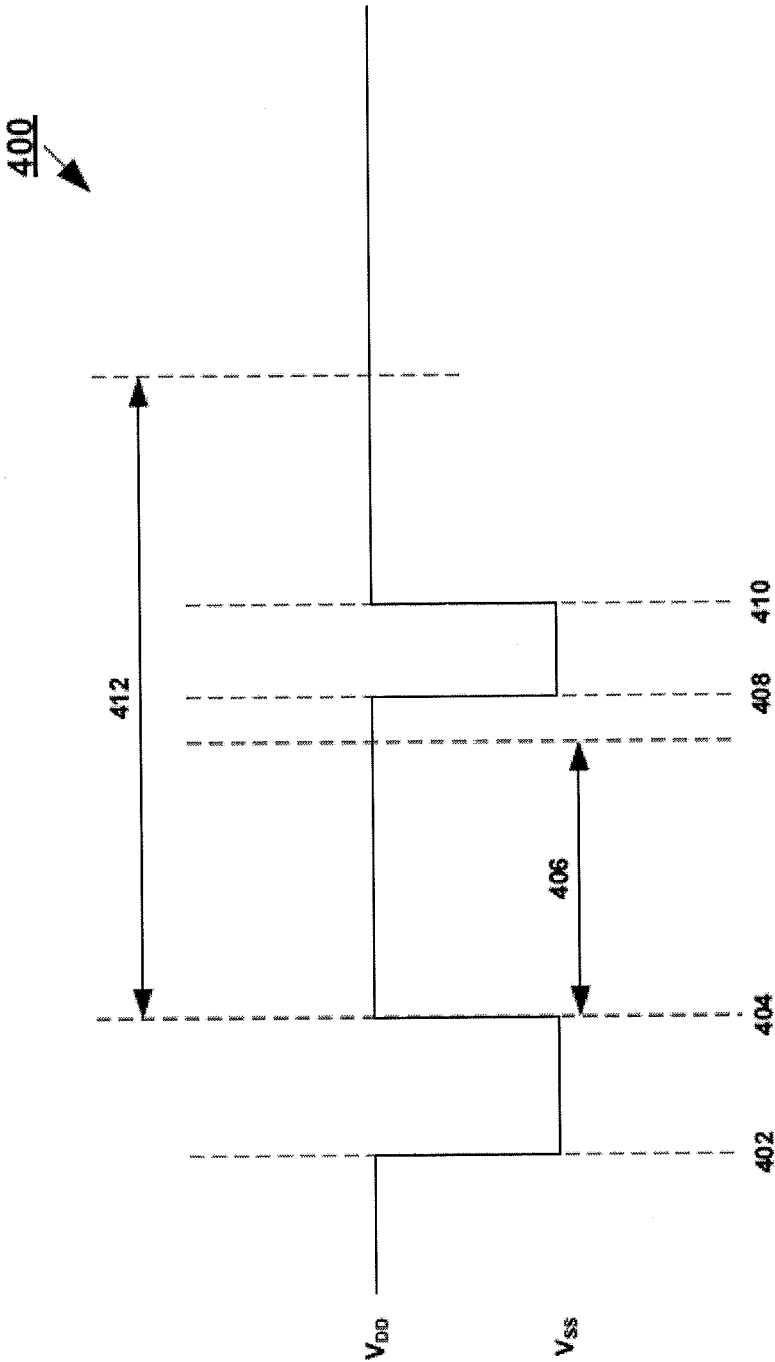
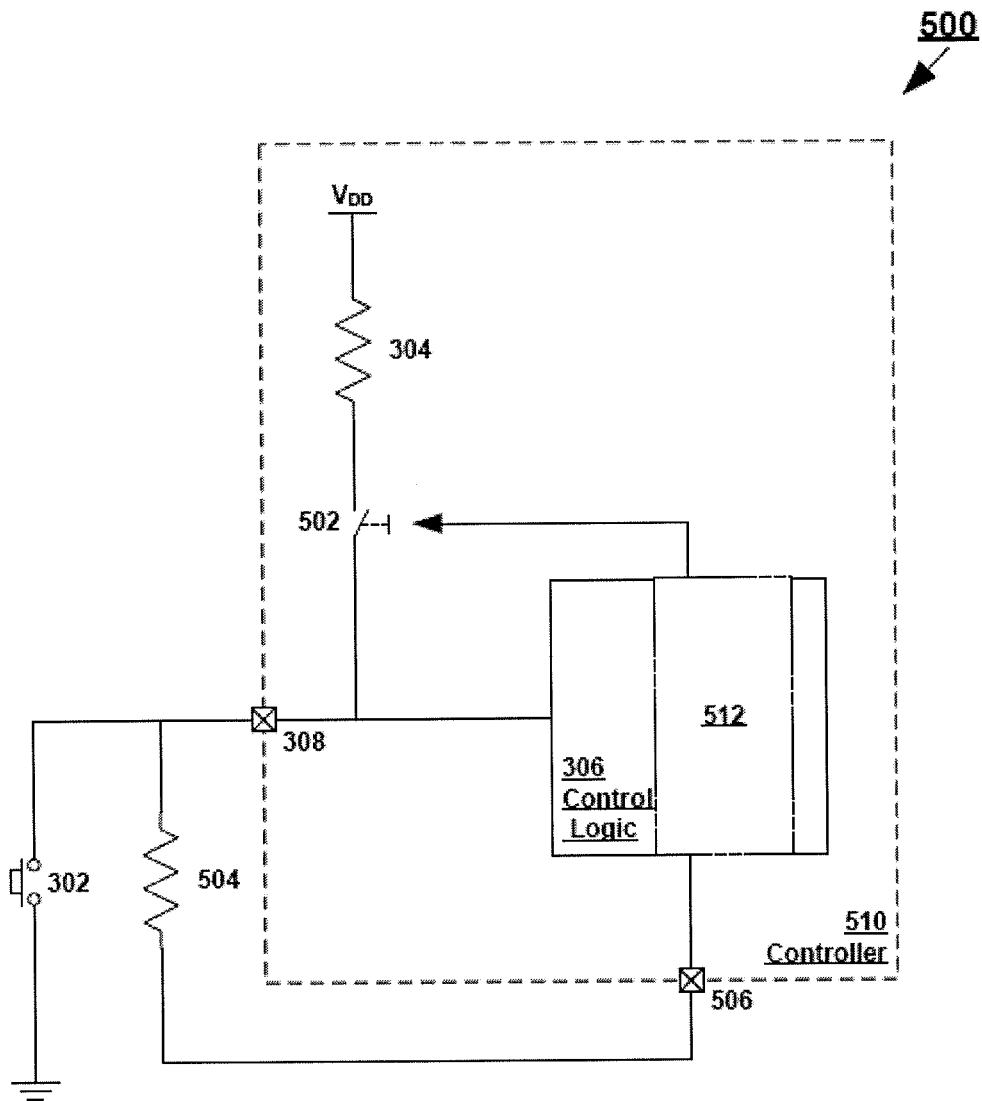


FIG. 4

**FIG. 5**

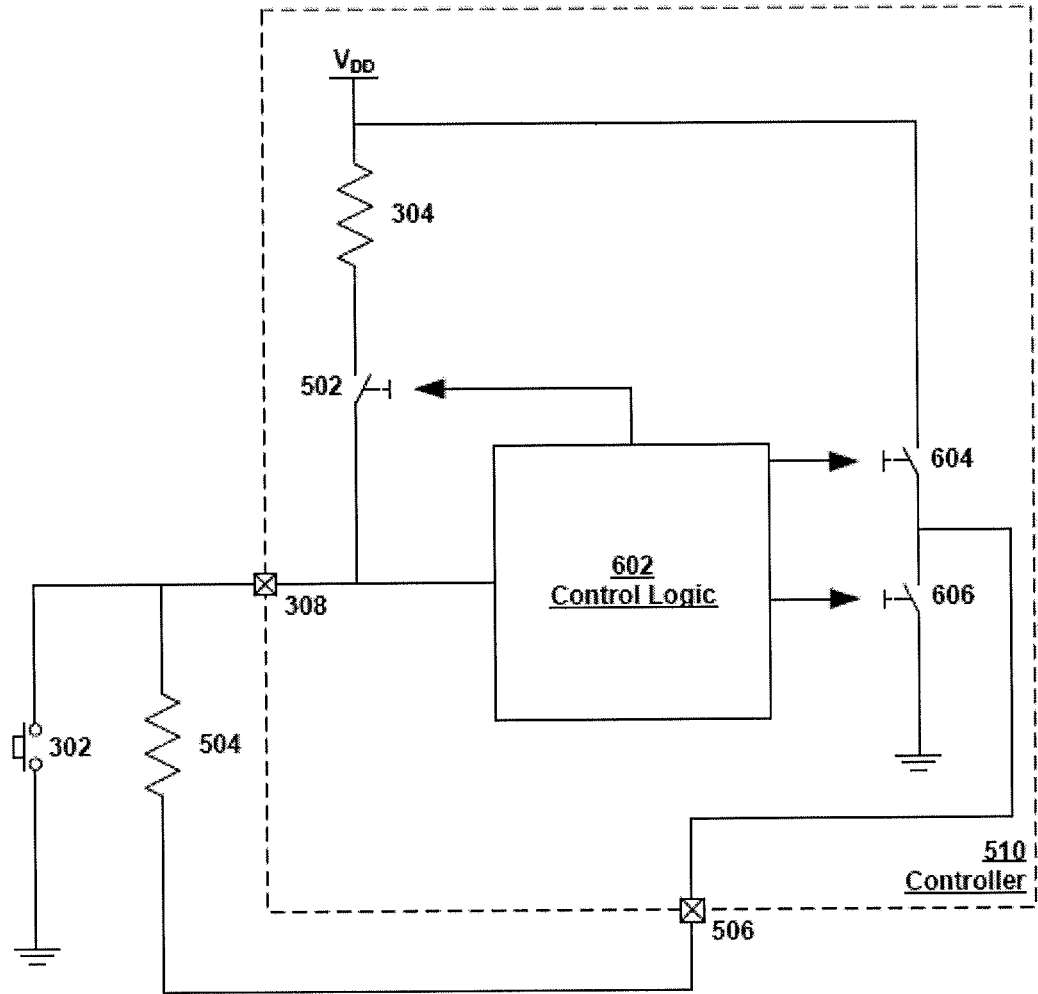


FIG. 6

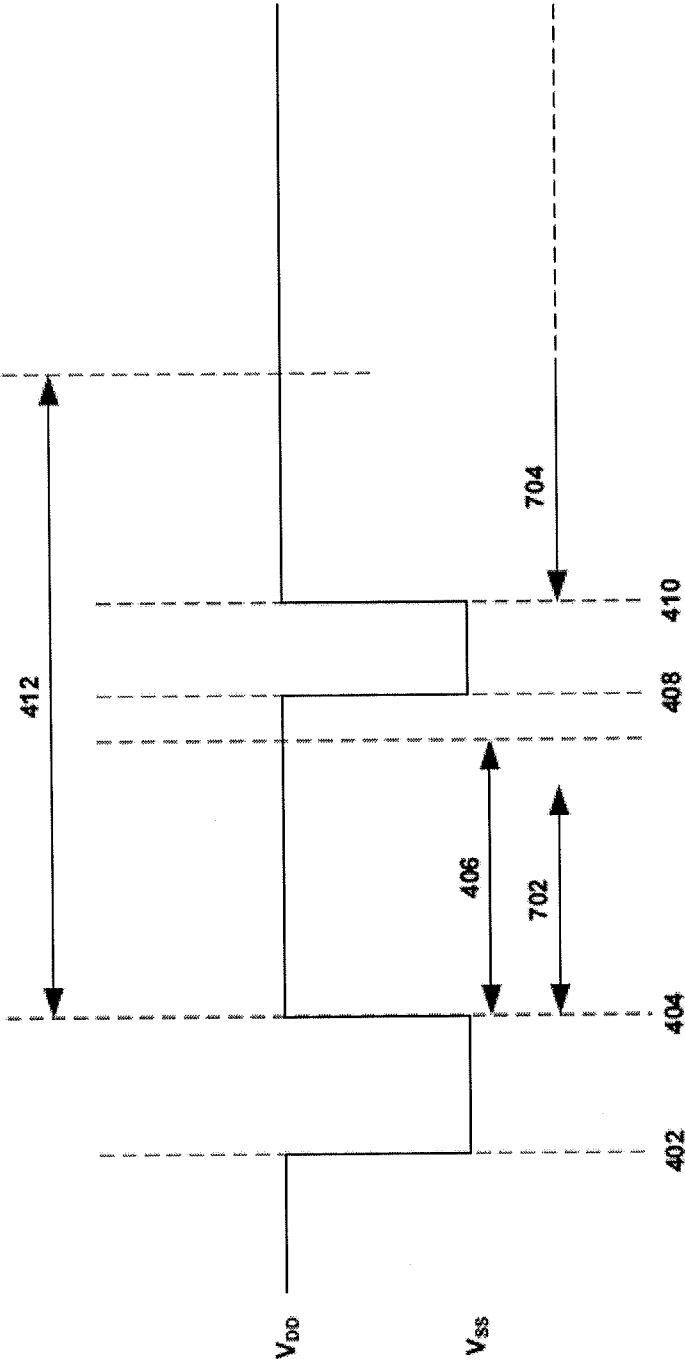
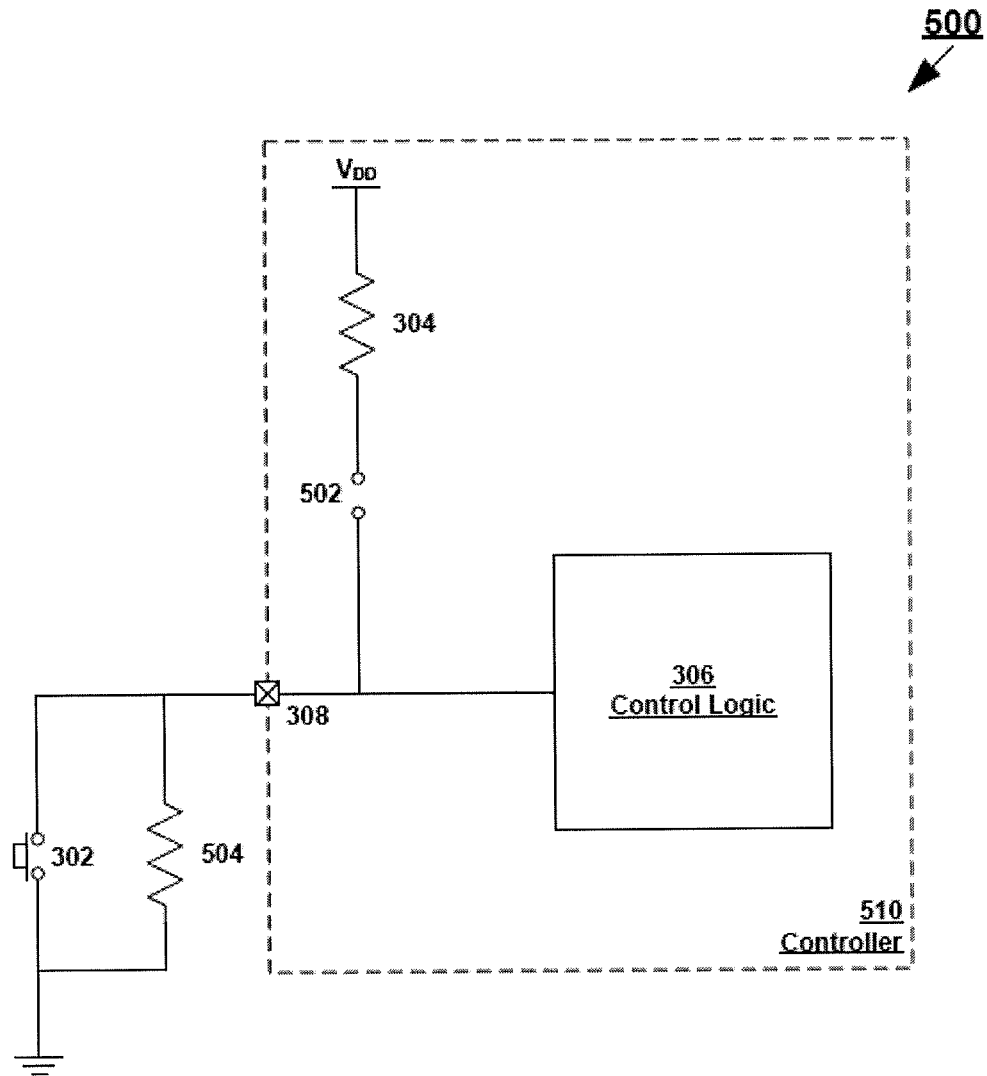


FIG. 7



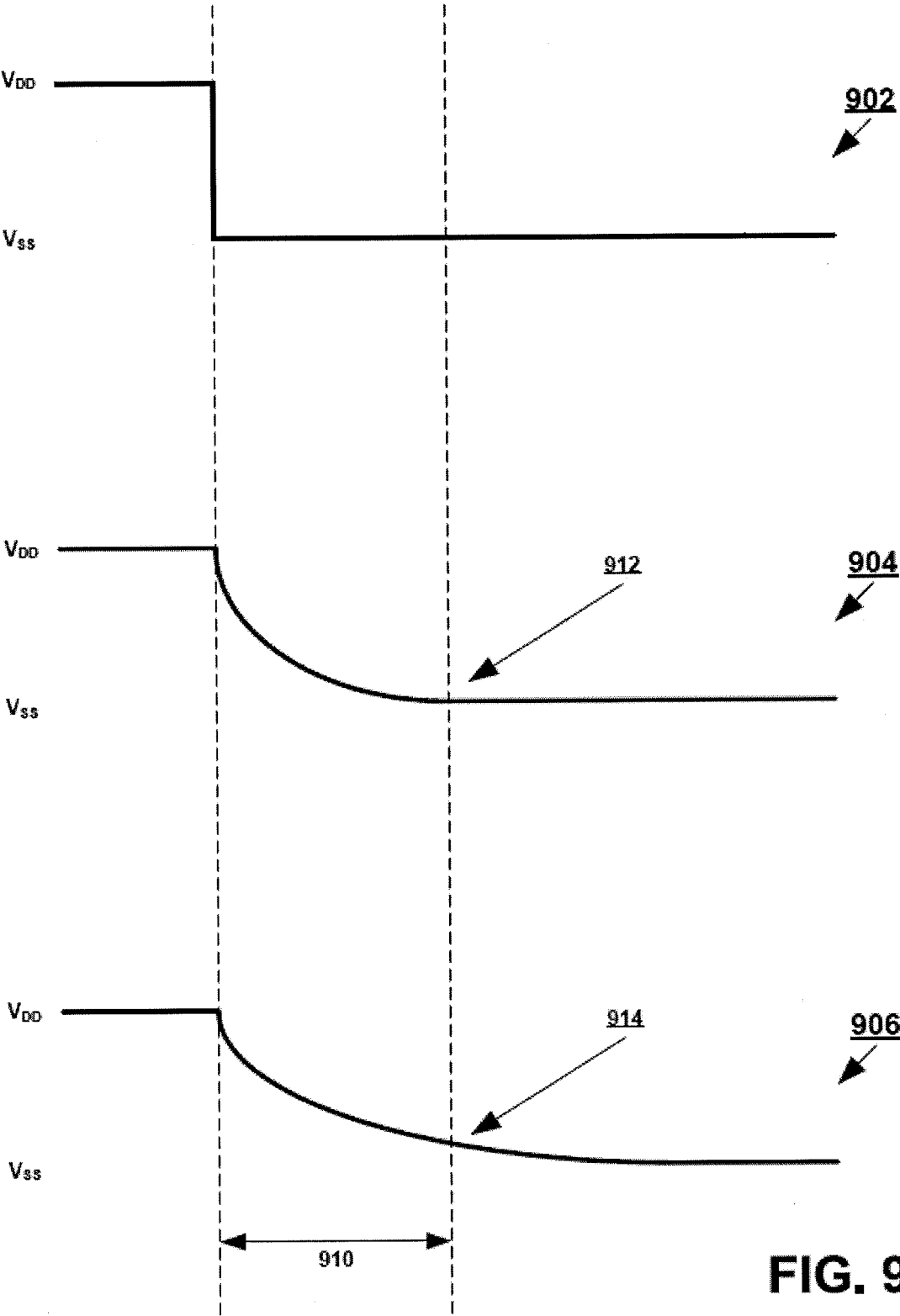


FIG. 9

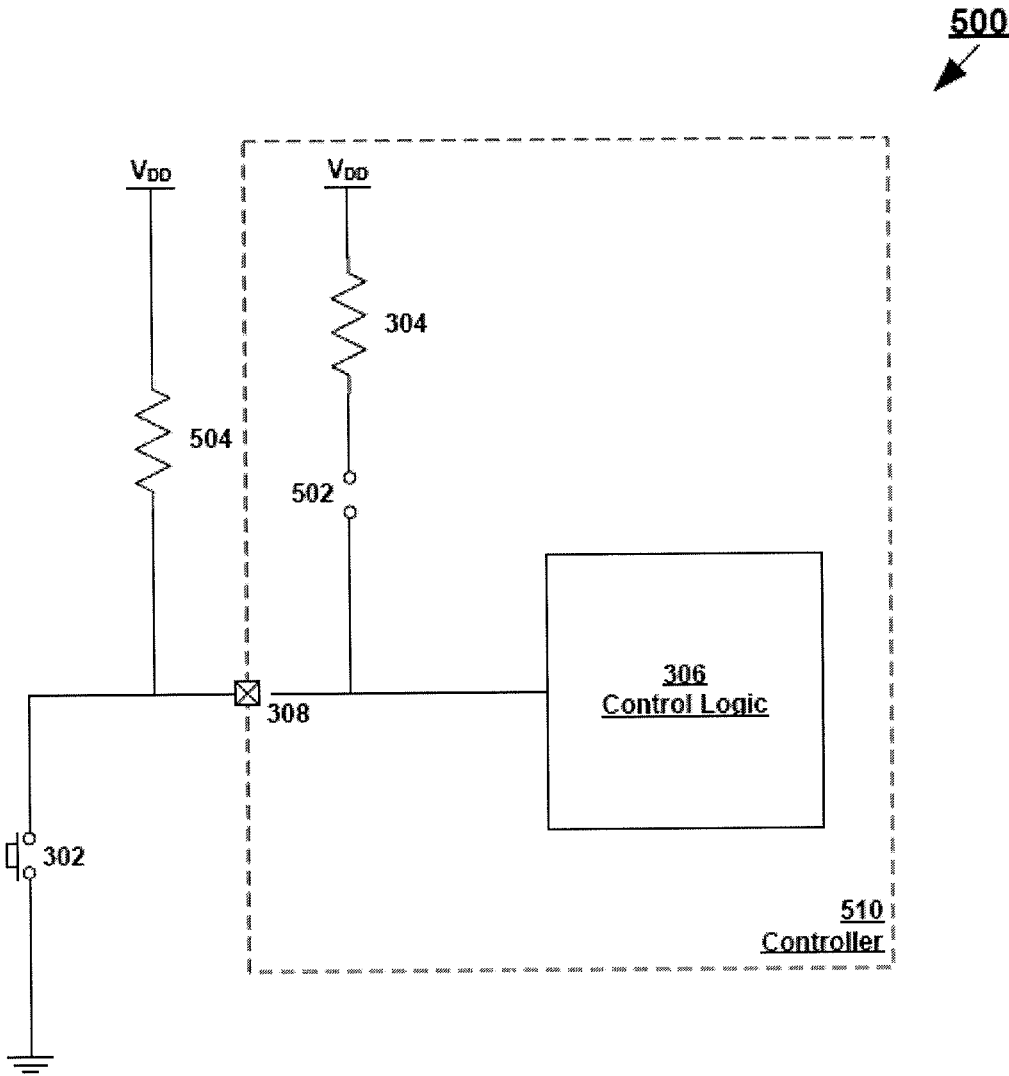


FIG. 10

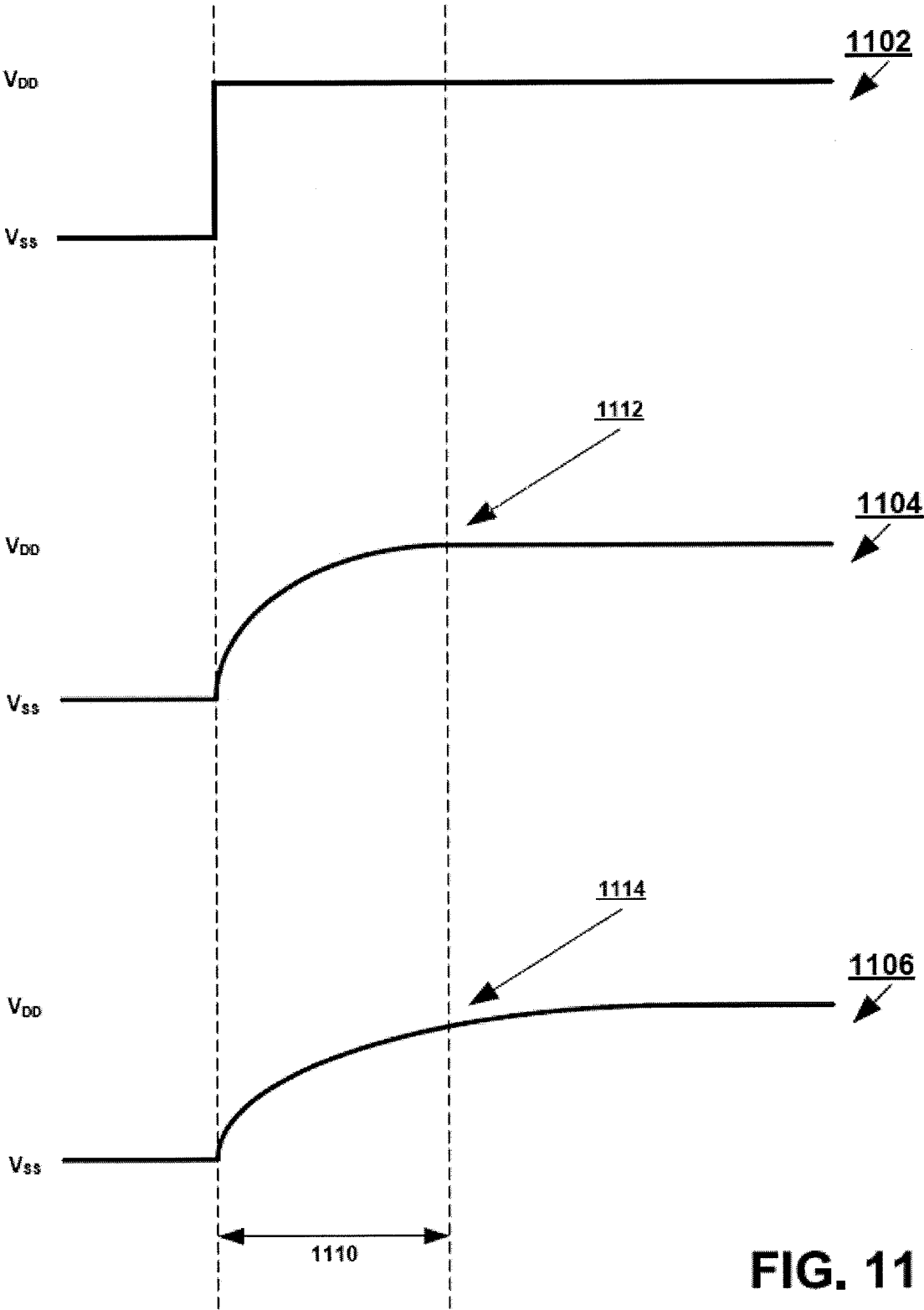


FIG. 11

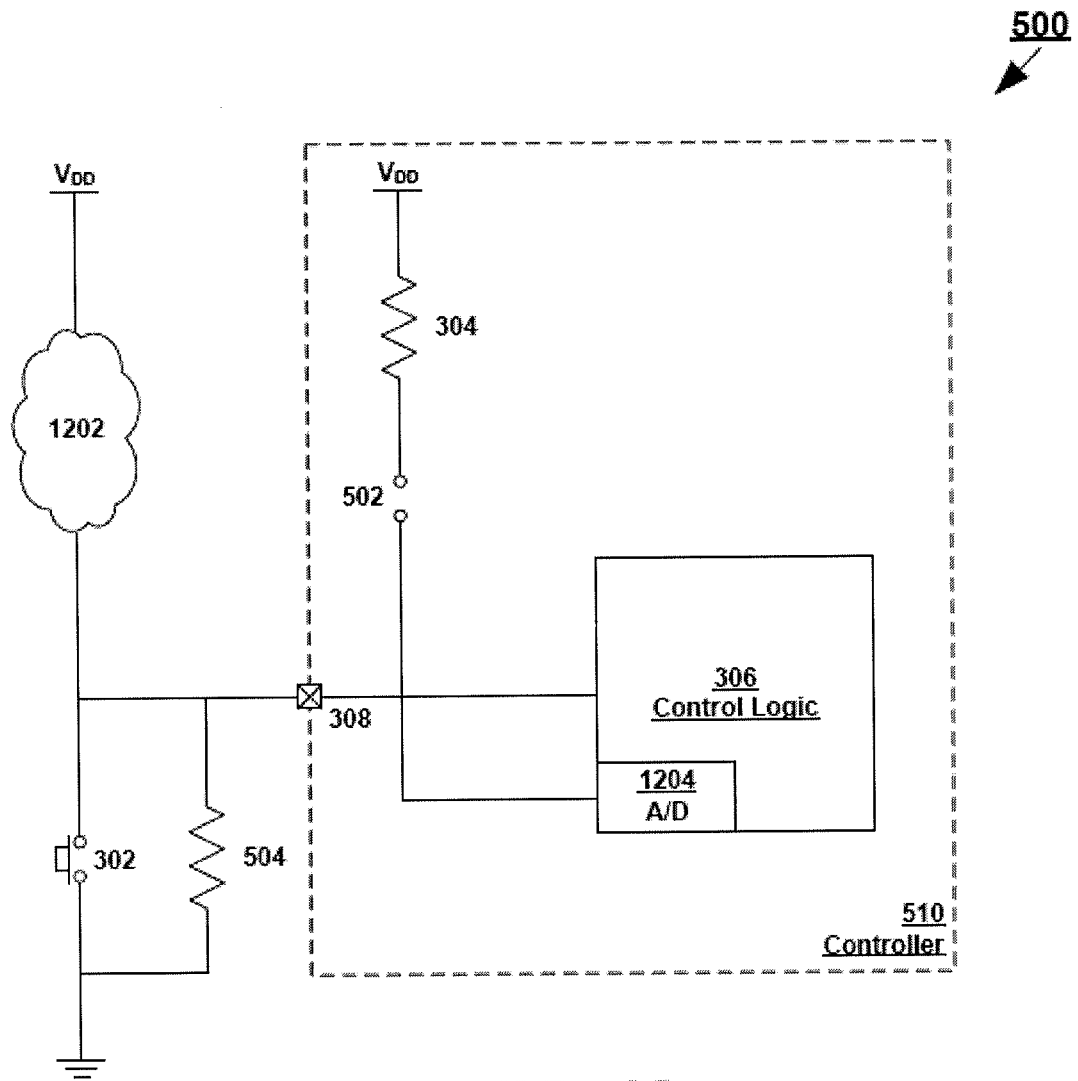


FIG. 12

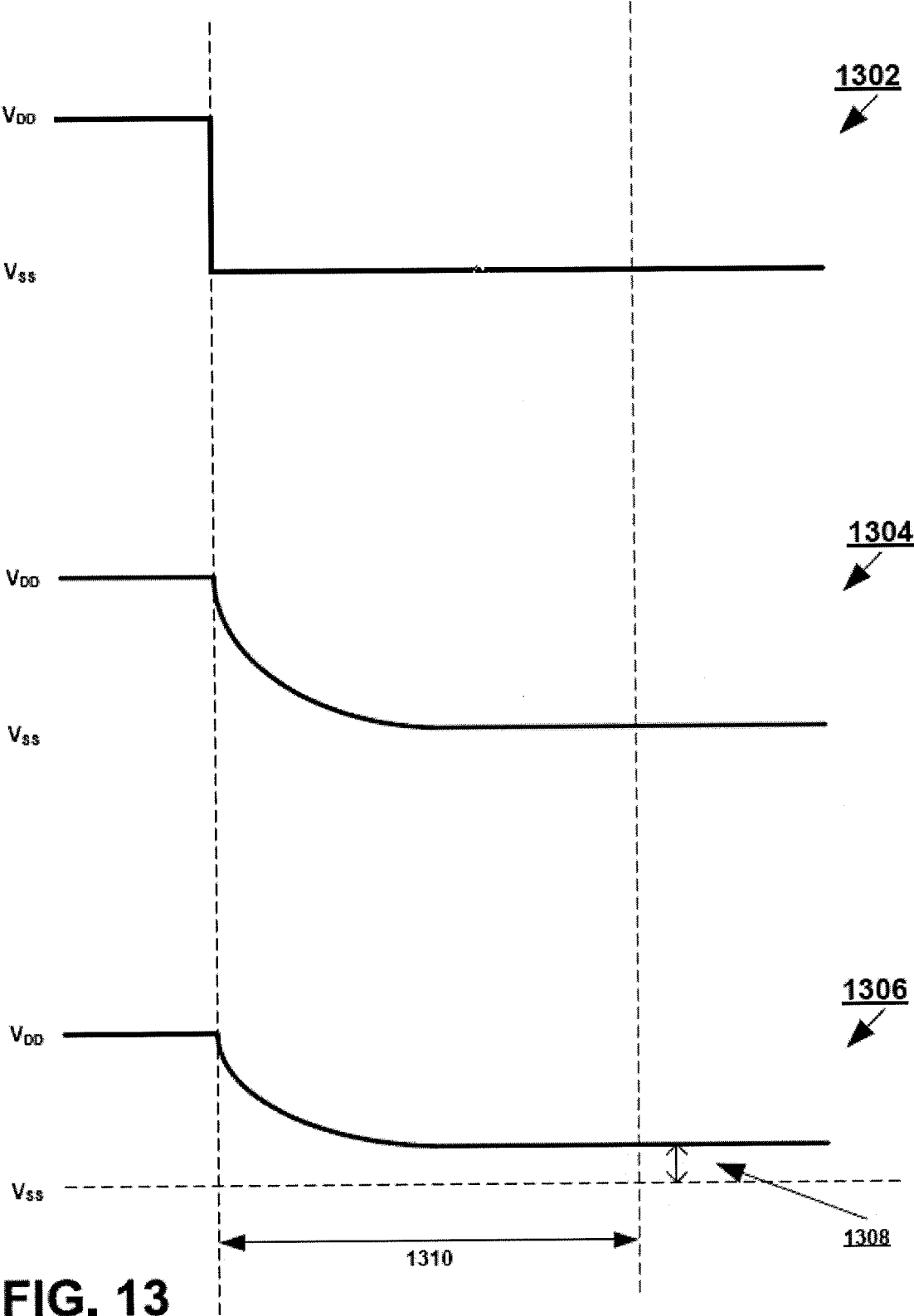
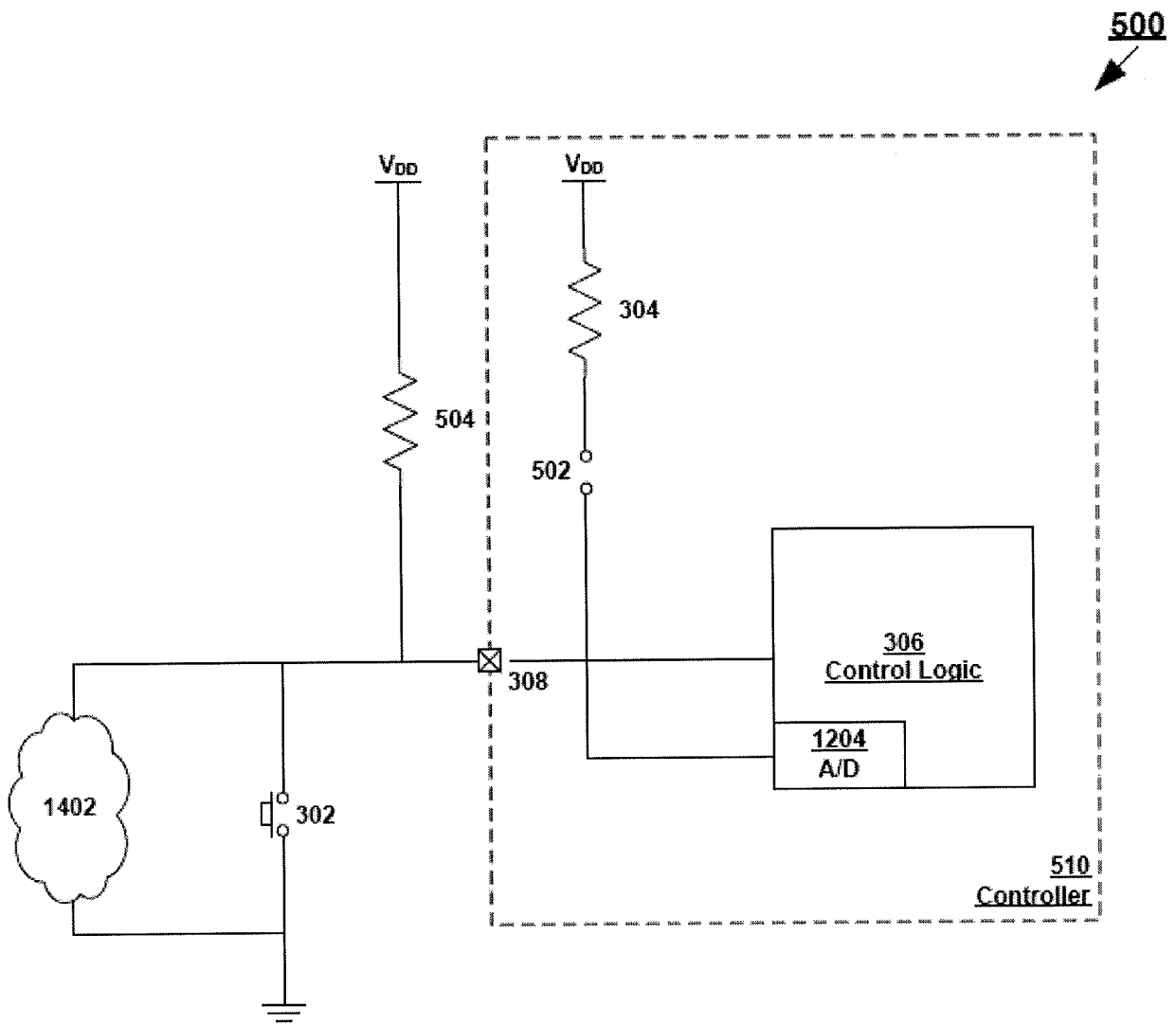


FIG. 13

**FIG. 14**

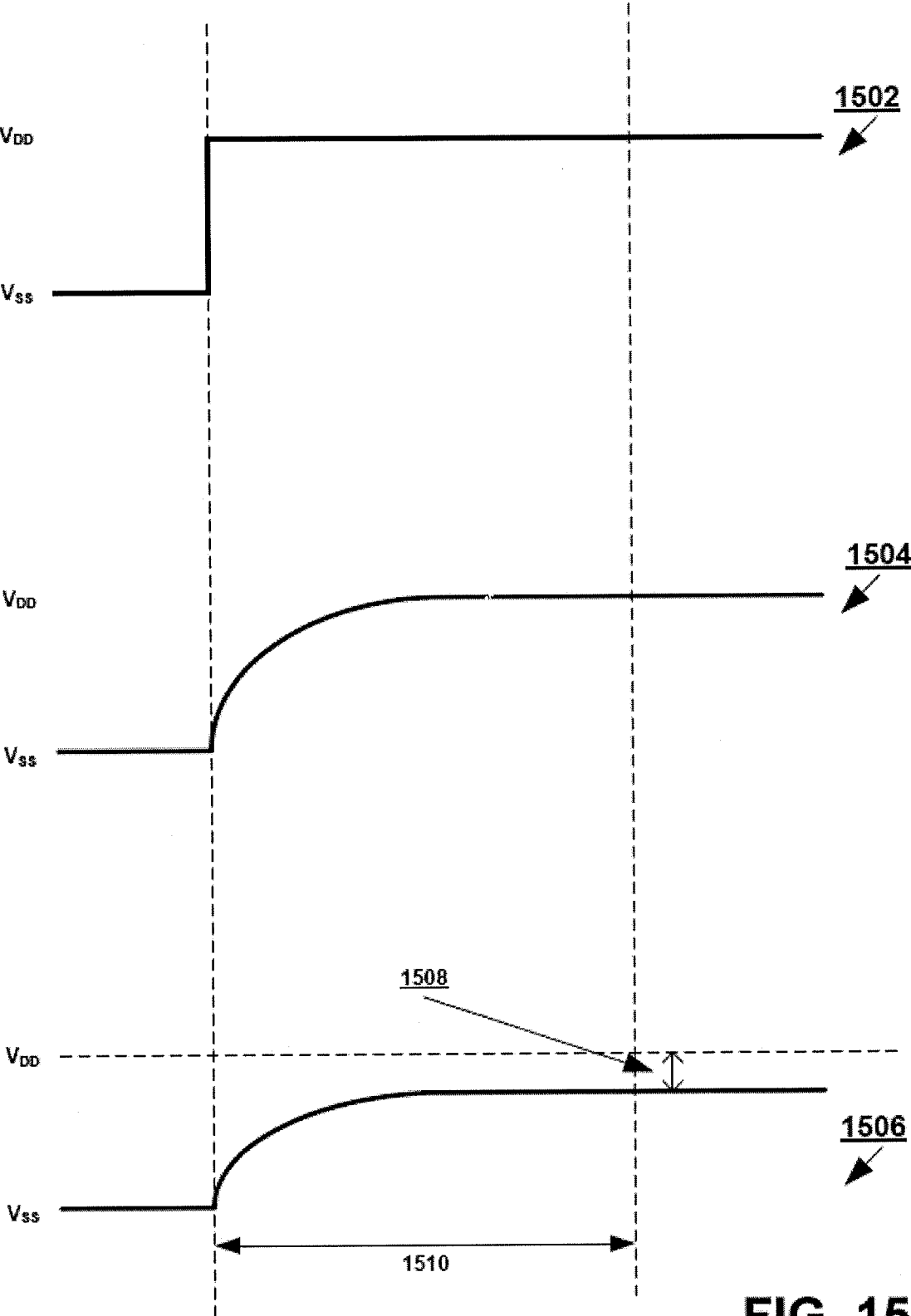
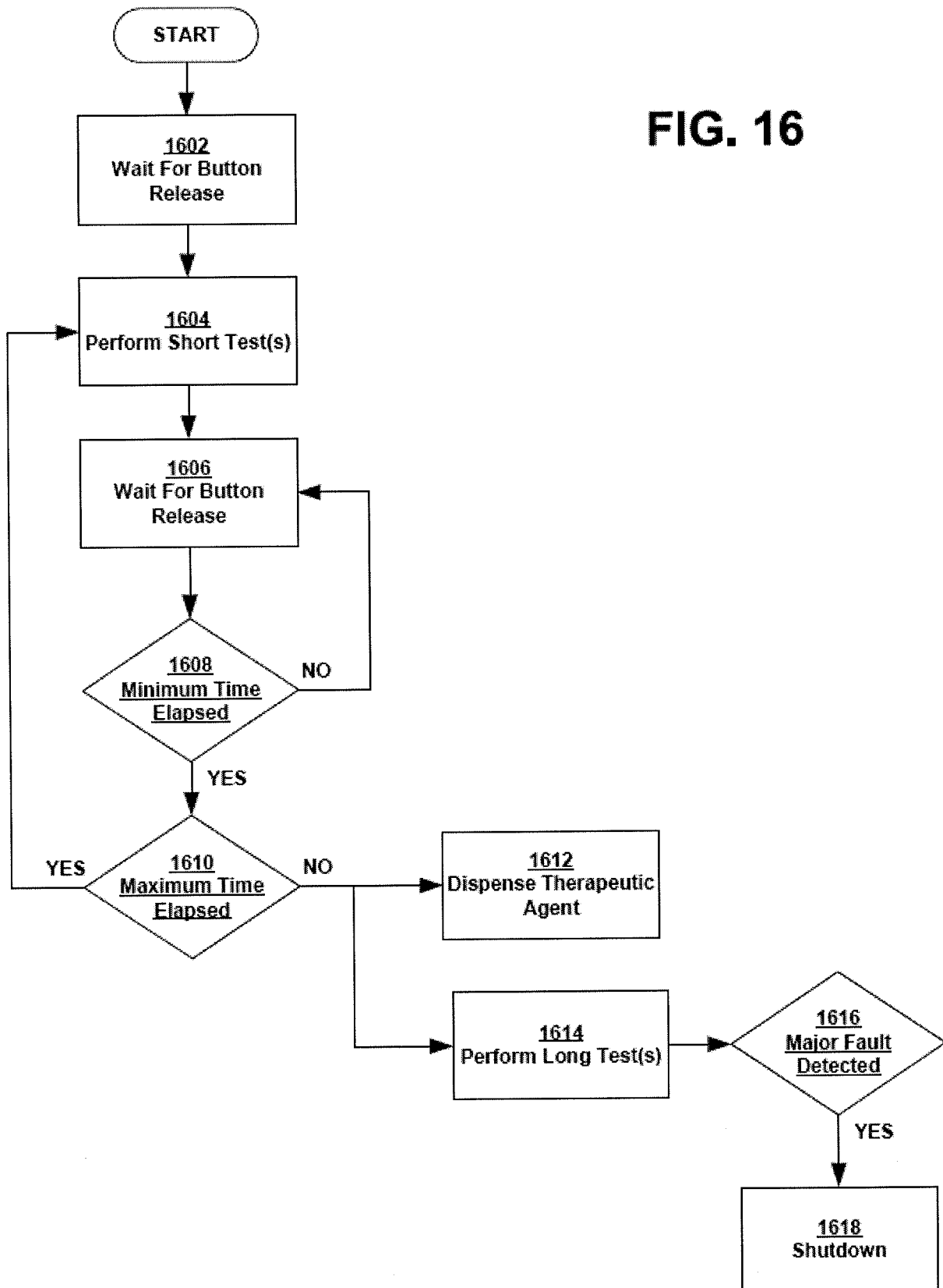


FIG. 15

FIG. 16



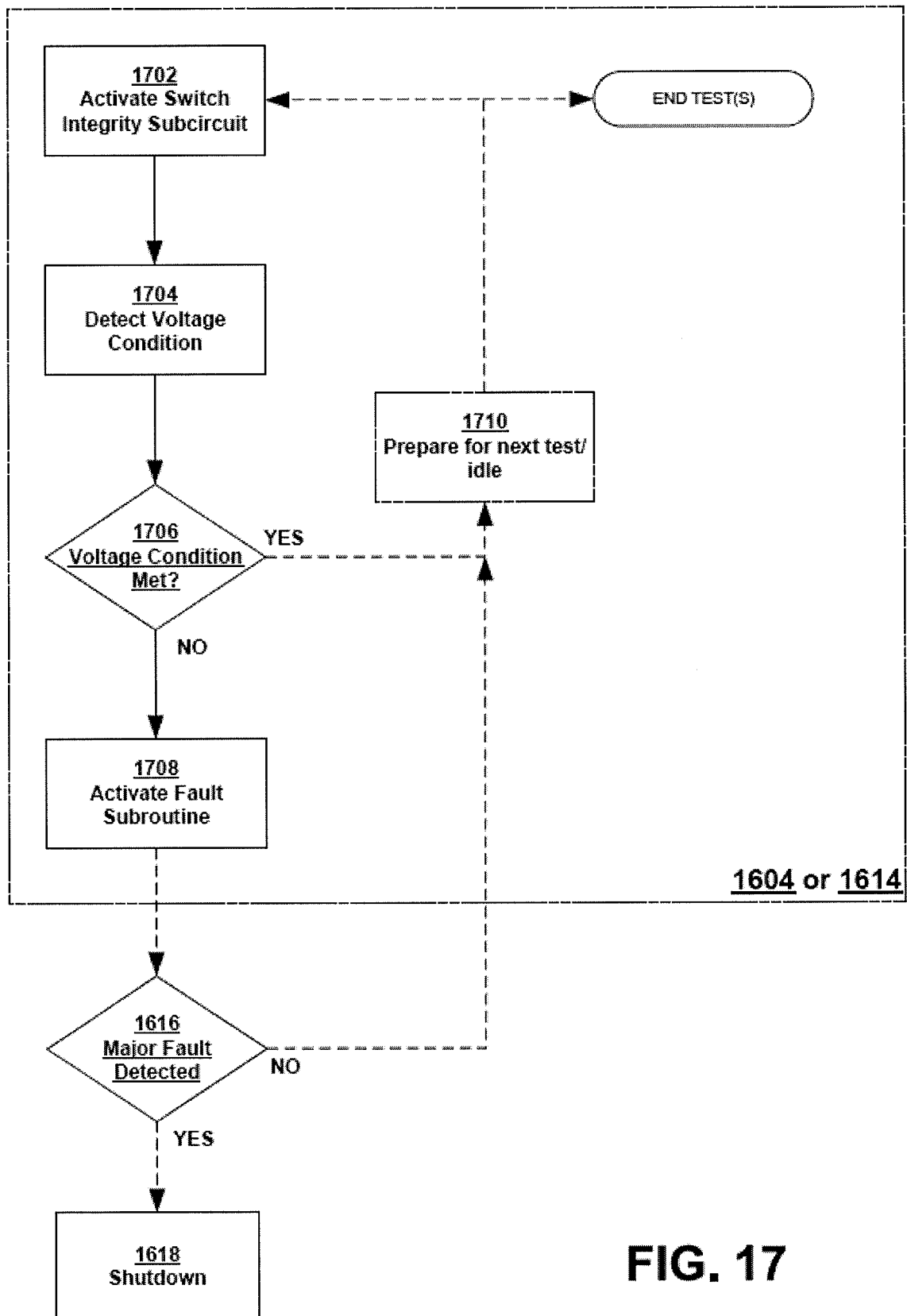


FIG. 17

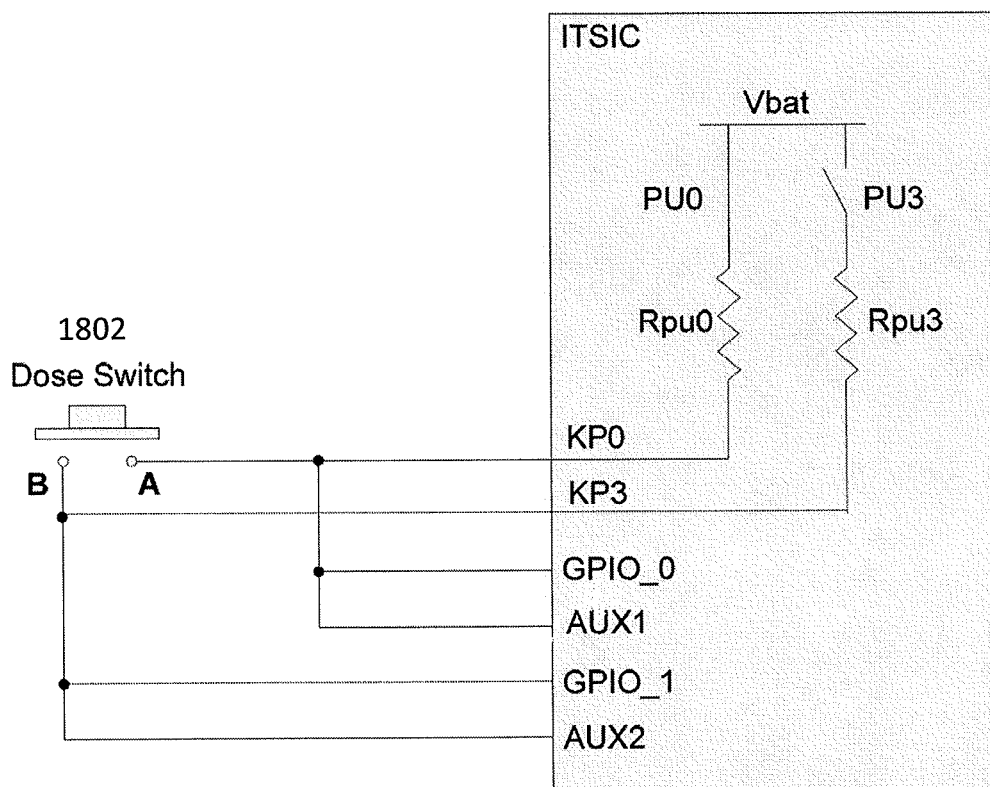
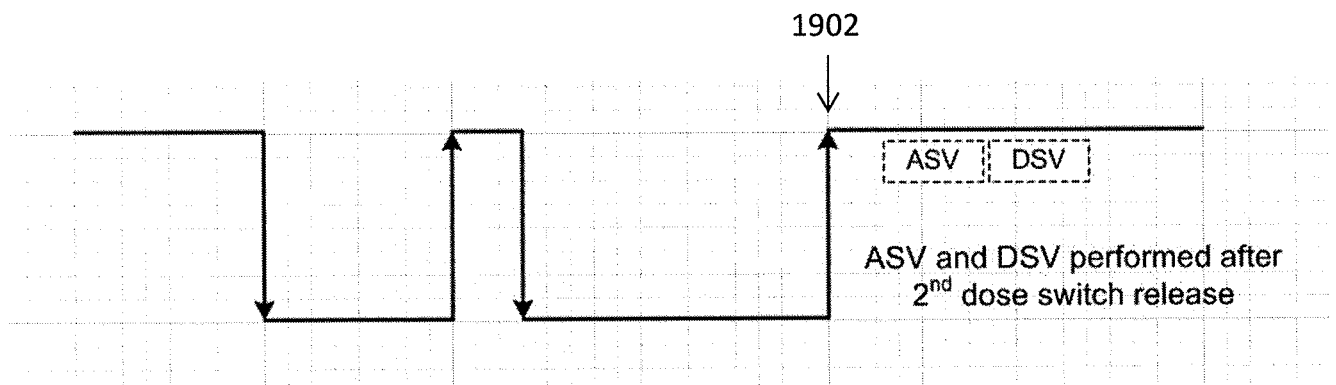
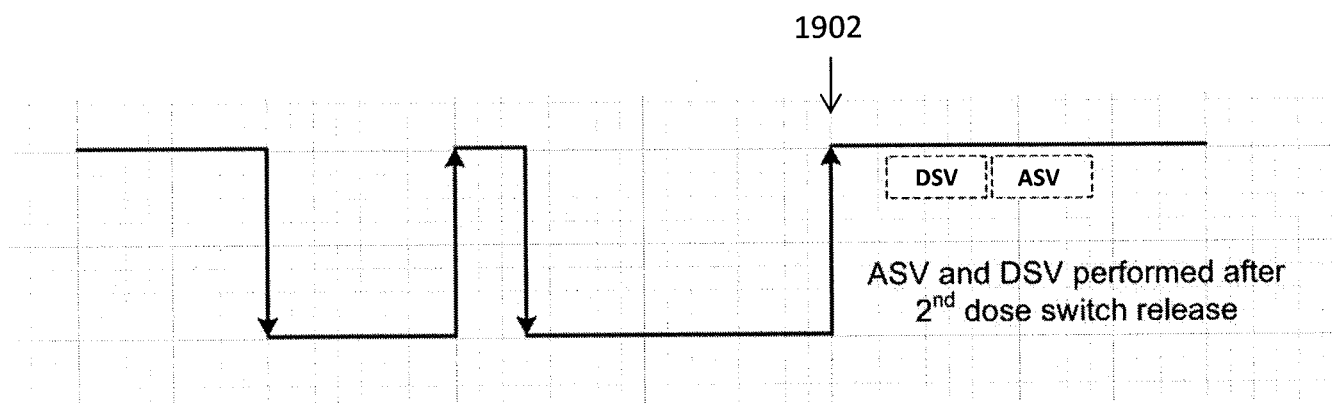
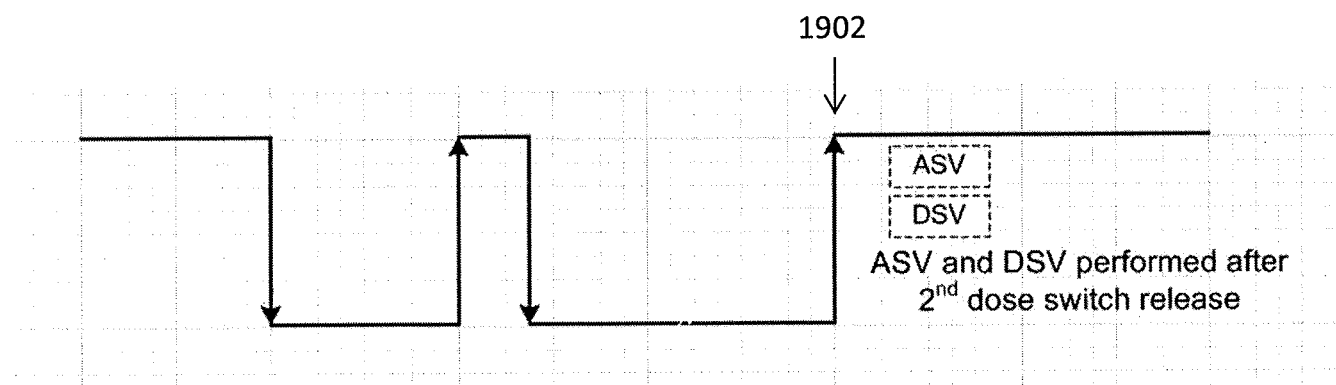


FIG. 18A

Pin #	Name	Type	Description
18	KP0	Digital Input	Vbat bias of switch using the internal pull-up for KP0; Dose switch input (high side) to ITSIC
21	KP3	Digital Input	Dose switch (low side) input to ITSIC
22	GPIO[0]	Digital I/O	Redundant dose switch (high side) input to ITSIC
23	GPIO[1]	Digital I/O	Configured as an OUTPUT-LOW to bias the low side of the switch
11	AUX1	Analog	Analog input of the high side of the switch
12	AUX2	Analog	Analog input of the low side of the switch

FIG. 18B

**FIG. 19A****FIG. 19B****FIG. 19C**