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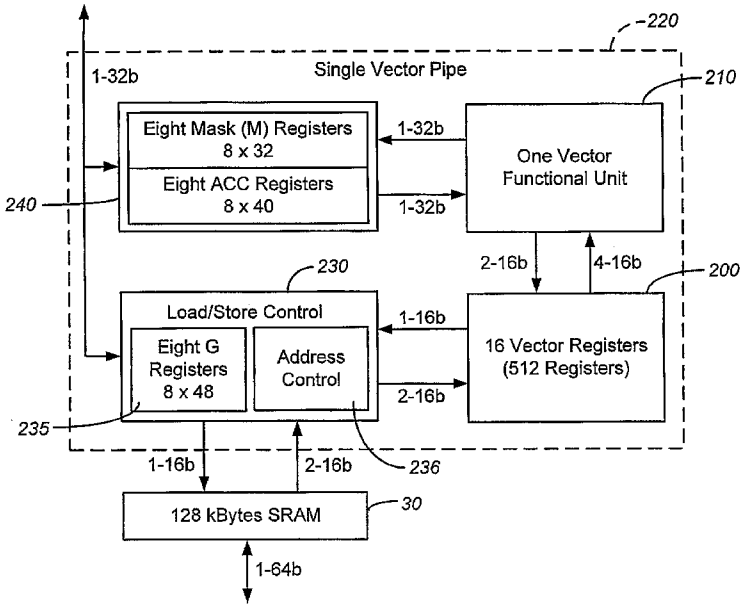
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(54) **Title:** VECTOR PROCESSOR WITH SPECIAL PURPOSE REGISTERS AND HIGH SPEED MEMORY ACCESS



(57) **Abstract:** A vector processor (20) includes a set of vector registers (200) for storing data to be used in the execution of instructions and a vector functional unit (210) coupled to the vector registers (200) for executing instructions. The functional unit (210) executes instructions using operation codes provided to it which operation codes include a field referencing a special register (235). The special register (235) contains information about the length and starting point for each vector instruction. The processor (20) includes a high speed memory access system (130) to facilitate faster operation.

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