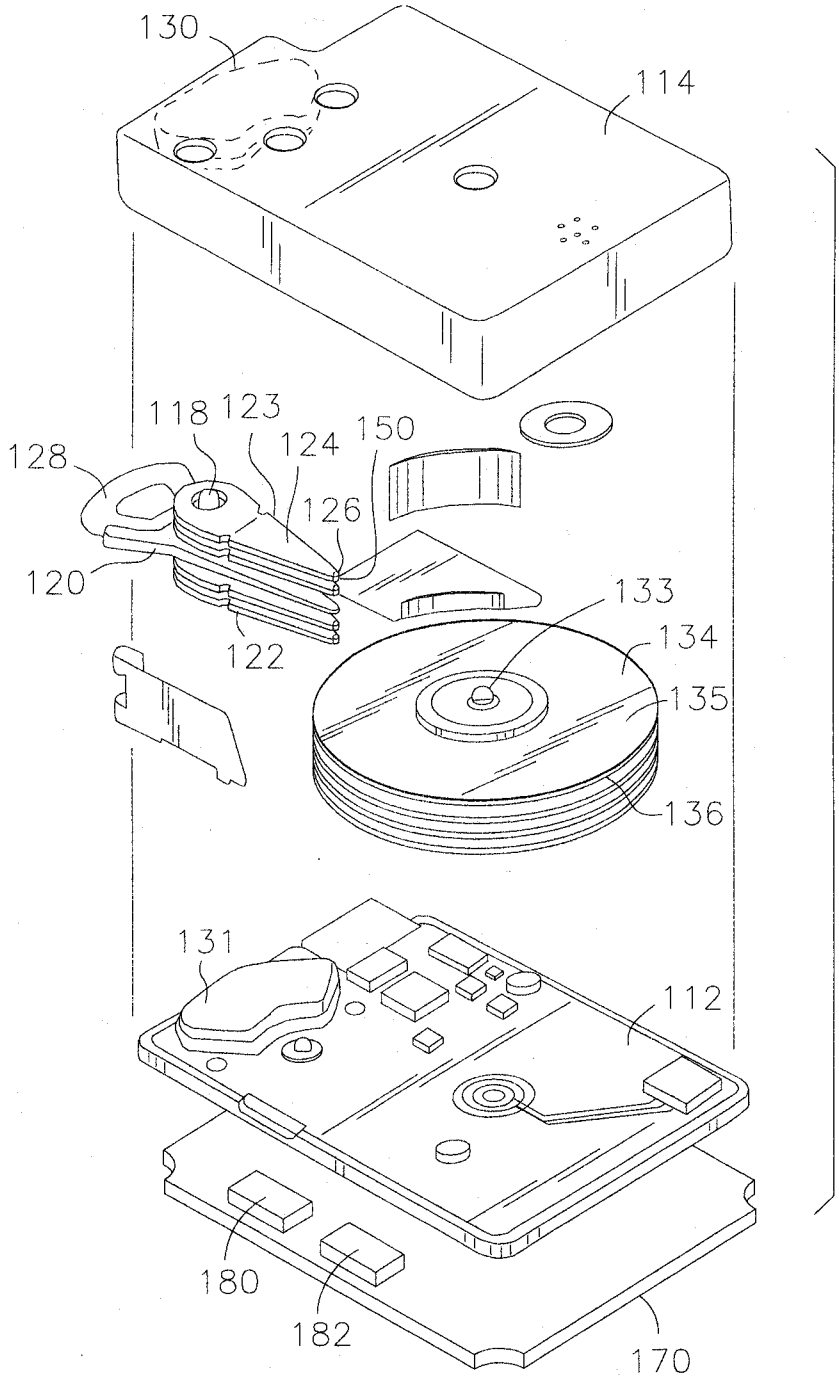

130
114
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124
150
128
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122
133
134
135
136
131
112
180
182
170

FIG. 1

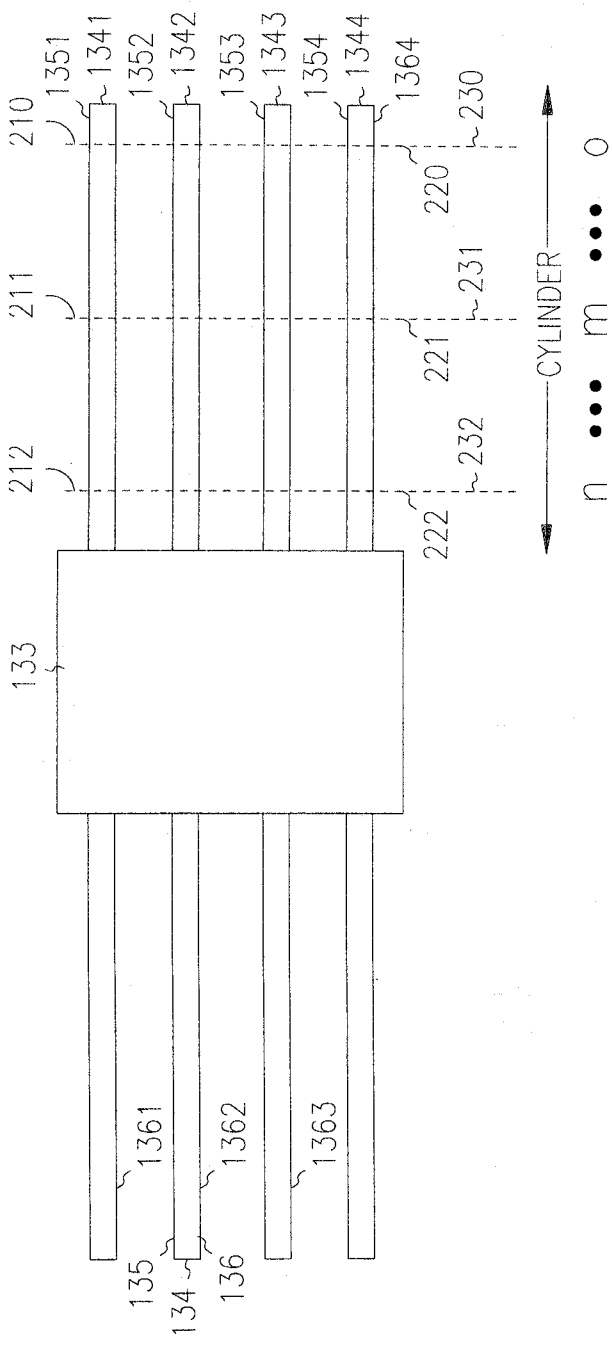

210
211
212
1351
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1352
1342
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1343
1354
1344
1364
230
220
231
221
232
222
CYLINDER
n ••• m ••• 0
133
1361
1362
1363
135
134
136

FIG. 2

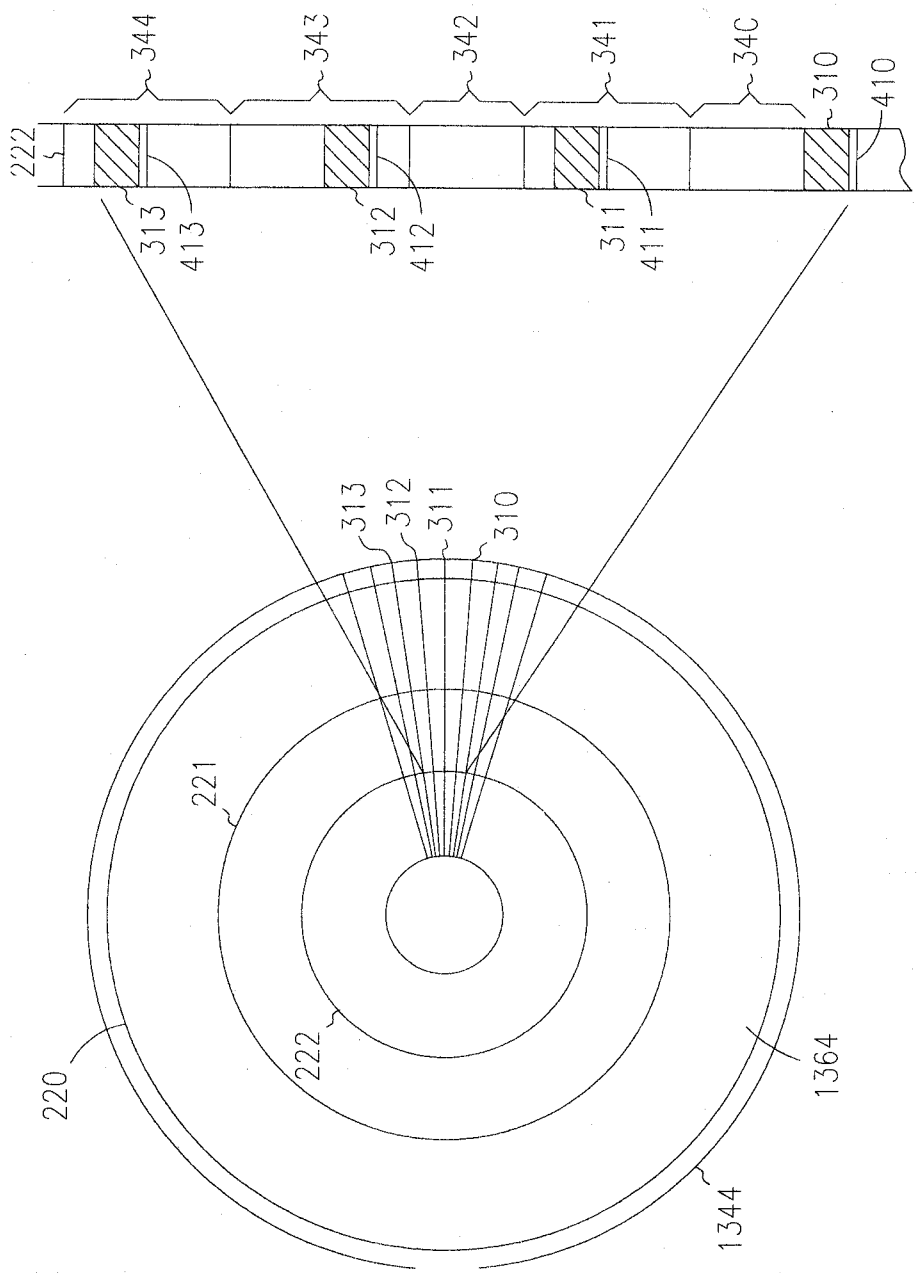

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340
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410
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312
311
310
221
222
220
1364
1344

FIG. 4

FIG. 3

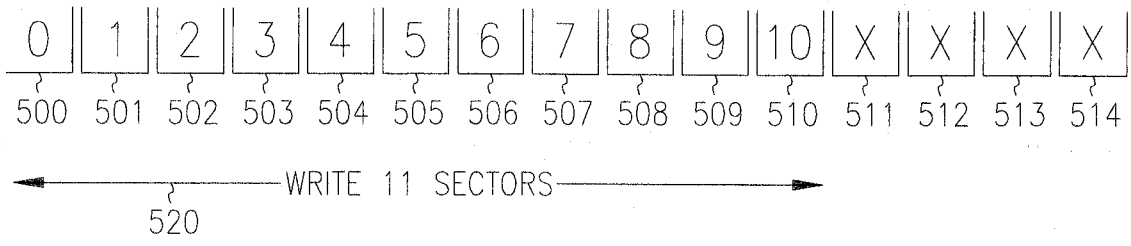

0 1 2 3 4 5 6 7 8 9 10 X X X X
500 501 502 503 504 505 506 507 508 509 510 511 512 513 514
WRITE 11 SECTORS
520

FIG. 5

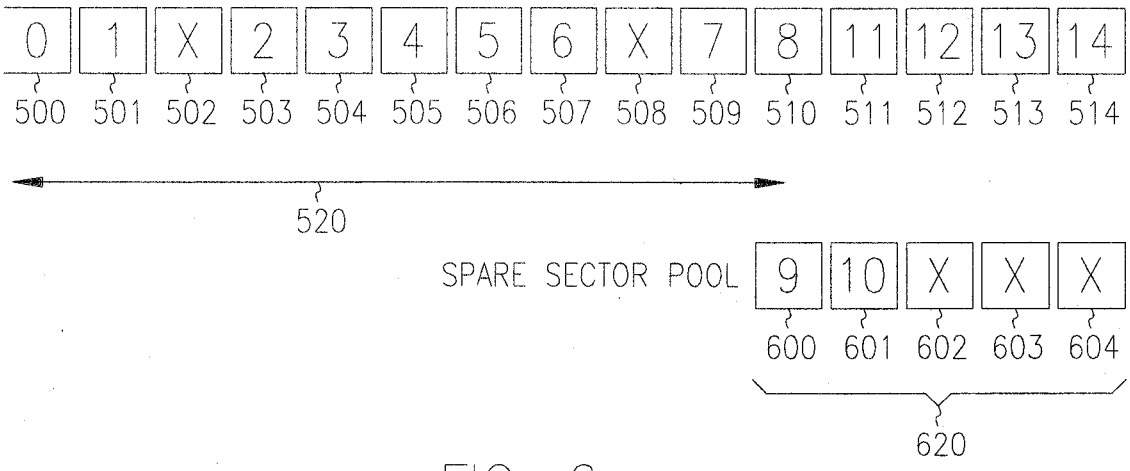

0 1 X 2 3 4 5 6 X 7 8 11 12 13 14
500 501 502 503 504 505 506 507 508 509 510 511 512 513 514
520
SPARE SECTOR POOL
9 10 X X X
600 601 602 603 604
620

FIG. 6

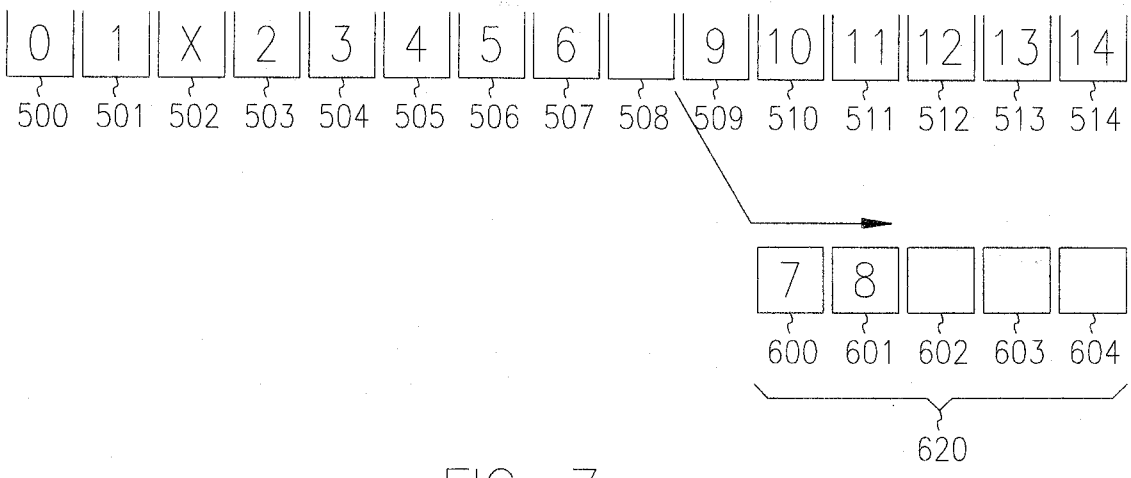

0 1 X 2 3 4 5 6 9 10 11 12 13 14
500 501 502 503 504 505 506 507 508 509 510 511 512 513 514
7 8
600 601 602 603 604
620

FIG. 7

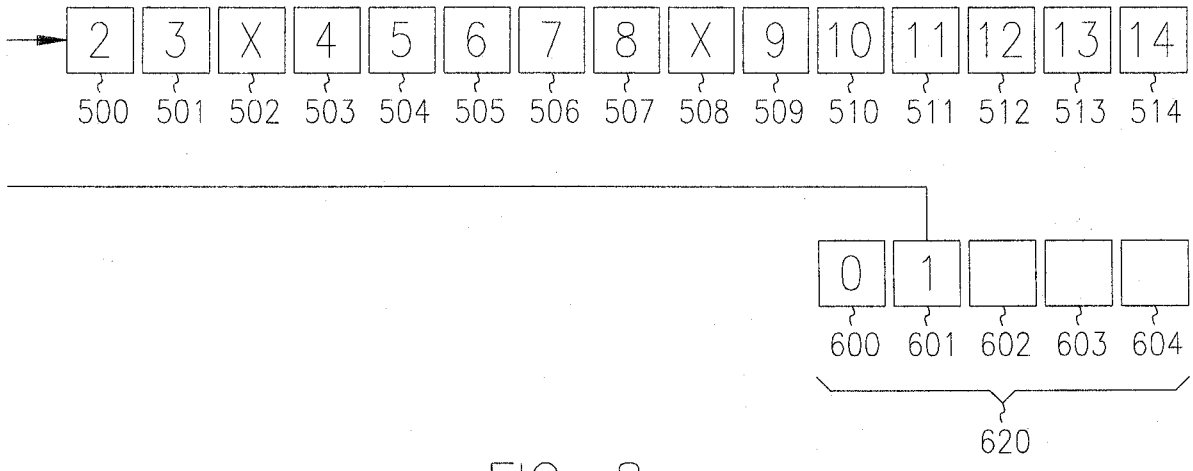

2 3 X 4 5 6 7 8 X 9 10 11 12 13 14
500 501 502 503 504 505 506 507 508 509 510 511 512 513 514
0 1
600 601 602 603 604
620

FIG. 8

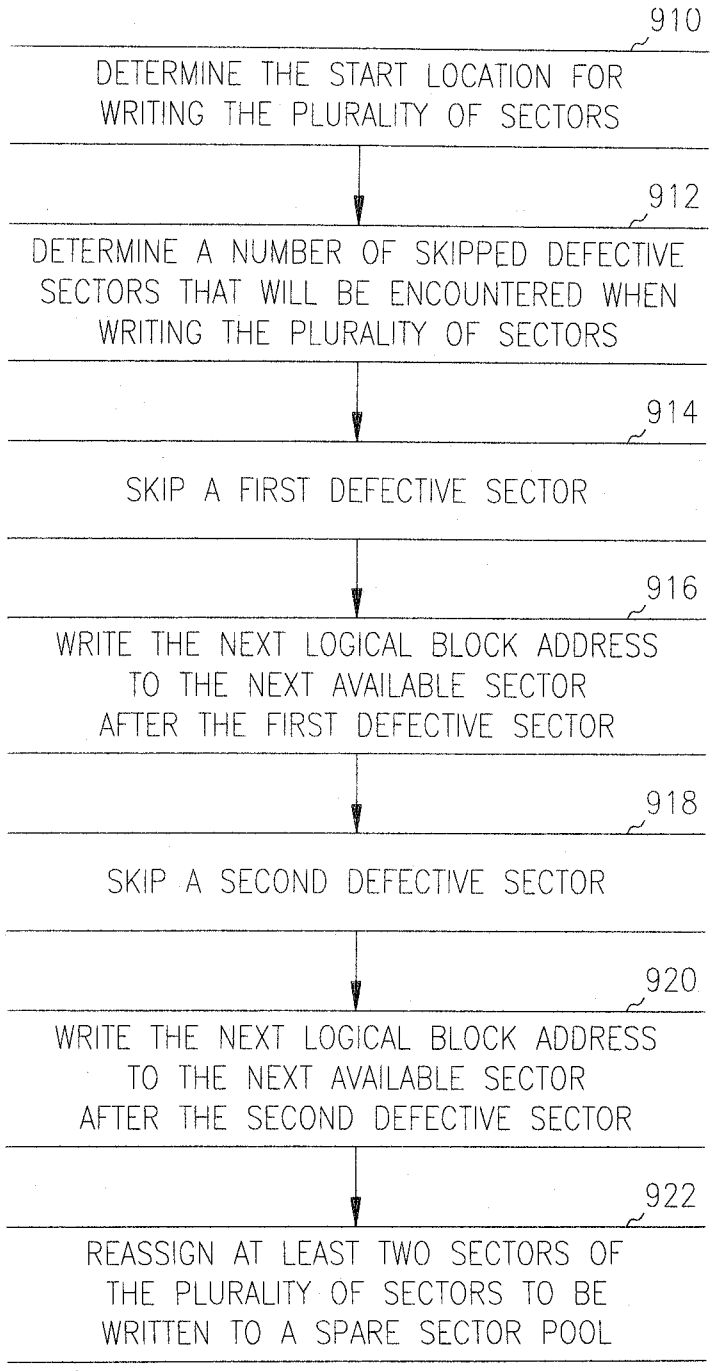

910
DETERMINE THE START LOCATION FOR WRITING THE PLURALITY OF SECTORS
912
DETERMINE A NUMBER OF SKIPPED DEFECTIVE SECTORS THAT WILL BE ENCOUNTERED WHEN WRITING THE PLURALITY OF SECTORS
914
SKIP A FIRST DEFECTIVE SECTOR
916
WRITE THE NEXT LOGICAL BLOCK ADDRESS TO THE NEXT AVAILABLE SECTOR AFTER THE FIRST DEFECTIVE SECTOR
918
SKIP A SECOND DEFECTIVE SECTOR
920
WRITE THE NEXT LOGICAL BLOCK ADDRESS TO THE NEXT AVAILABLE SECTOR AFTER THE SECOND DEFECTIVE SECTOR
922
REASSIGN AT LEAST TWO SECTORS OF THE PLURALITY OF SECTORS TO BE WRITTEN TO A SPARE SECTOR POOL

FIG. 9

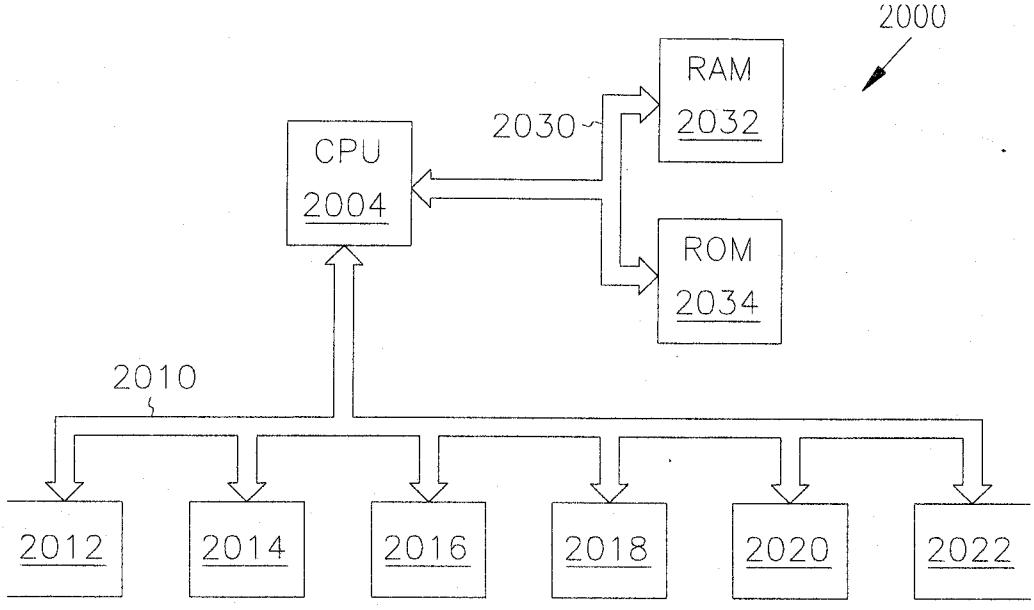

2000
RAM
2032
2030
CPU
2004
ROM
2034
2010
2012
2014
2016
2018
2020
2022

FIG. 10