

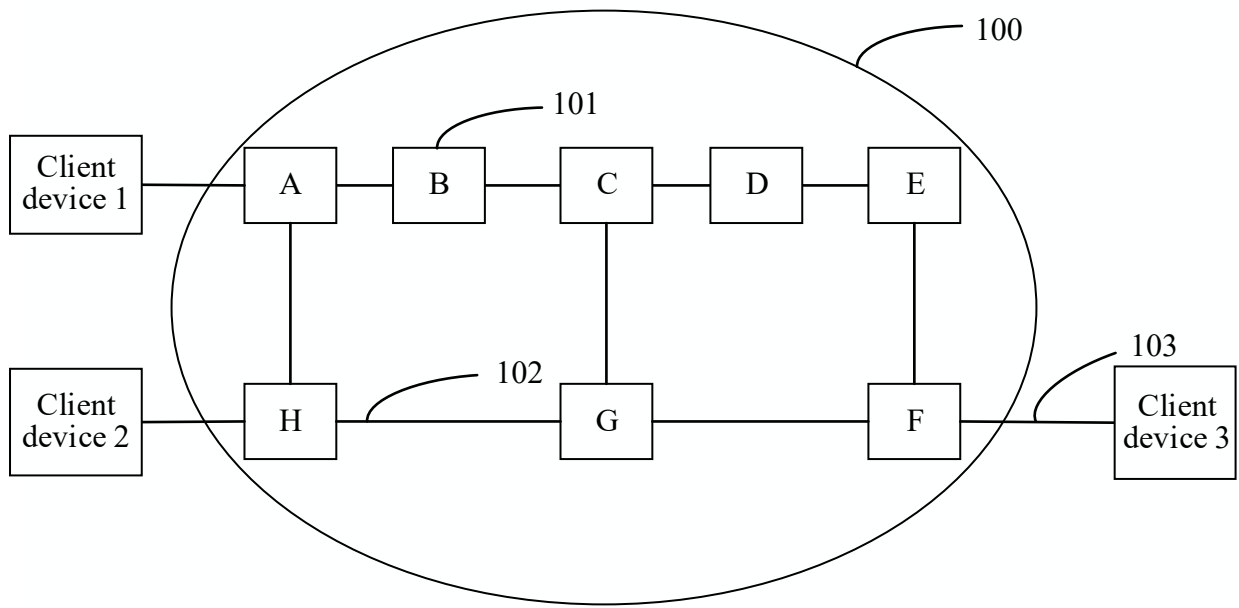


FIG. 1

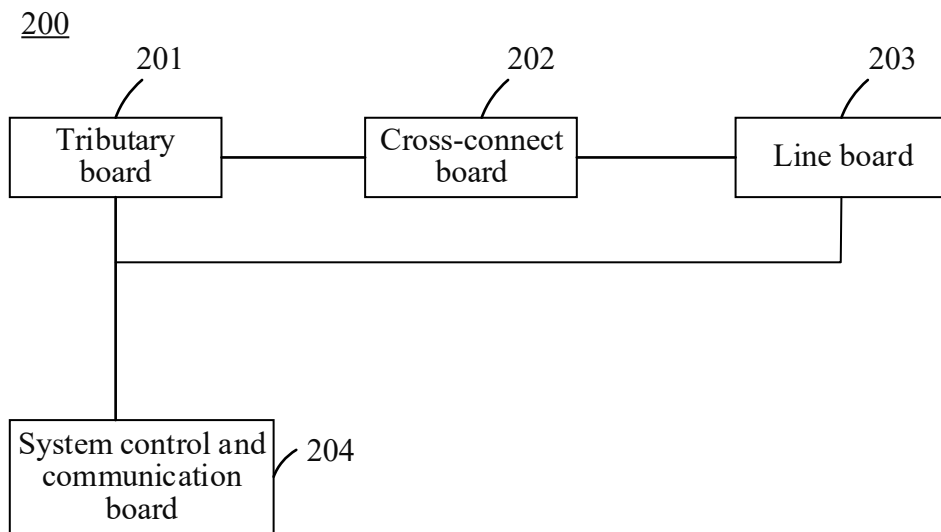


FIG. 2

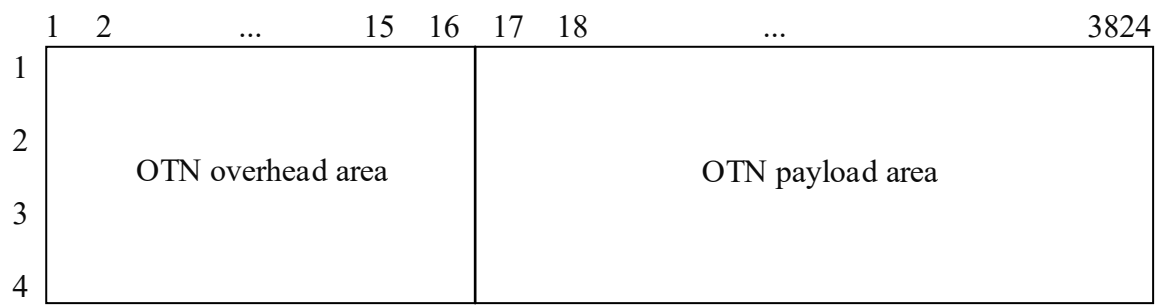


FIG. 3

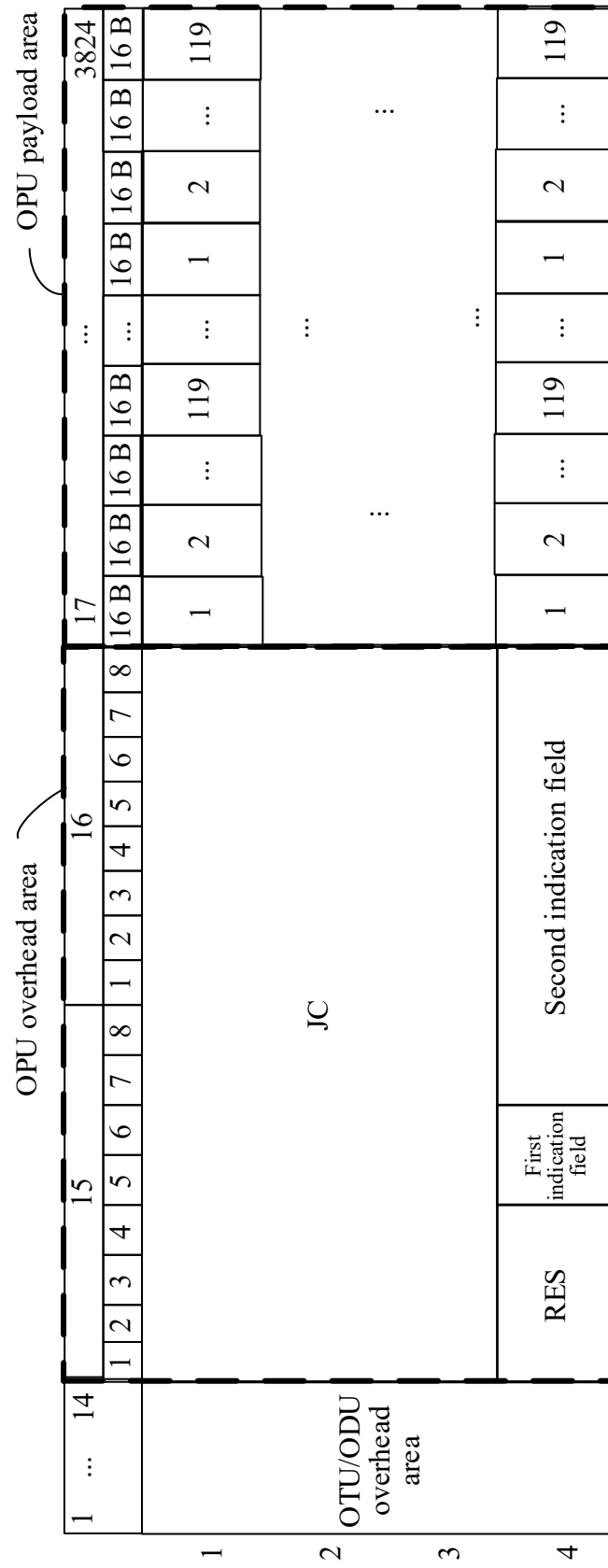


FIG. 4

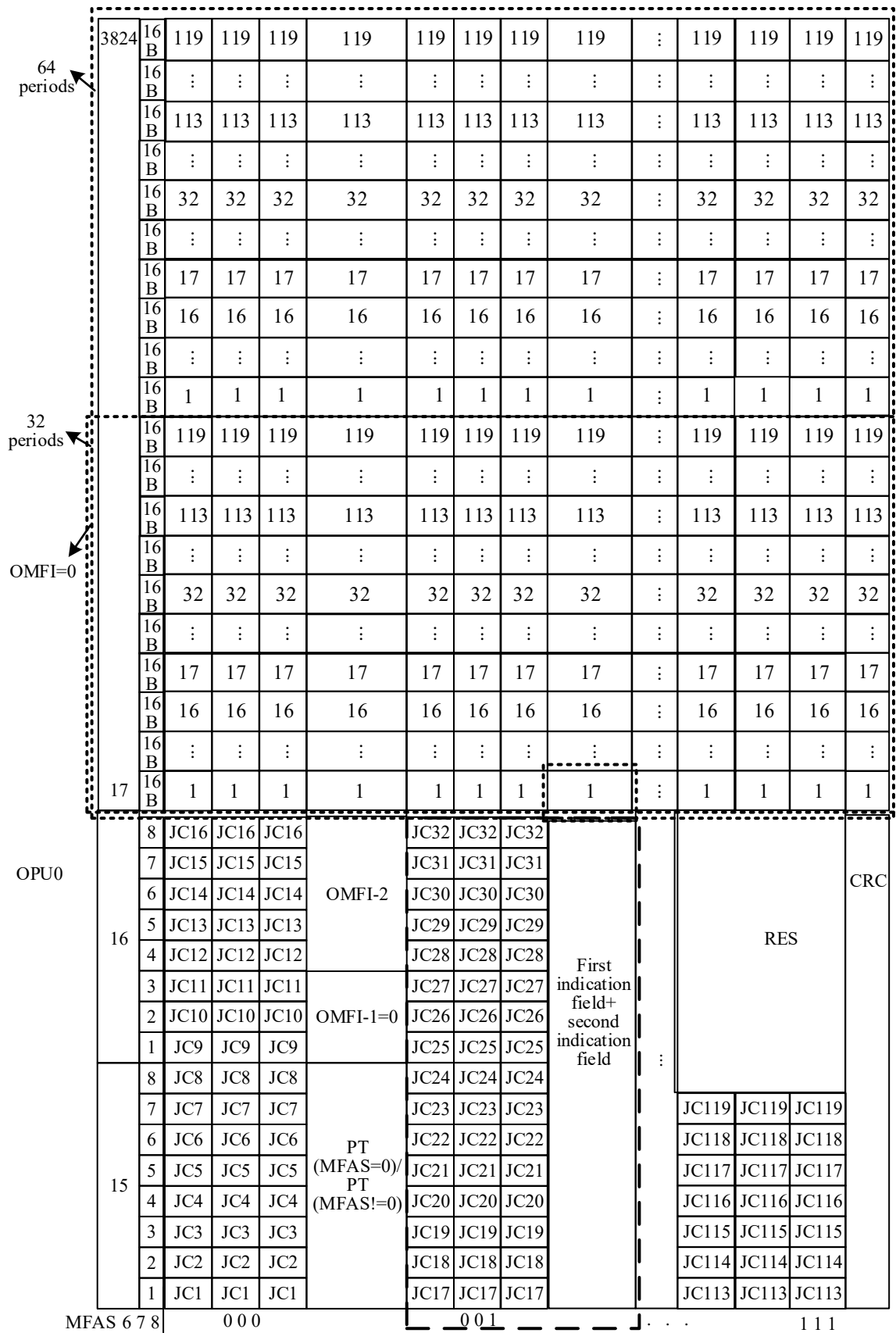


FIG. 5

OPU0

MFAS 6 7 8	15								16							
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
0 0 0	PT (MFAS=0)								OMFI-1=0				OMFI-2=0			
0 0 1	RES				CTRL				TPID of TS #1							
0 1 0	RES				CTRL				TPID of TS #2							
0 1 1	RES				CTRL				TPID of TS #3							
1 0 0	RES				CTRL				TPID of TS #4							
1 0 1	RES				CTRL				TPID of TS #5							
1 1 0	RES				CTRL				TPID of TS #6							
1 1 1	CRC 16															
0 0 0	RES (MFAS! =0)								OMFI-1=0				OMFI-2=1			
0 0 1	RES				CTRL				TPID of TS #7							
0 1 0	RES				CTRL				TPID of TS #8							
0 1 1	RES				CTRL				TPID of TS #9							
1 0 0	RES				CTRL				TPID of TS #10							
1 0 1	RES				CTRL				TPID of TS #11							
1 1 0	RES				CTRL				TPID of TS #12							
1 1 1	CRC 16															
...	...															
0 0 0	RES (MFAS! =0)								OMFI-1=0				OMFI-2=19			
0 0 1	RES				CTRL				TPID of TS #115							
0 1 0	RES				CTRL				TPID of TS #116							
0 1 1	RES				CTRL				TPID of TS #117							
1 0 0	RES				CTRL				TPID of TS #118							
1 0 1	RES				CTRL				TPID of TS #119							
1 1 0	RES				CTRL				RES							
1 1 1	CRC 16															

FIG. 6

[illegible]

FIG. 7

OPUflex (2*OPU0)																	
MFAS	15								16								
6 7 8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
0 0 0	PT (MFAS=0)								OMFI-1=0			OMFI-2=0					
0 0 1	RES				CTRL				TPID of TS #1								
0 1 0	RES				CTRL				TPID of TS #2								
0 1 1	RES				CTRL				TPID of TS #3								
1 0 0	RES				CTRL				TPID of TS #4								
1 0 1	RES				CTRL				TPID of TS #5								
1 1 0	RES				CTRL				TPID of TS #6								
1 1 1	CRC 16																
0 0 0	RES (MFAS!=0)								OMFI-1=0			OMFI-2=1					
0 0 1	RES				CTRL				TPID of TS #7								
0 1 0	RES				CTRL				TPID of TS #8								
0 1 1	RES				CTRL				TPID of TS #9								
1 0 0	RES				CTRL				TPID of TS #10								
1 0 1	RES				CTRL				TPID of TS #11								
1 1 0	RES				CTRL				TPID of TS #12								
1 1 1	CRC 16																
...	...																
0 0 0	RES (MFAS!=0)								OMFI-1=0			OMFI-2=19					
0 0 1	RES				CTRL				TPID of TS #115								
0 1 0	RES				CTRL				TPID of TS #116								
0 1 1	RES				CTRL				TPID of TS #117								
1 0 0	RES				CTRL				TPID of TS #118								
1 0 1	RES				CTRL				TPID of TS #119								
1 1 0	RES				CTRL				RES								
1 1 1	CRC 16																
0 0 0	RES (MFAS!=0)								OMFI-1=1			OMFI-2=0					
0 0 1	RES				CTRL				TPID of TS #120								
0 1 0	RES				CTRL				TPID of TS #121								
0 1 1	RES				CTRL				TPID of TS #122								
1 0 0	RES				CTRL				TPID of TS #123								
1 0 1	RES				CTRL				TPID of TS #124								
1 1 0	RES				CTRL				TPID of TS #125								
1 1 1	CRC 16																
0 0 0	PT (MFAS=0)								OMFI-1=1			OMFI-2=1					
0 0 1	RES				CTRL				TPID of TS #126								
0 1 0	RES				CTRL				TPID of TS #127								
0 1 1	RES				CTRL				TPID of TS #128								
1 0 0	RES				CTRL				TPID of TS #129								
1 0 1	RES				CTRL				TPID of TS #130								
1 1 0	RES				CTRL				TPID of TS #131								
1 1 1	CRC 16																
...	...																
0 0 0	PT (MFAS=0)								OMFI-1=1			OMFI-2=19					
0 0 1	RES				CTRL				TPID of TS #234								
0 1 0	RES				CTRL				TPID of TS #235								
0 1 1	RES				CTRL				TPID of TS #236								
1 0 0	RES				CTRL				TPID of TS #237								
1 0 1	RES				CTRL				TPID of TS #238								
1 1 0	RES				CTRL				RES								
1 1 1	CRC 16																

FIG. 8

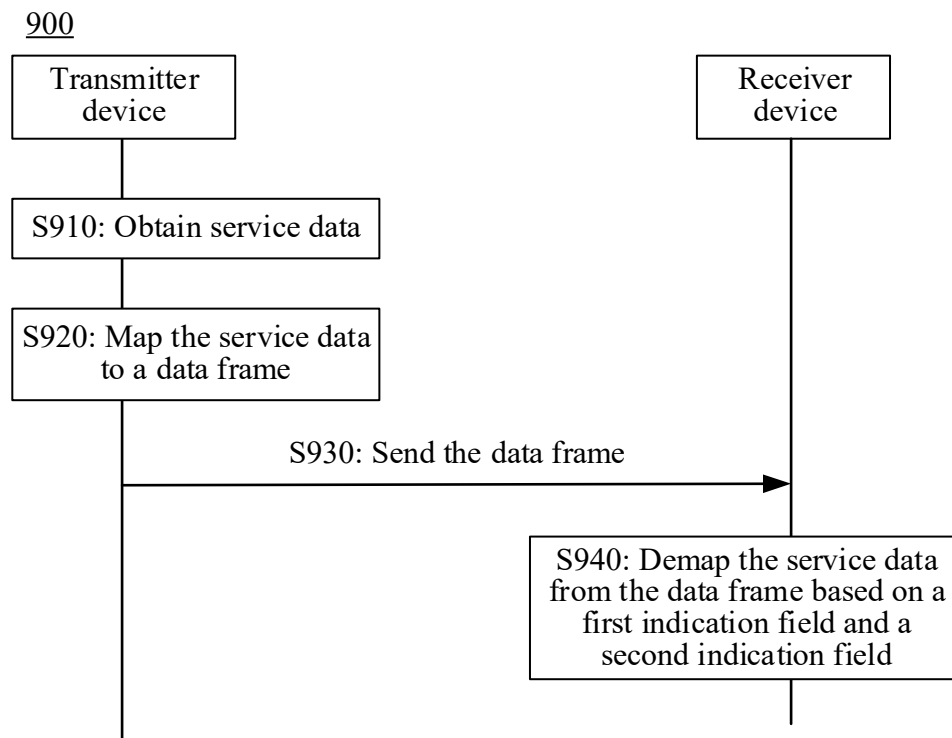


FIG. 9

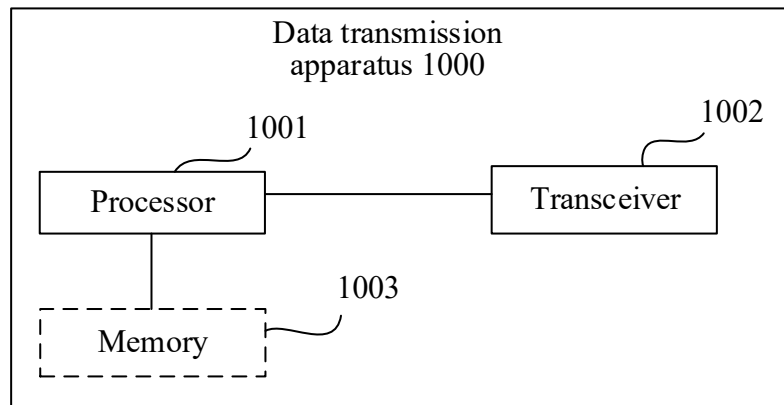


FIG. 10