

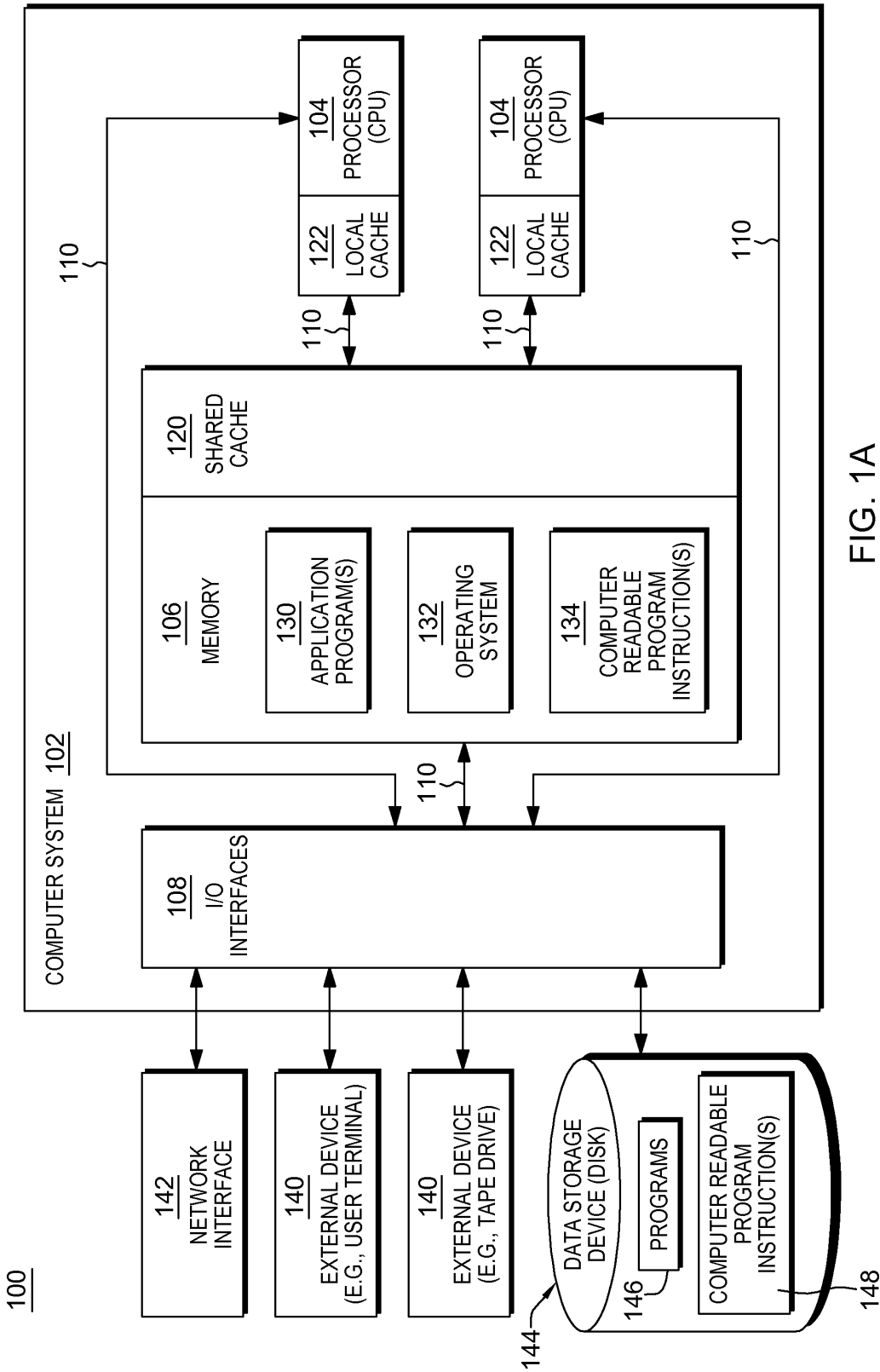


FIG. 1A

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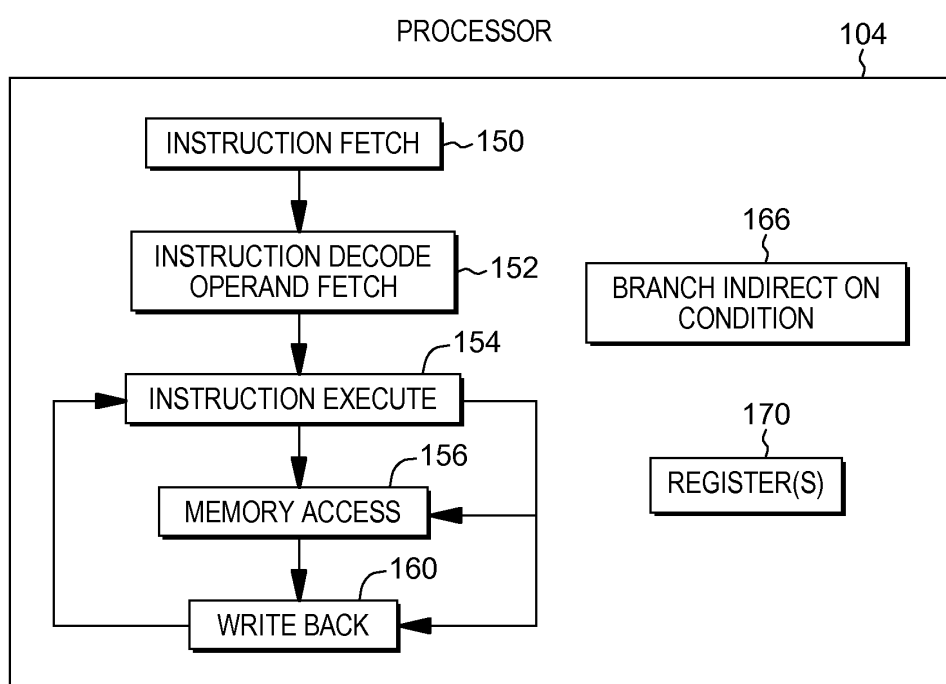


FIG. 1B

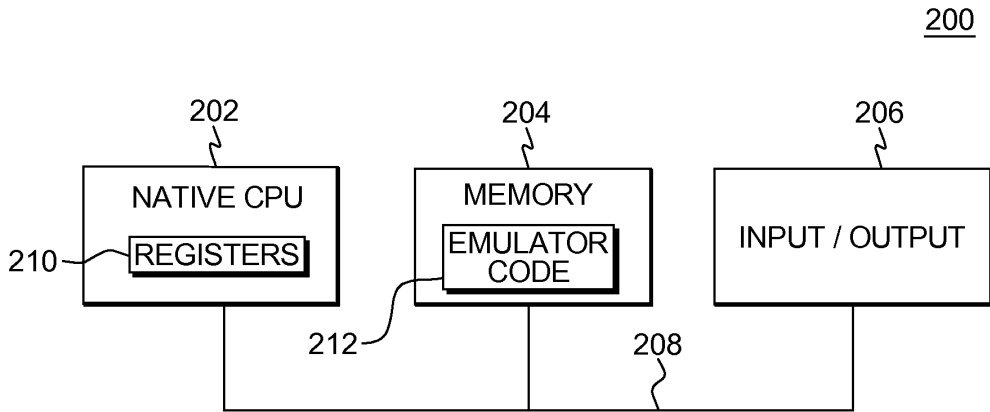


FIG. 2A

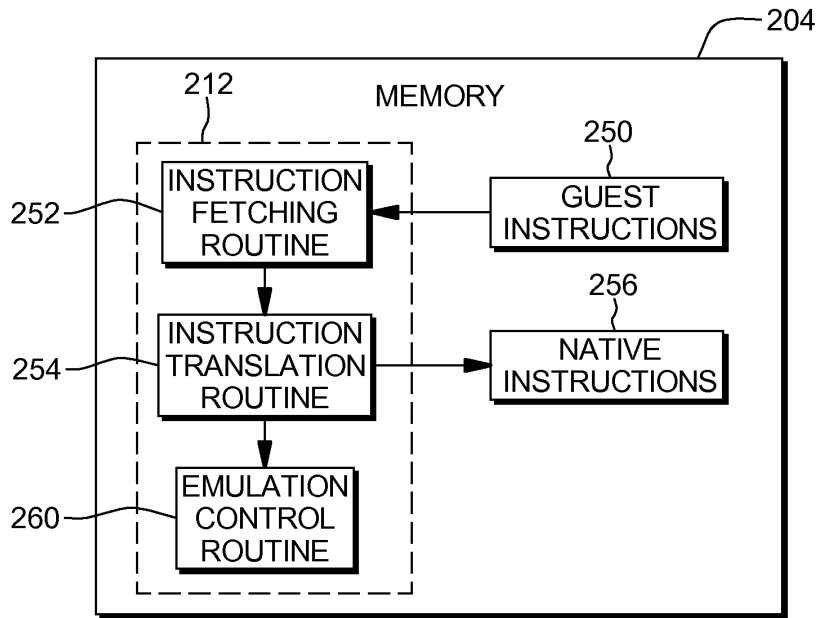


FIG. 2B

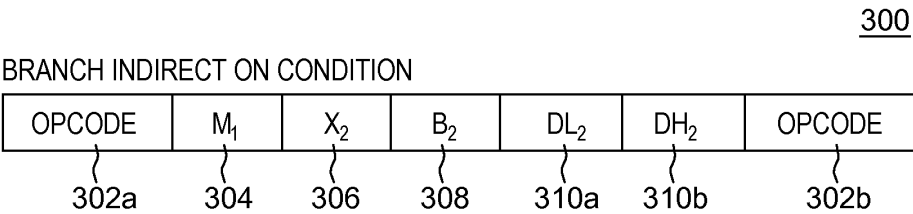


FIG. 3A

|     |                |                                |                     |
|-----|----------------|--------------------------------|---------------------|
| 320 | 322            |                                | 324                 |
|     | CONDITION CODE | INSTRUCTION BIT NUMBER OF MASK | MASK POSITION VALUE |
|     | 0              | 8                              | 8                   |
|     | 1              | 9                              | 4                   |
|     | 2              | 10                             | 2                   |
|     | 3              | 11                             | 1                   |

FIG. 3B

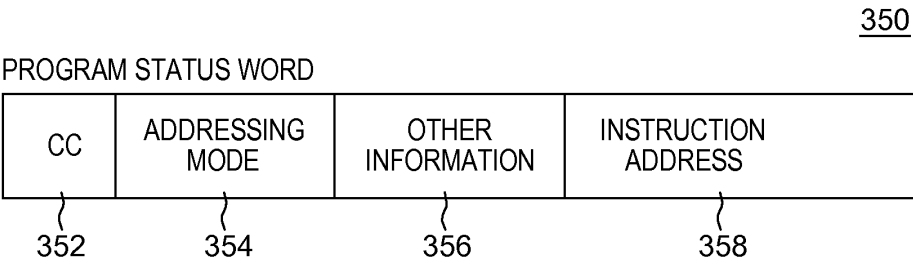


FIG. 3C

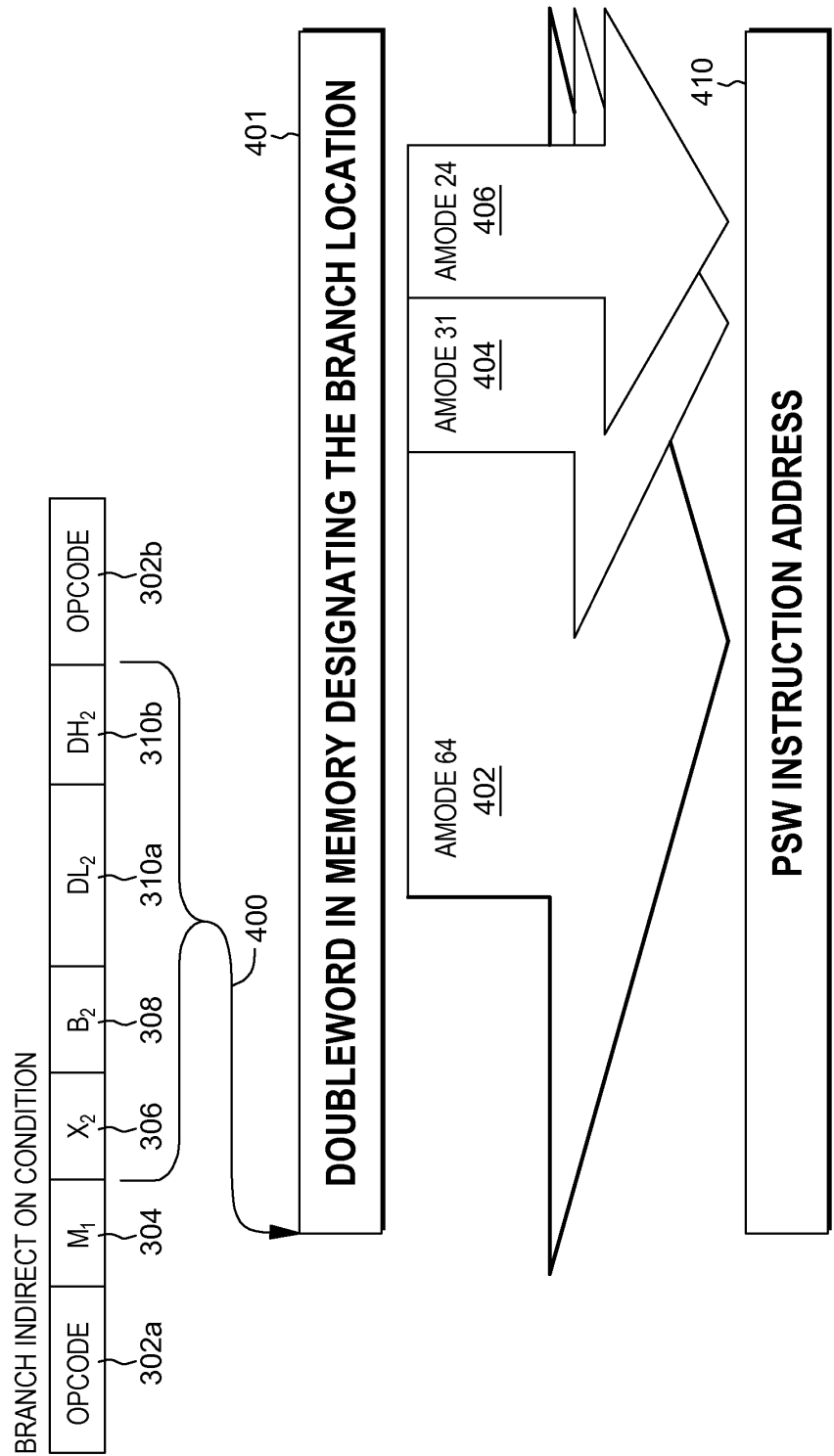


FIG. 4

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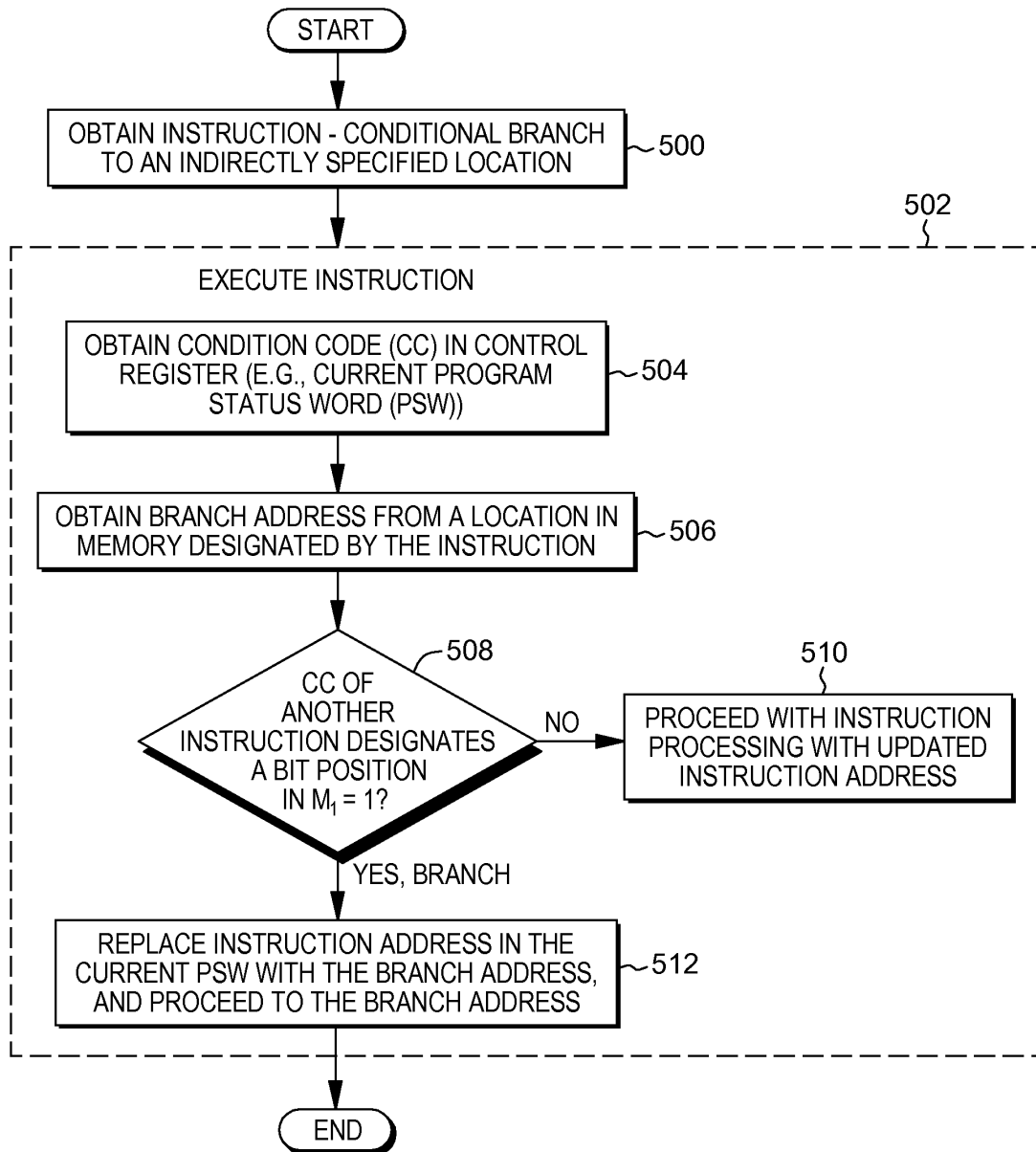


FIG. 5

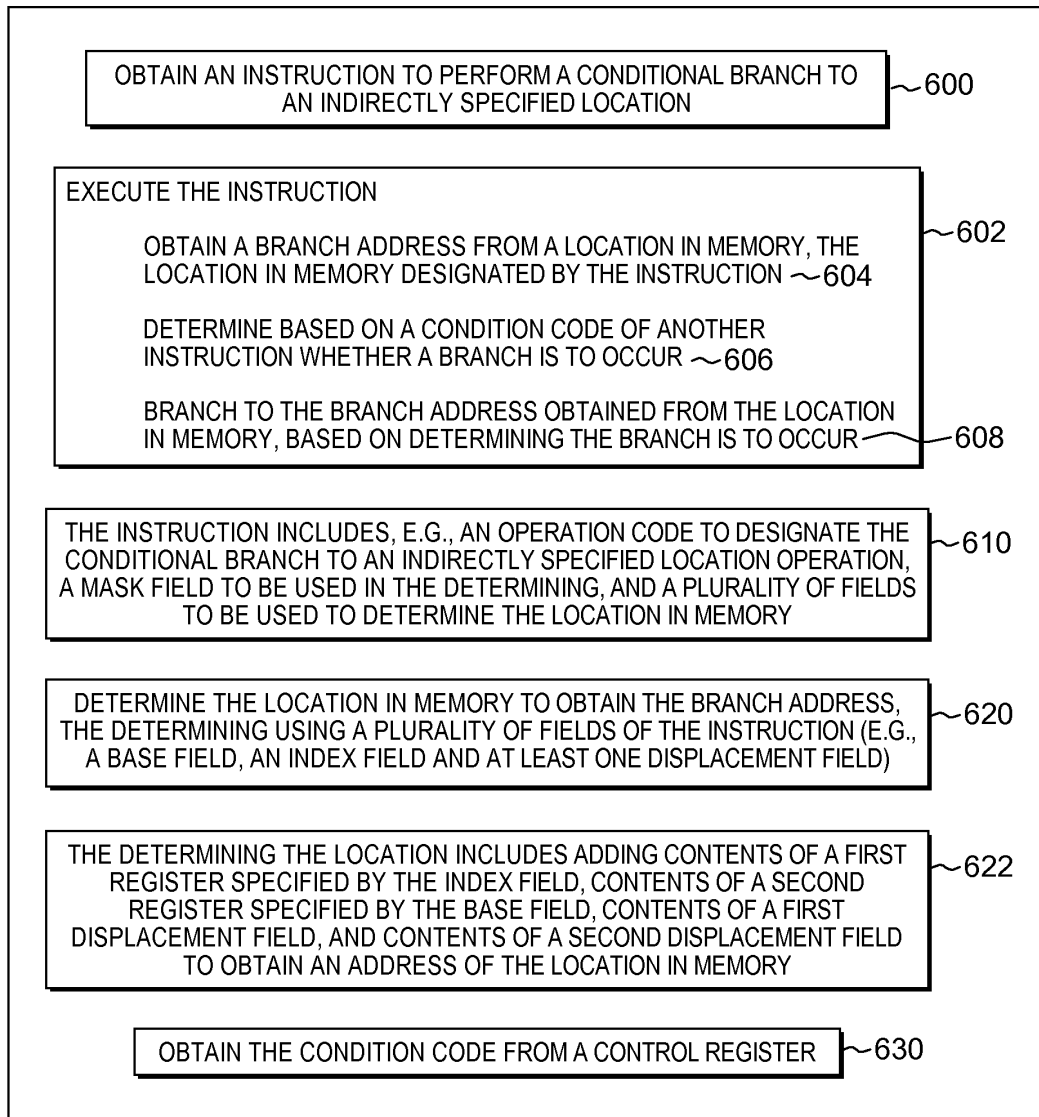


FIG. 6A

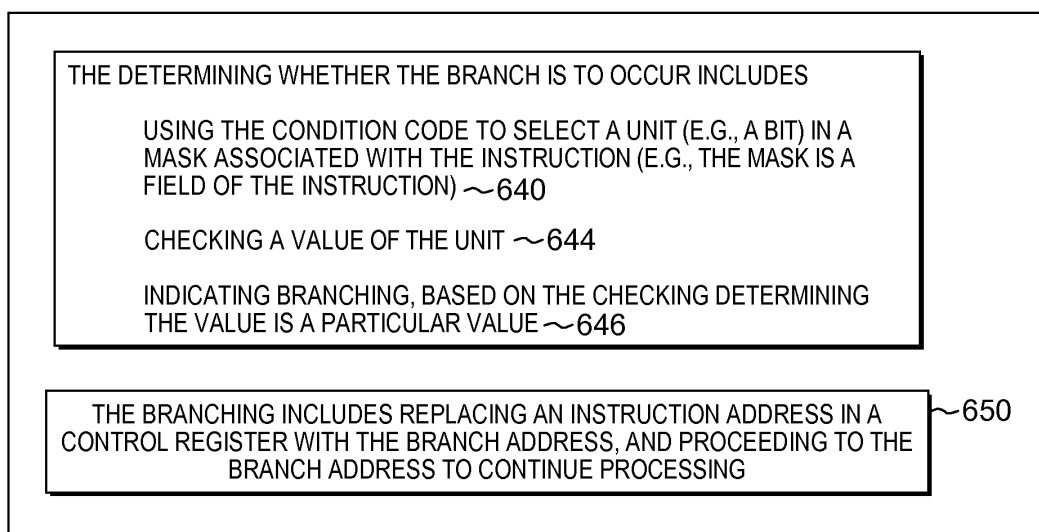


FIG. 6B