

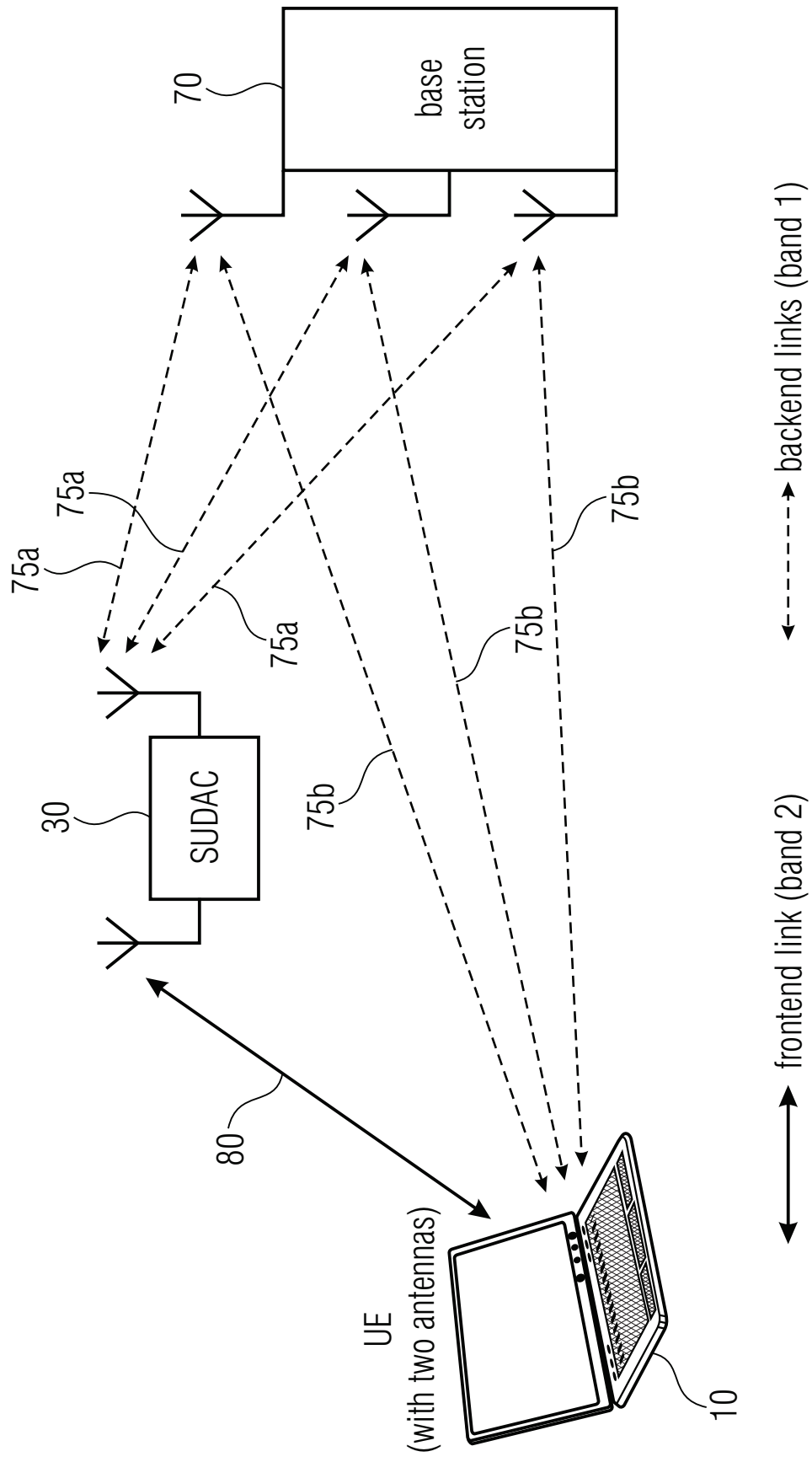


FIG 1

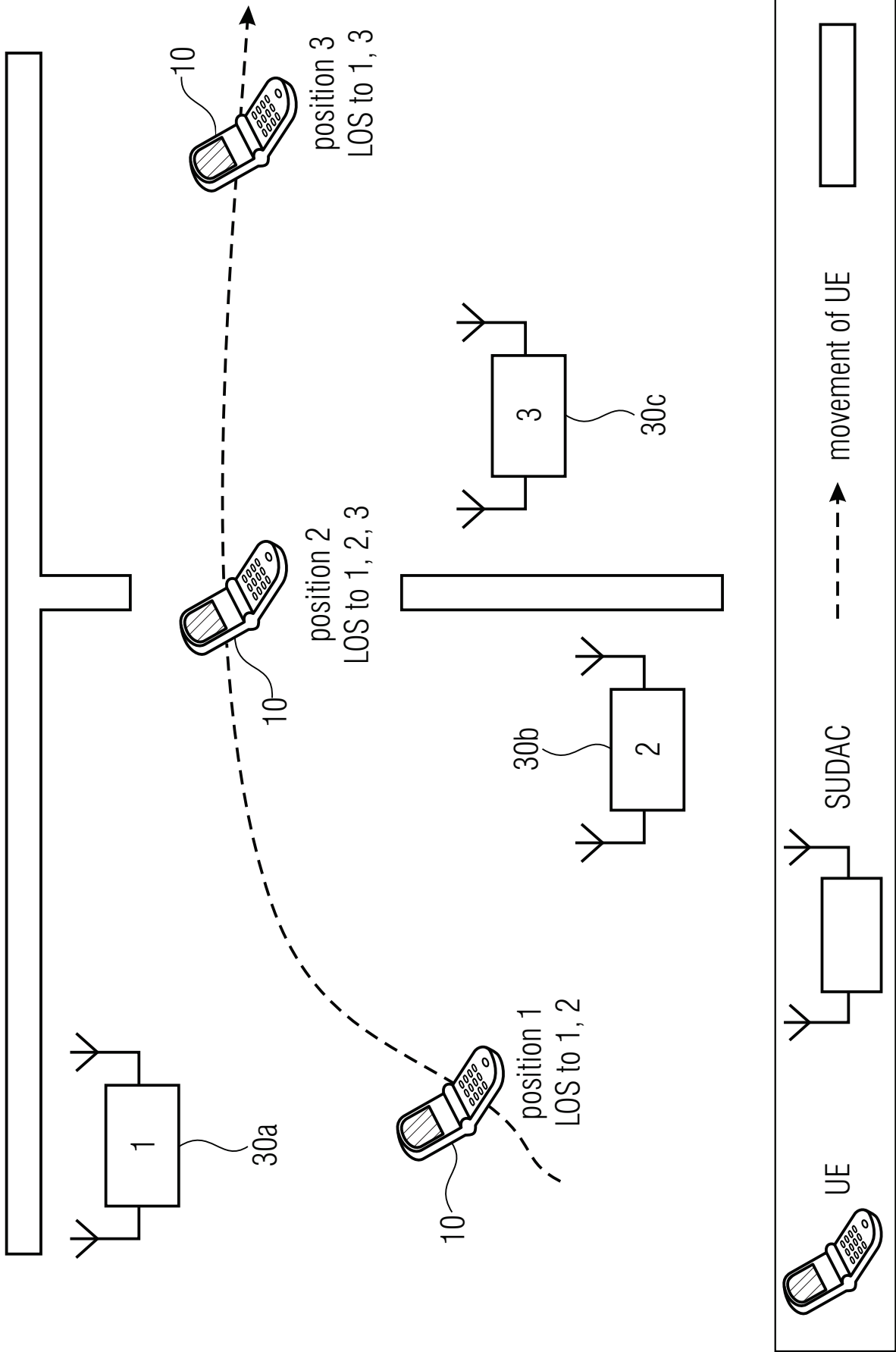


FIG 2

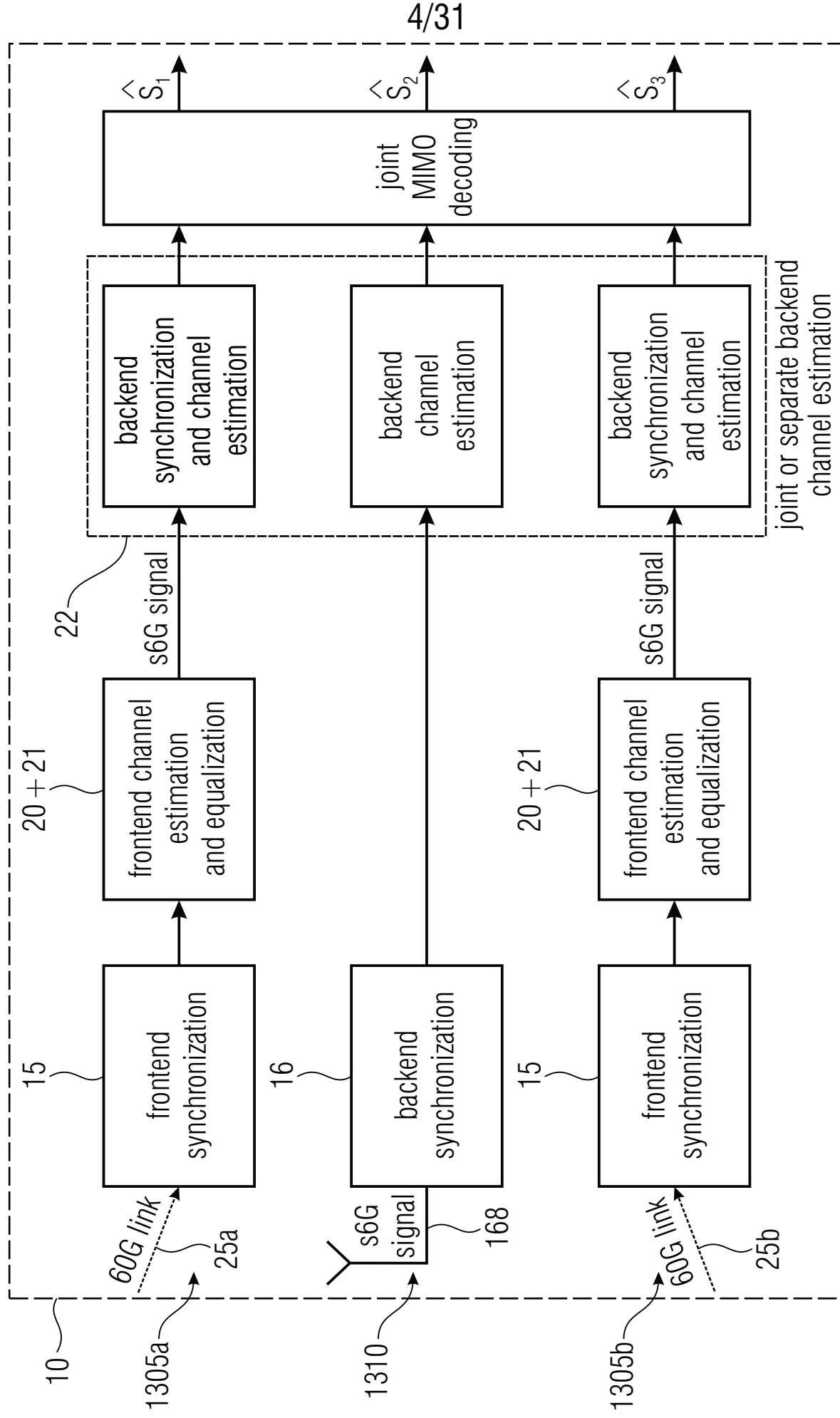
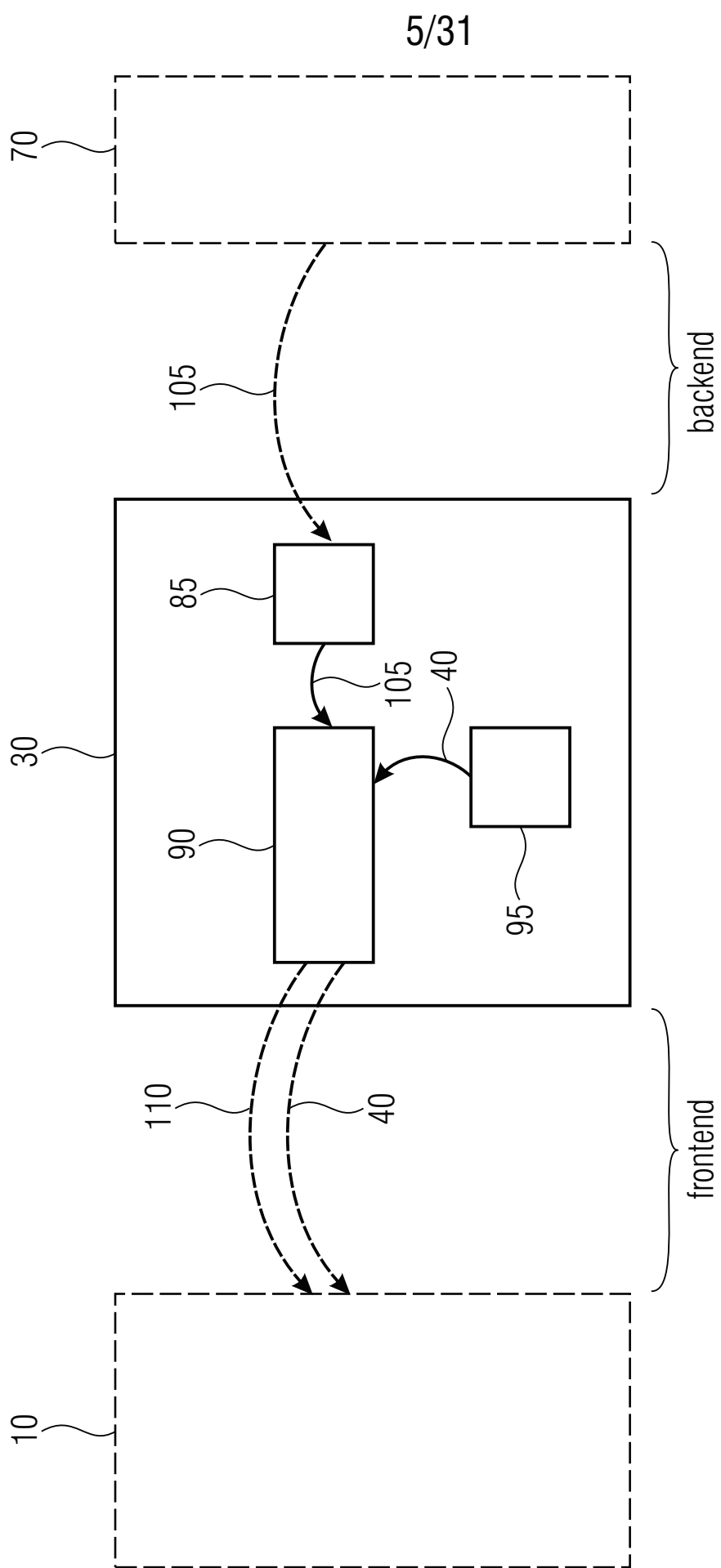


FIG 4



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FIG 5

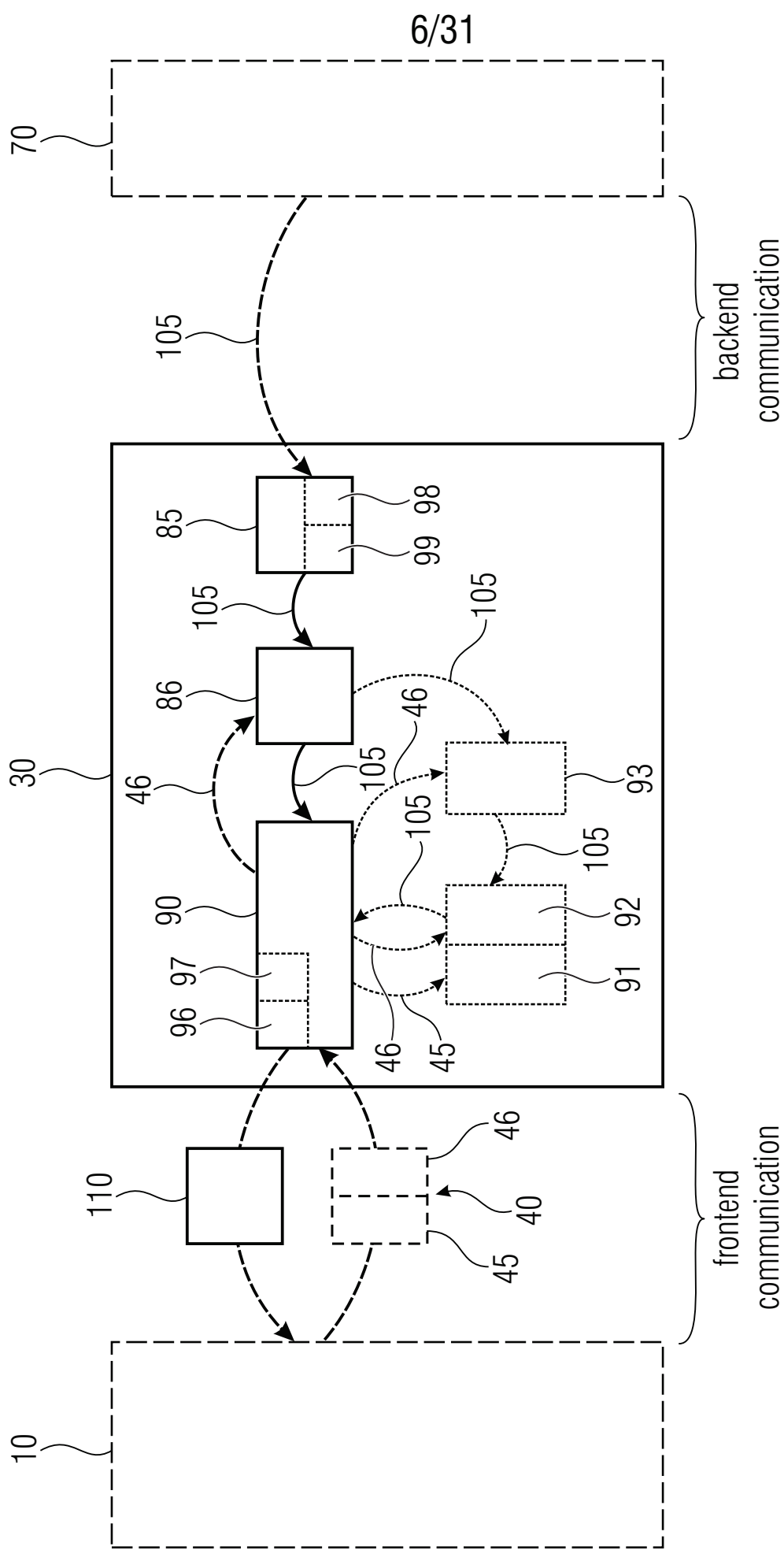


FIG 6

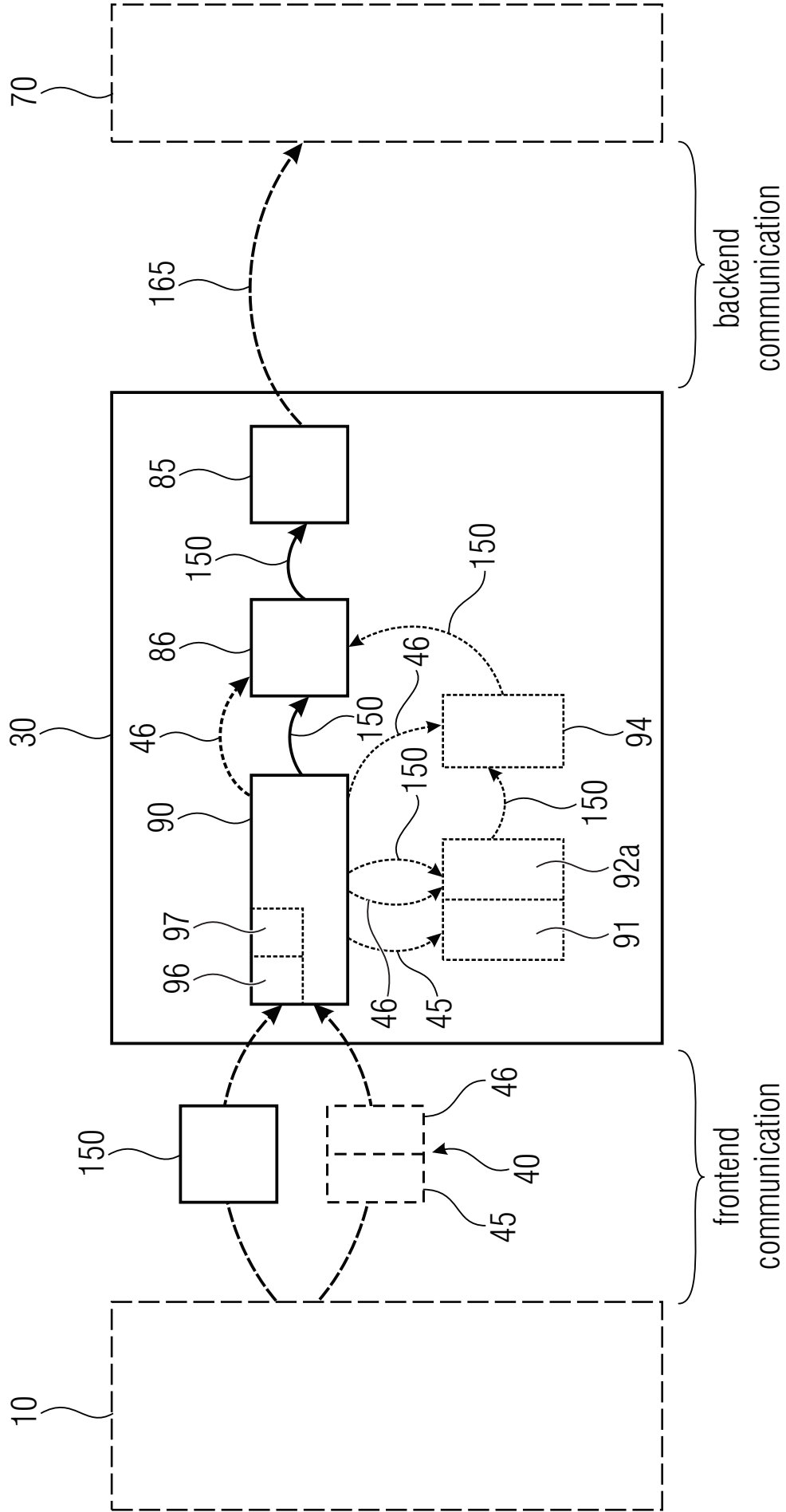


FIG 7

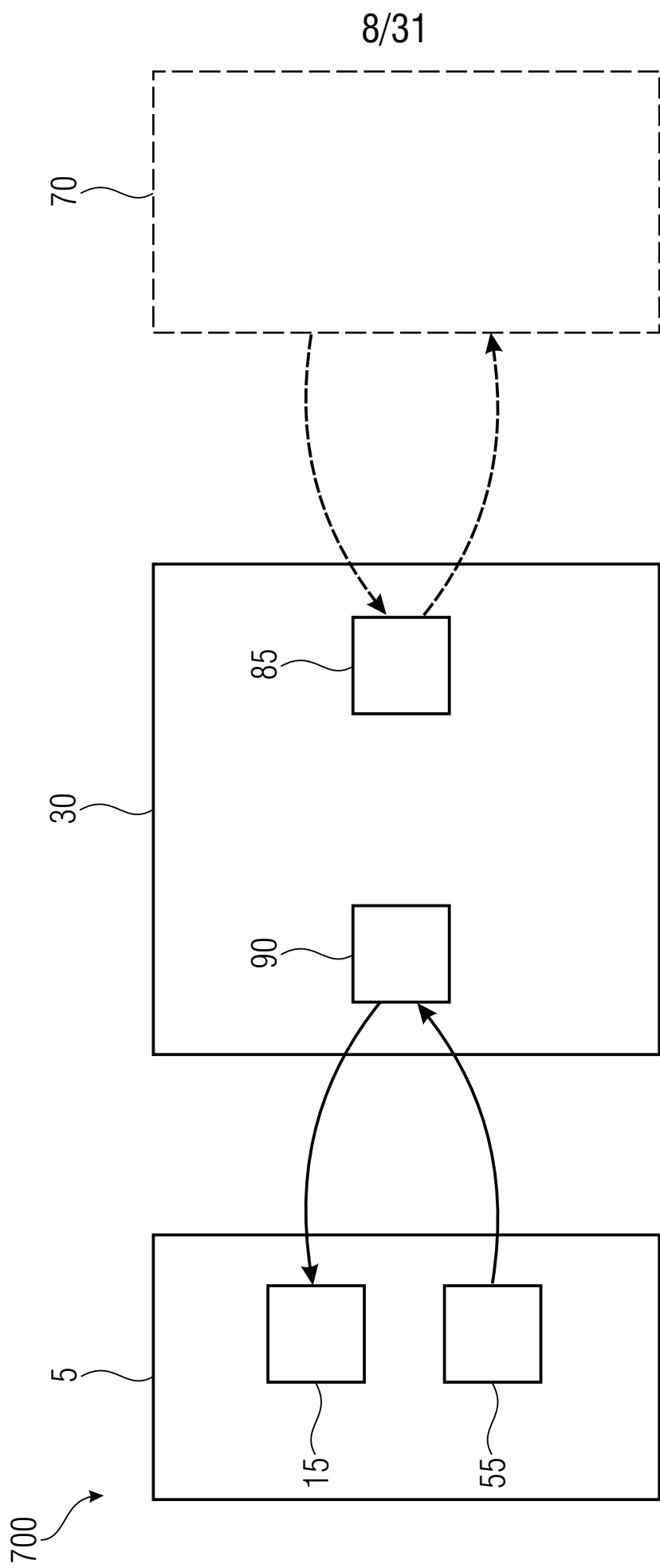


FIG 8

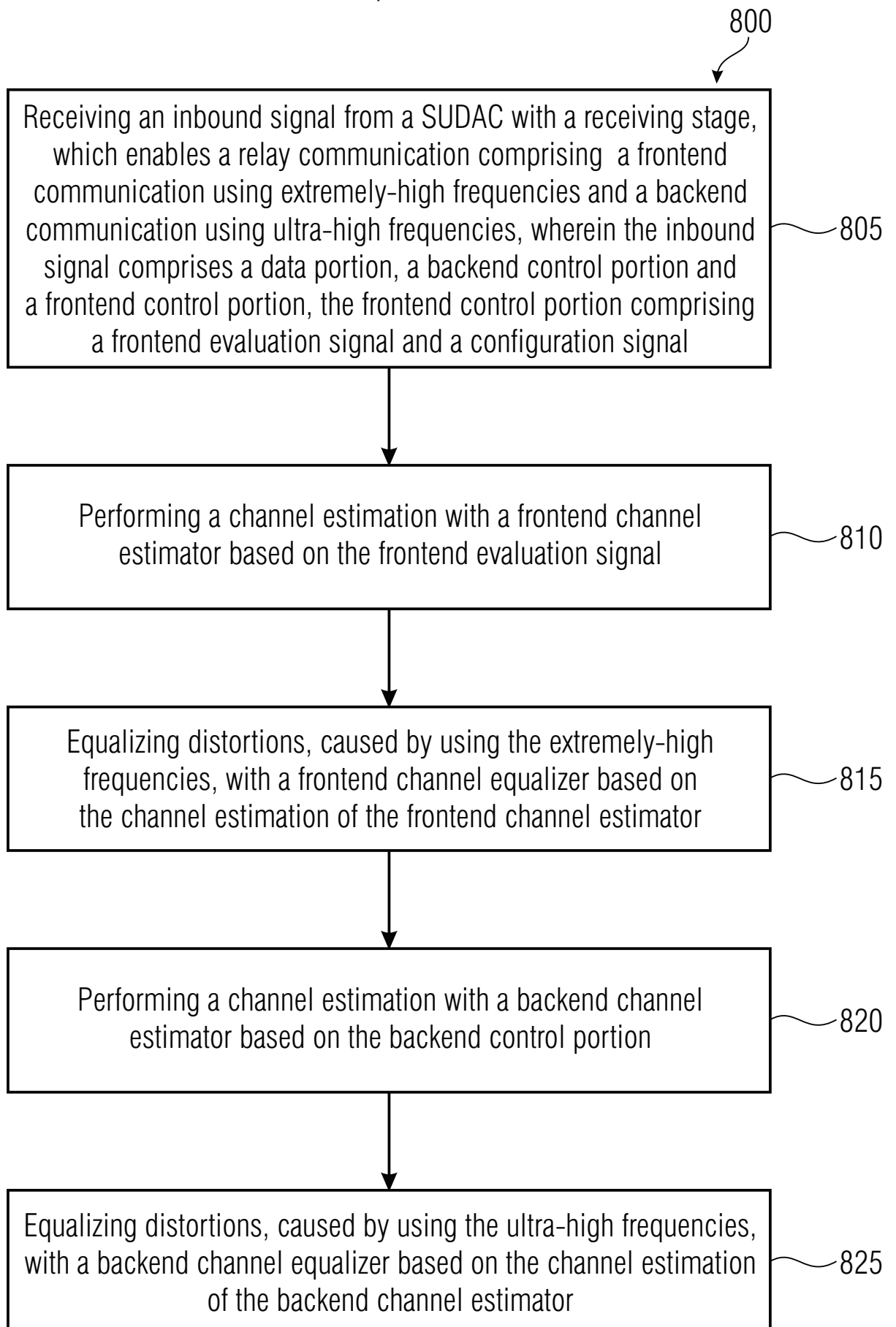


FIG 9

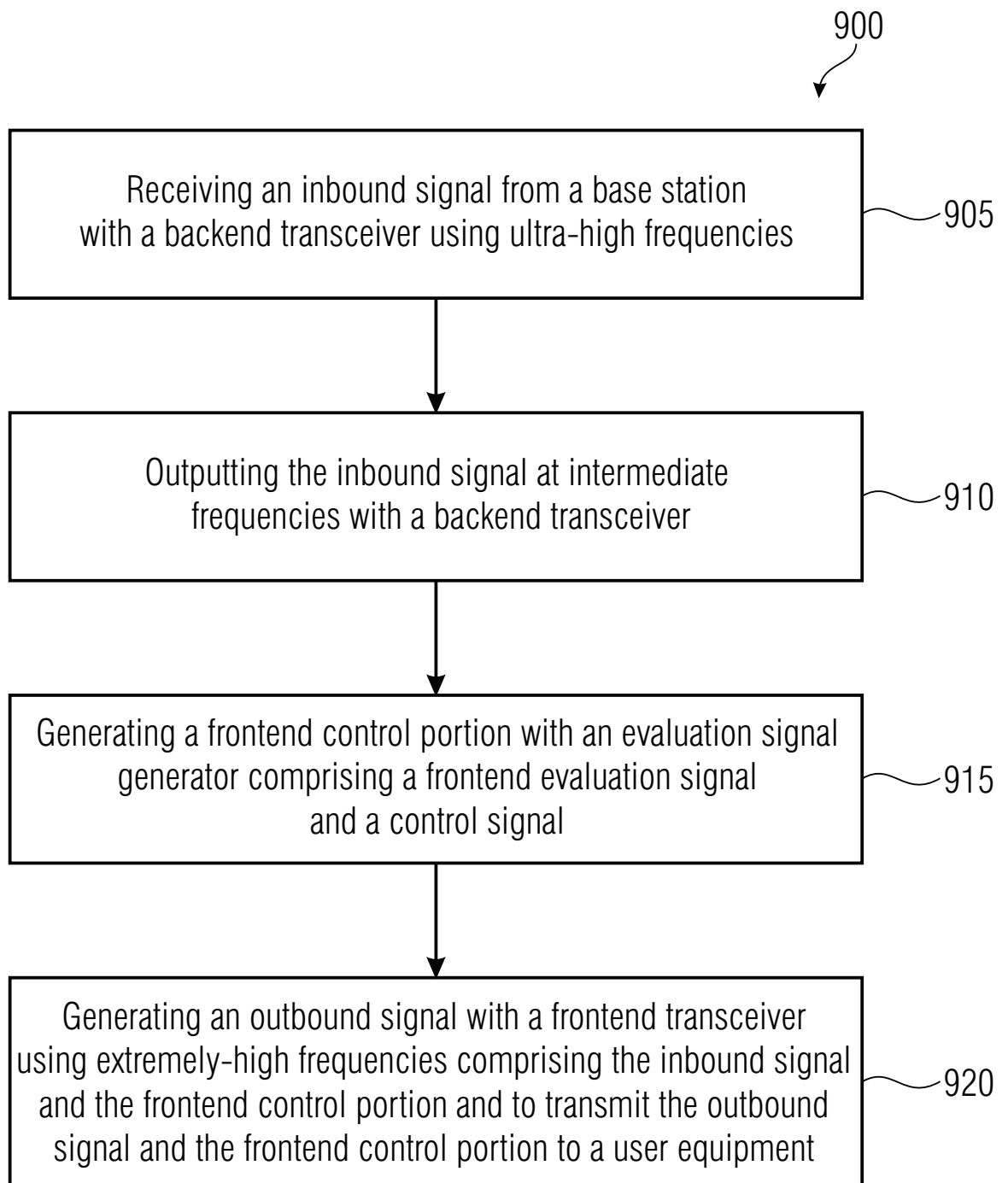


FIG 10

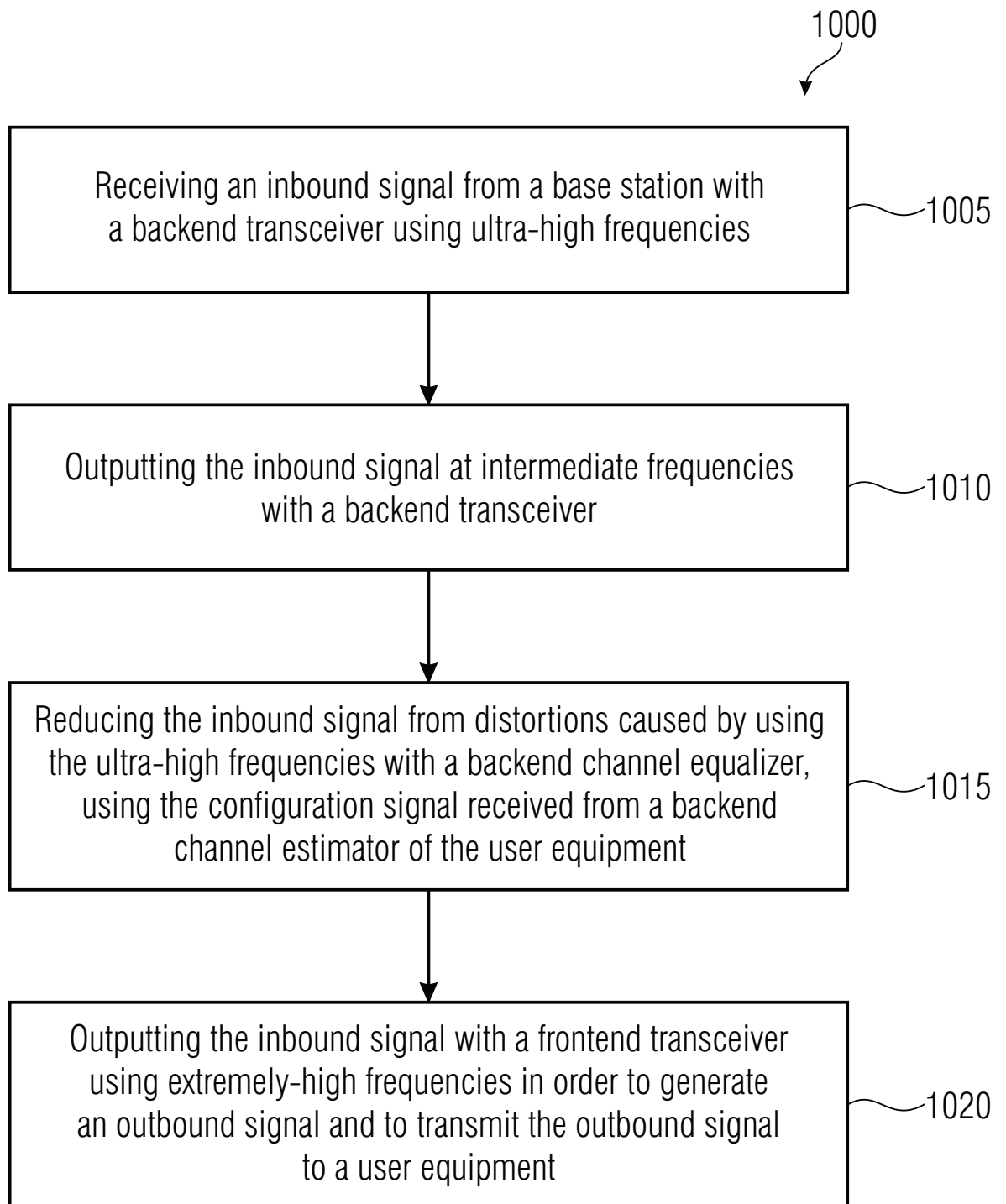


FIG 11

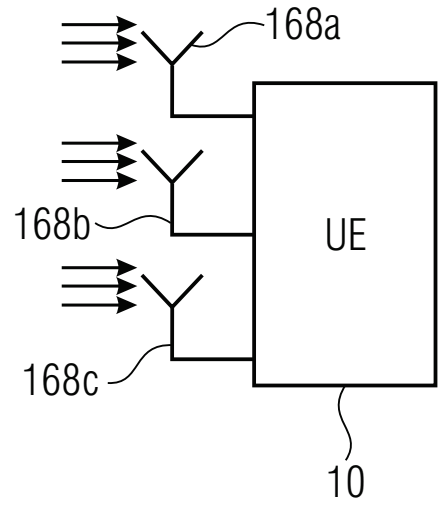
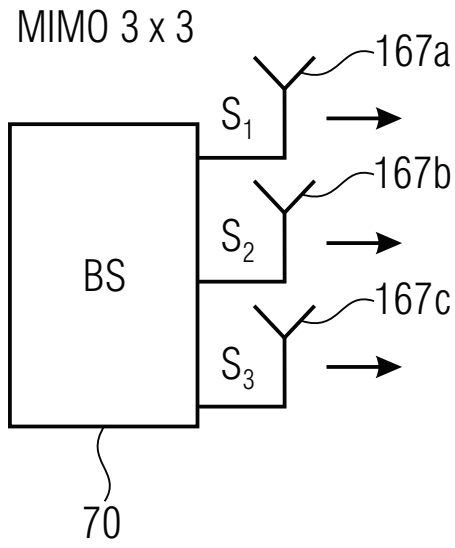


FIG 12A

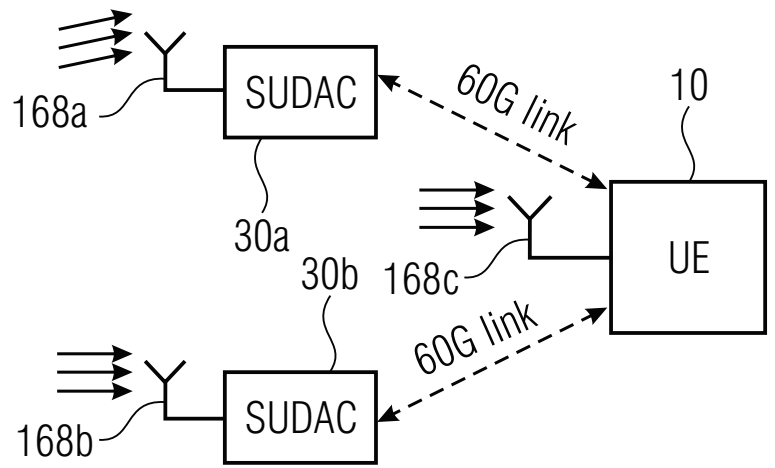
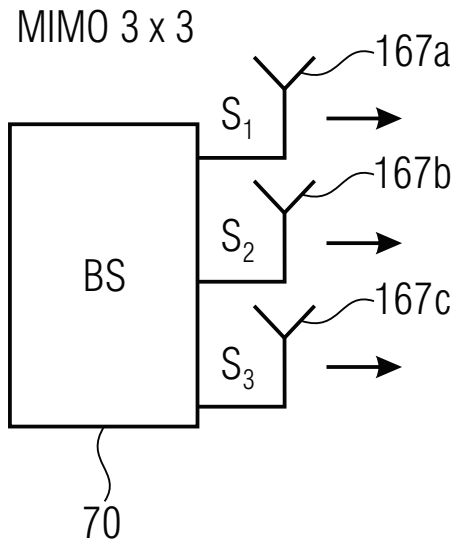


FIG 12B

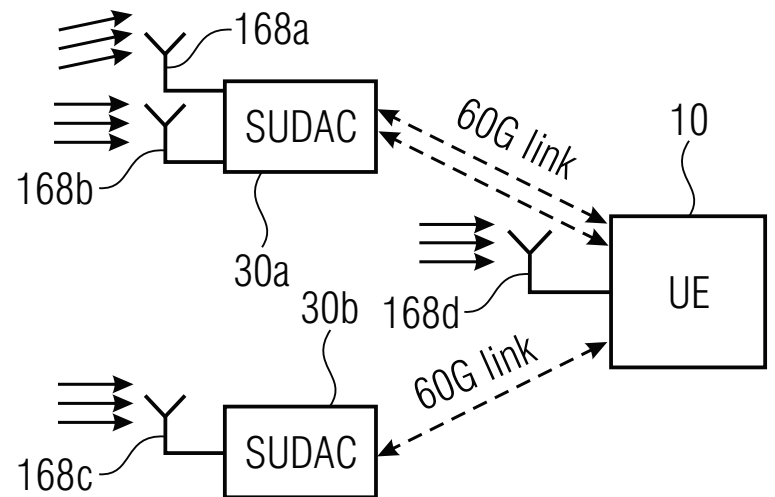
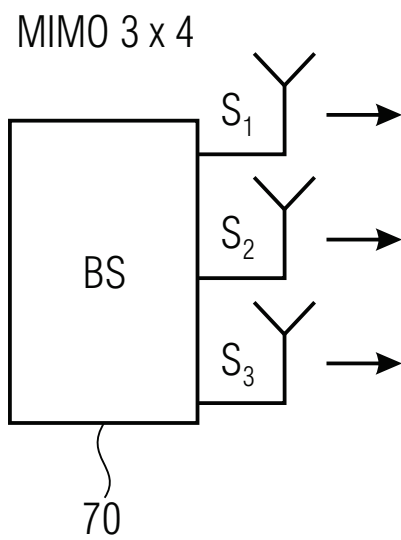
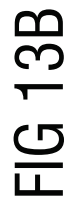
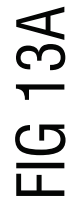


FIG 12C



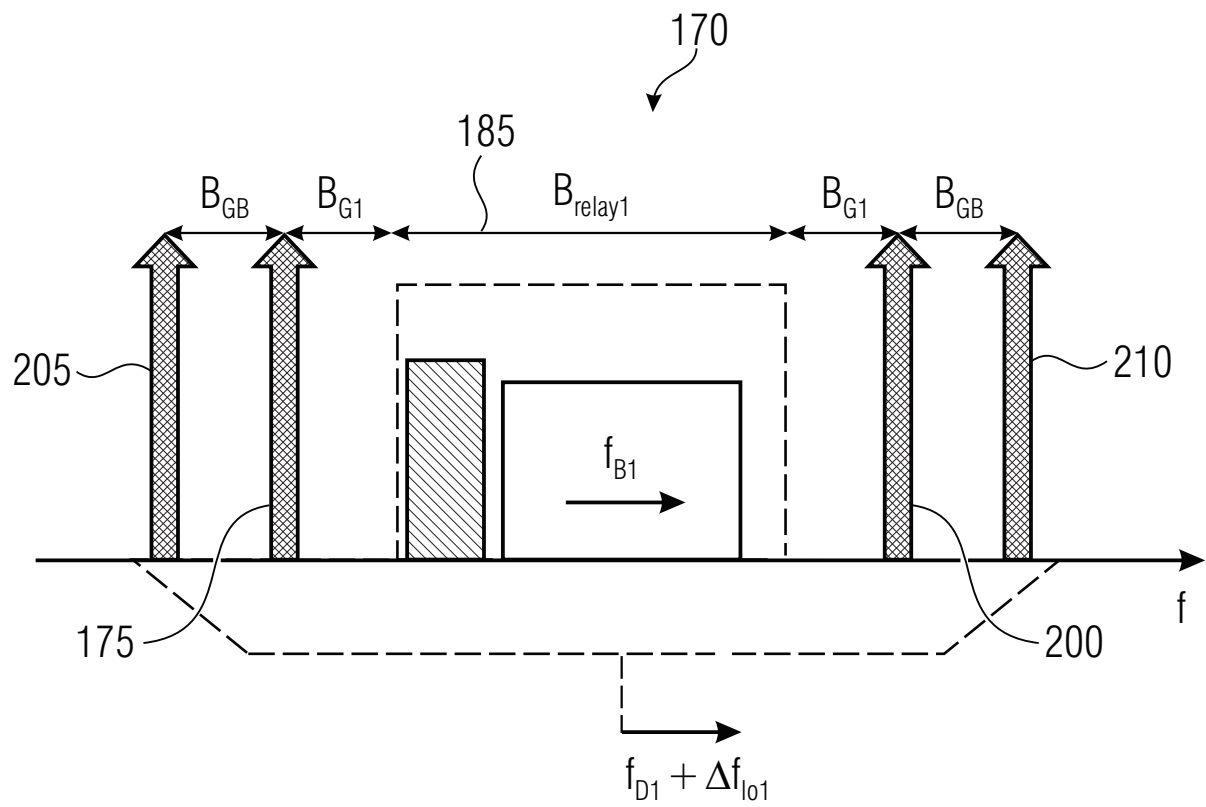


FIG 14

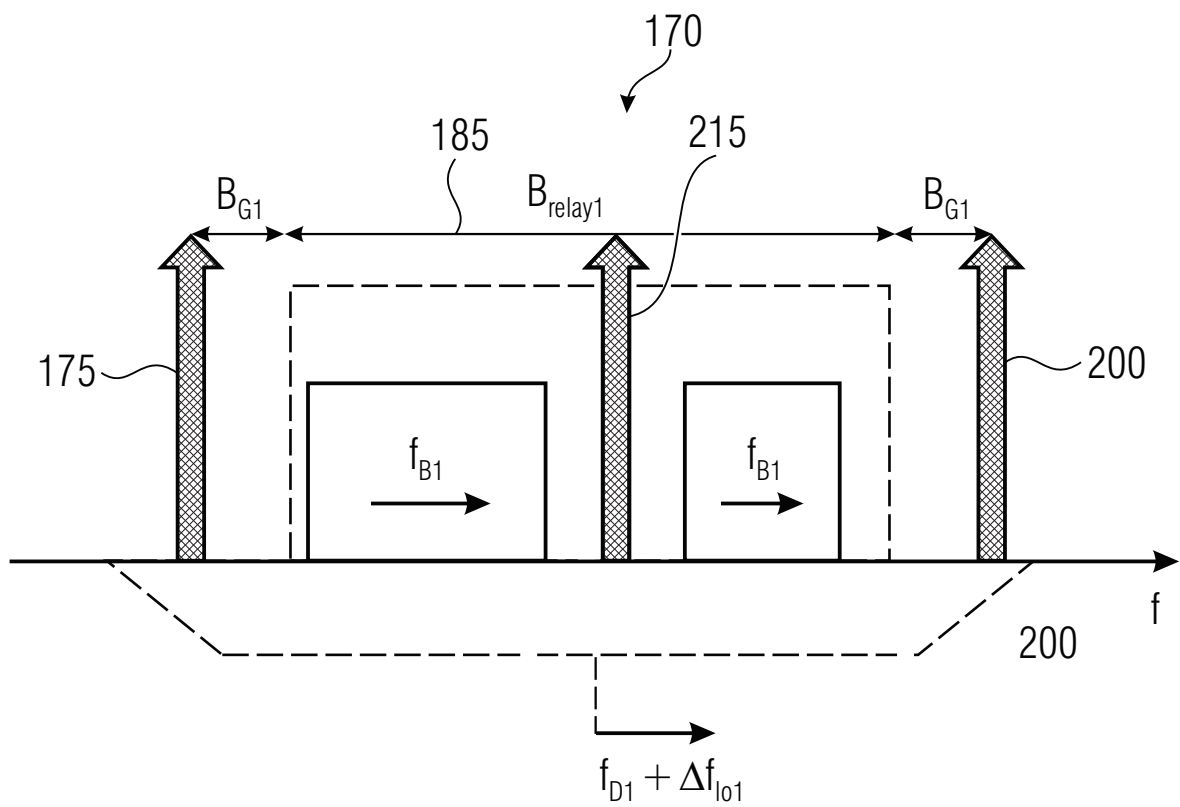


FIG 15

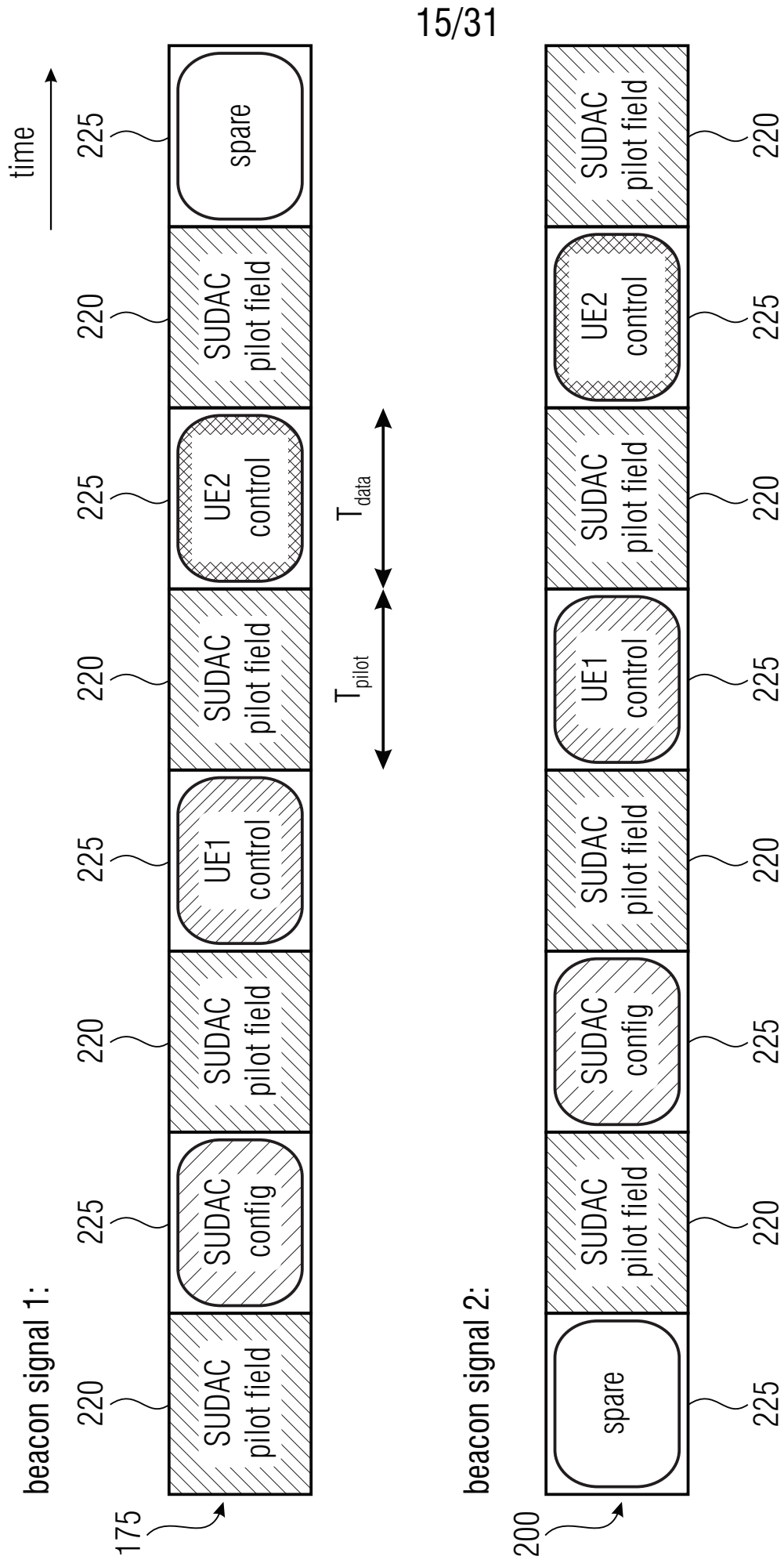


FIG 16

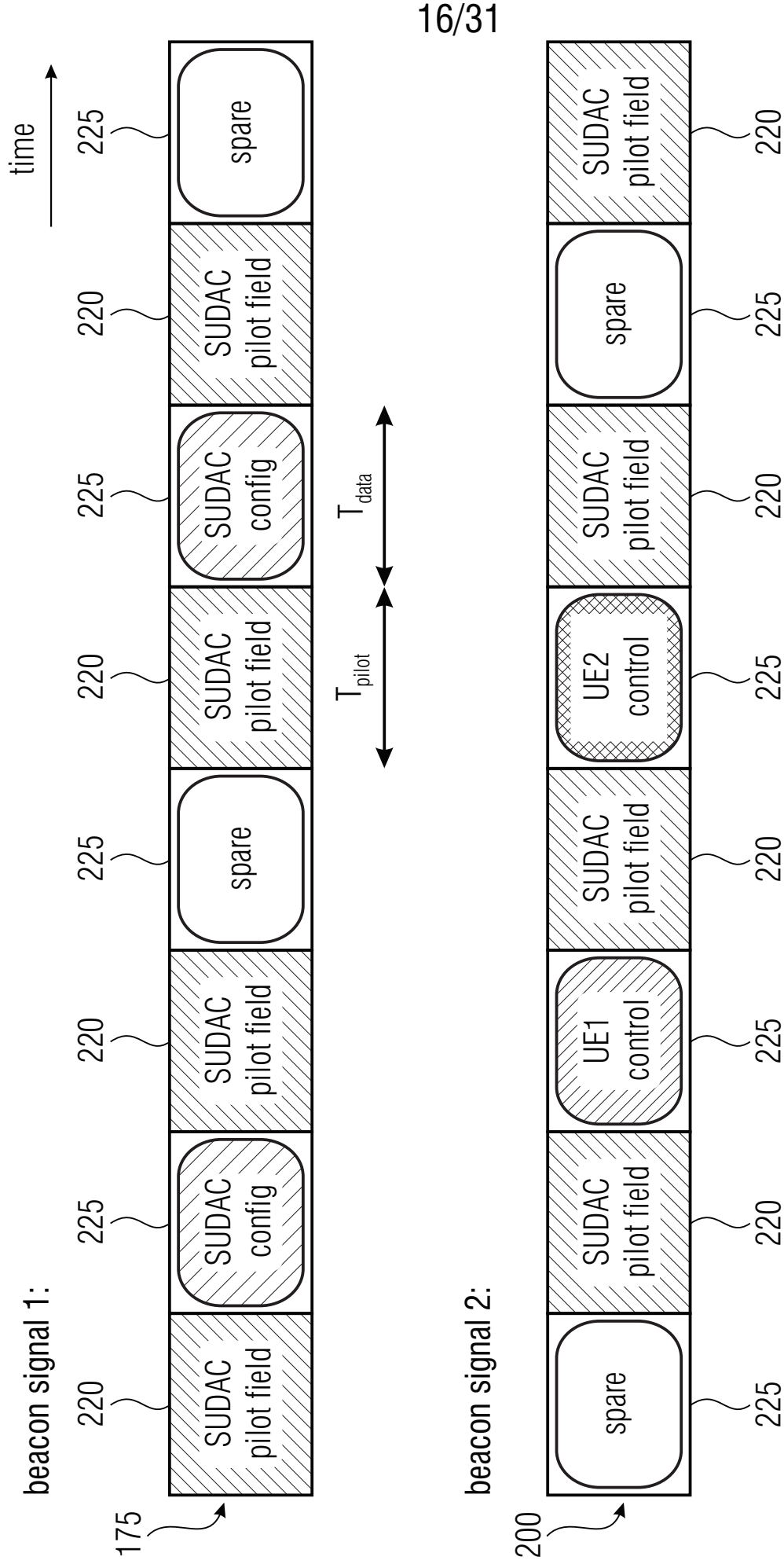


FIG 17

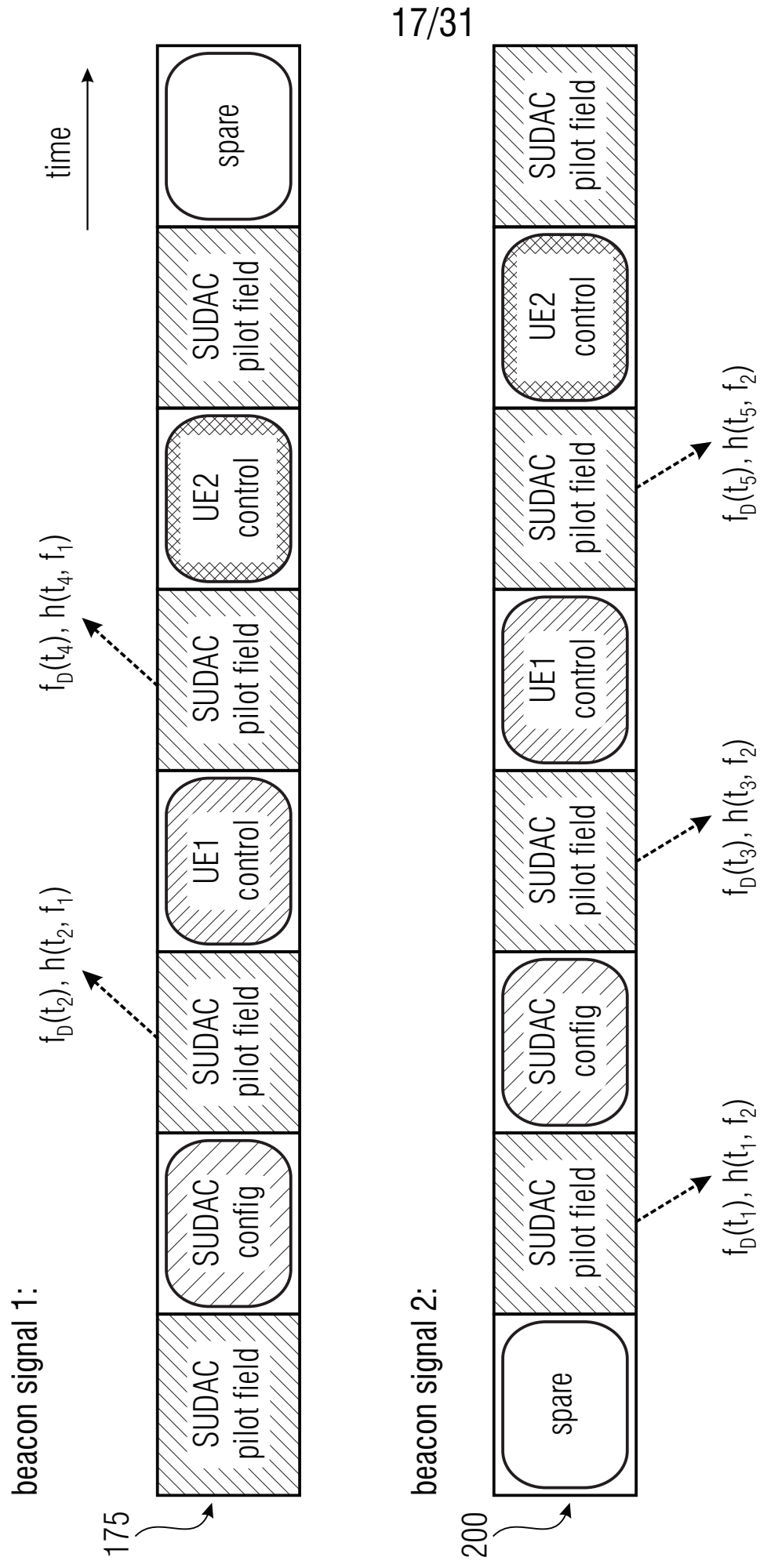


FIG 18

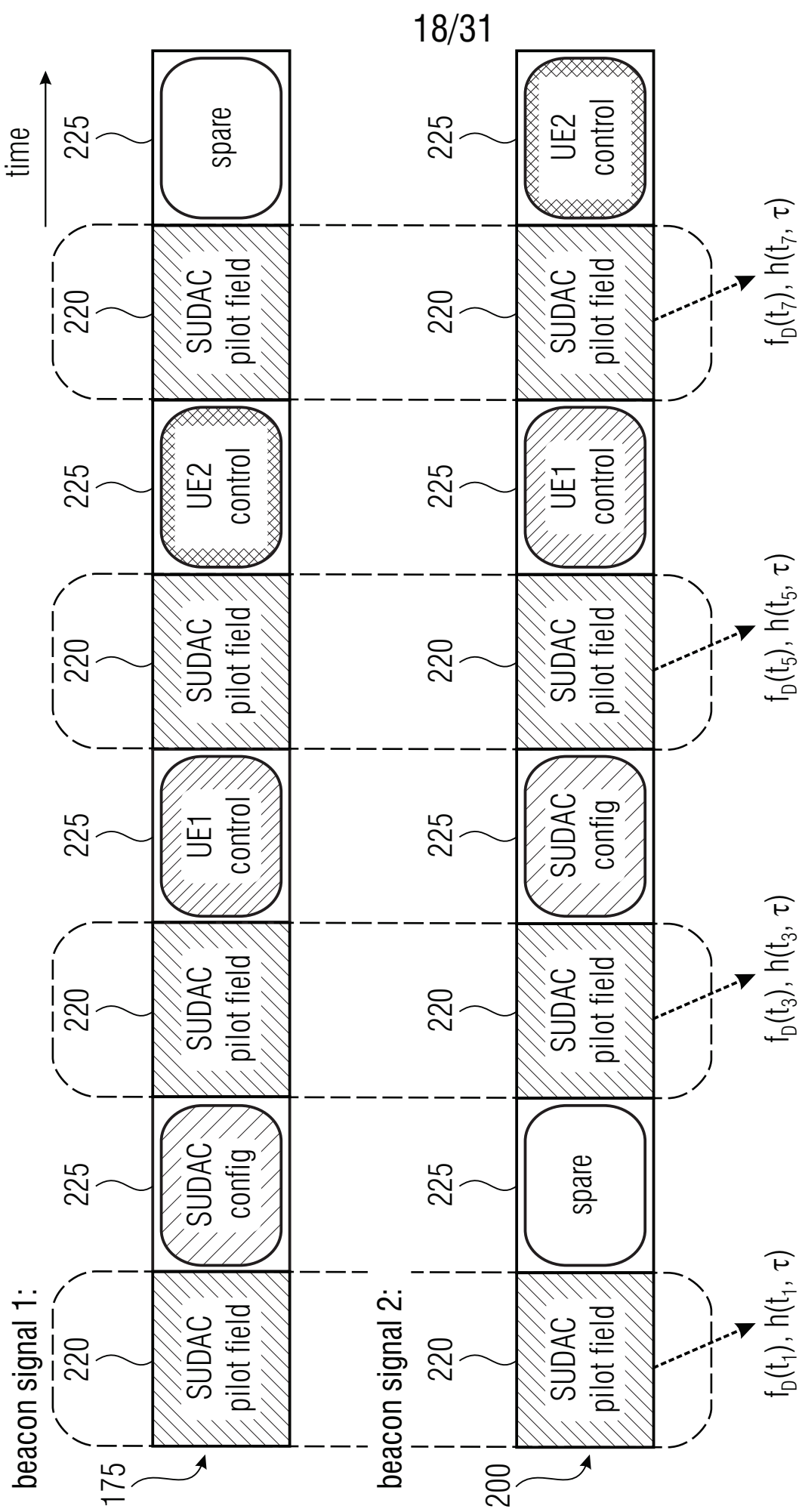


FIG 19

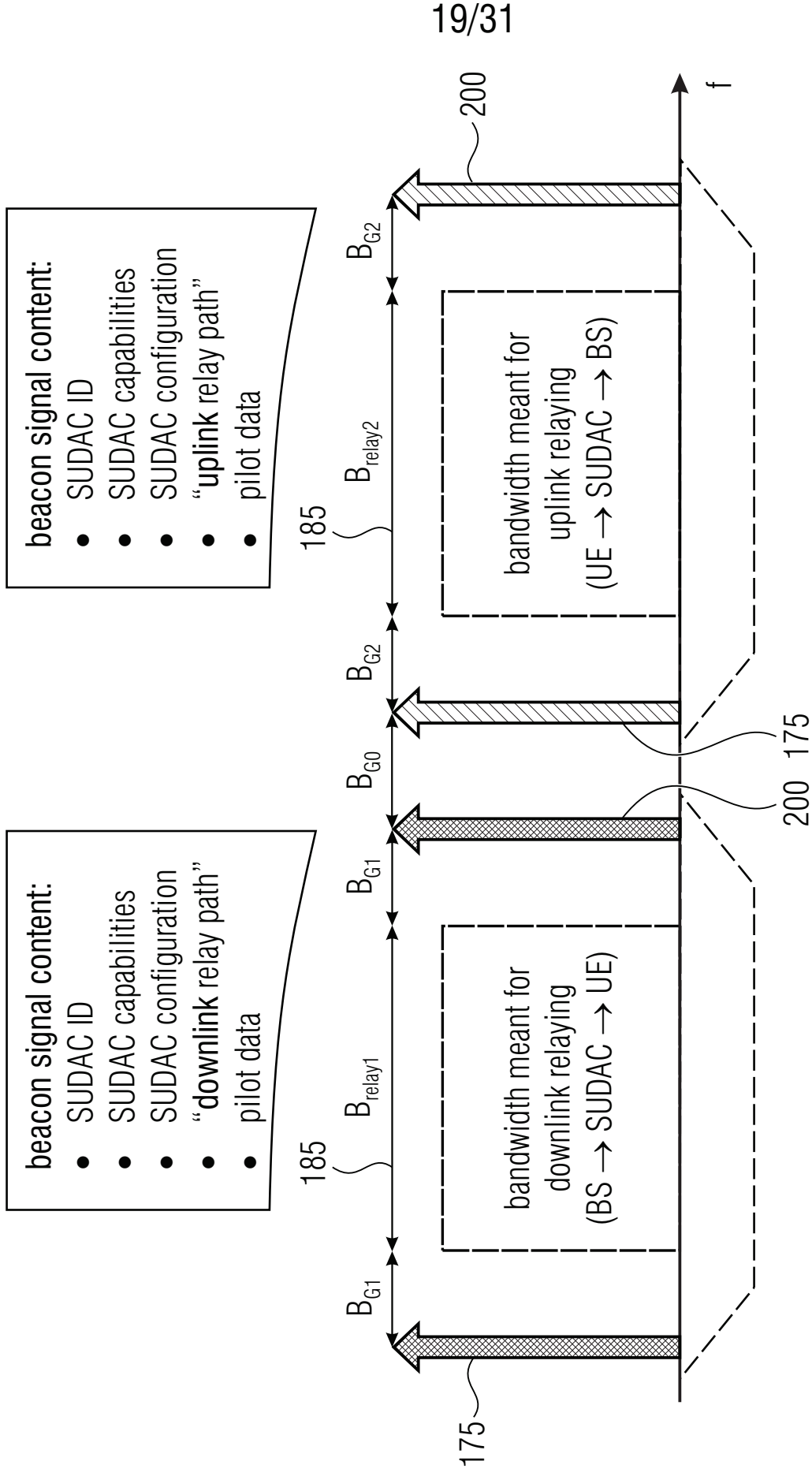


FIG 20

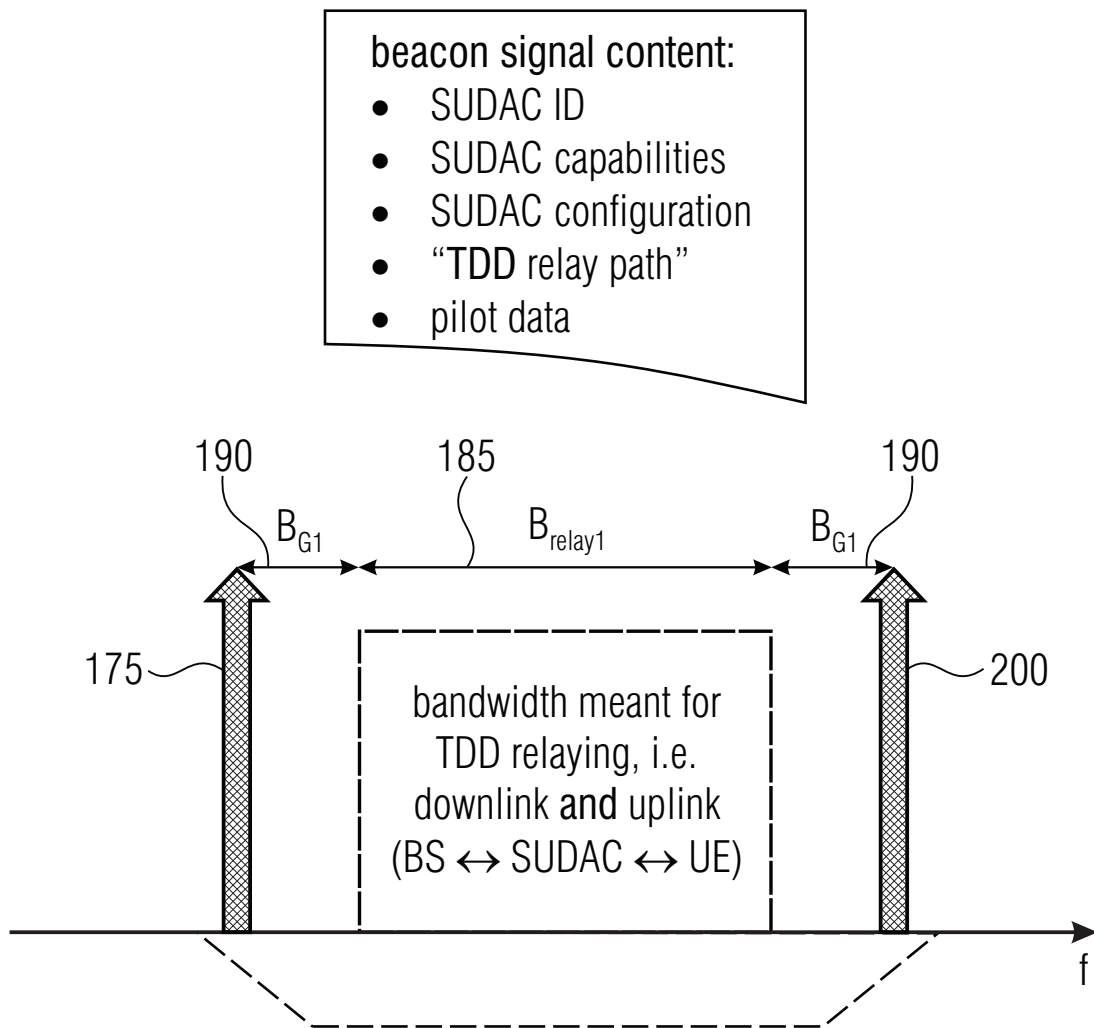
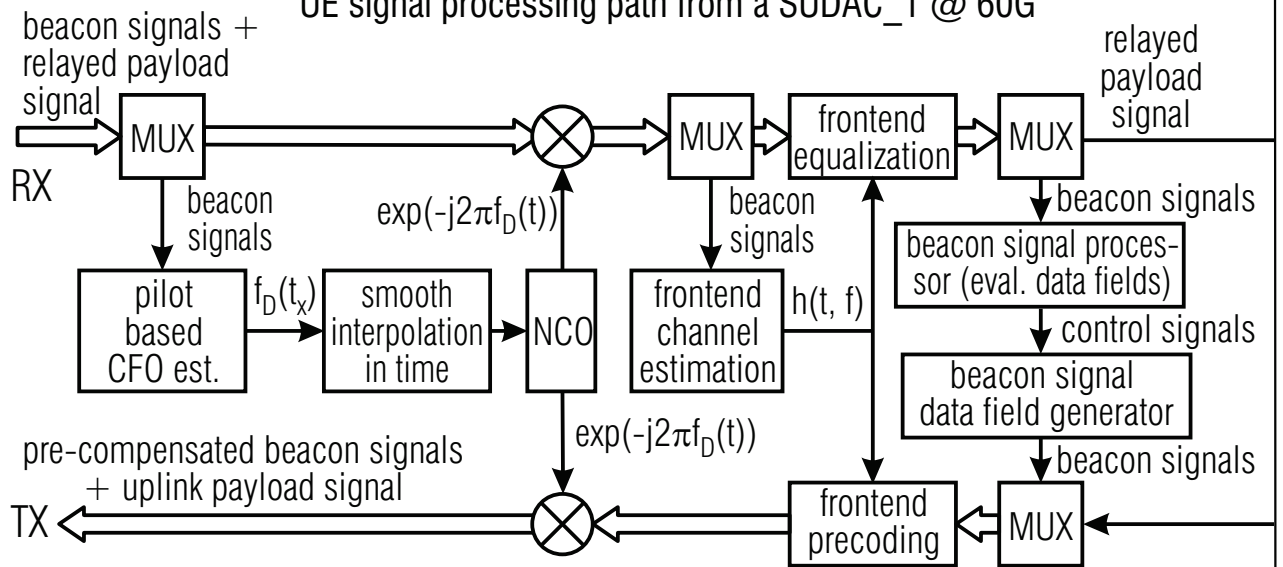


FIG 21

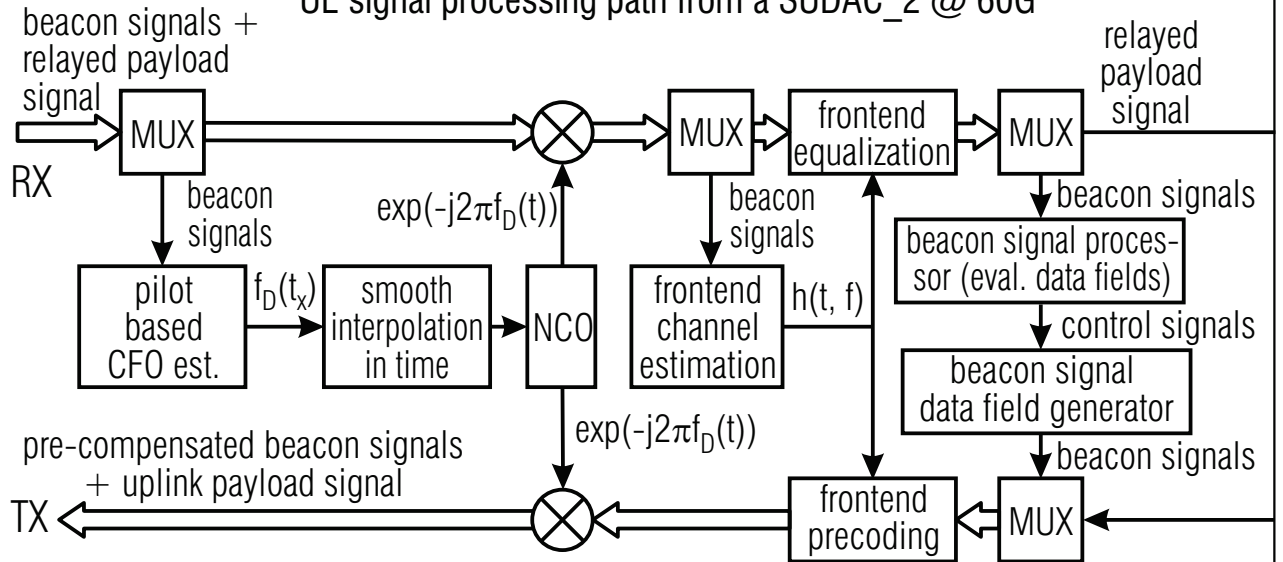


FIG 22

UE signal processing path from a SUDAC_1 @ 60G



UE signal processing path from a SUDAC_2 @ 60G



UE signal processing path from a SUDAC_3 @ 60G

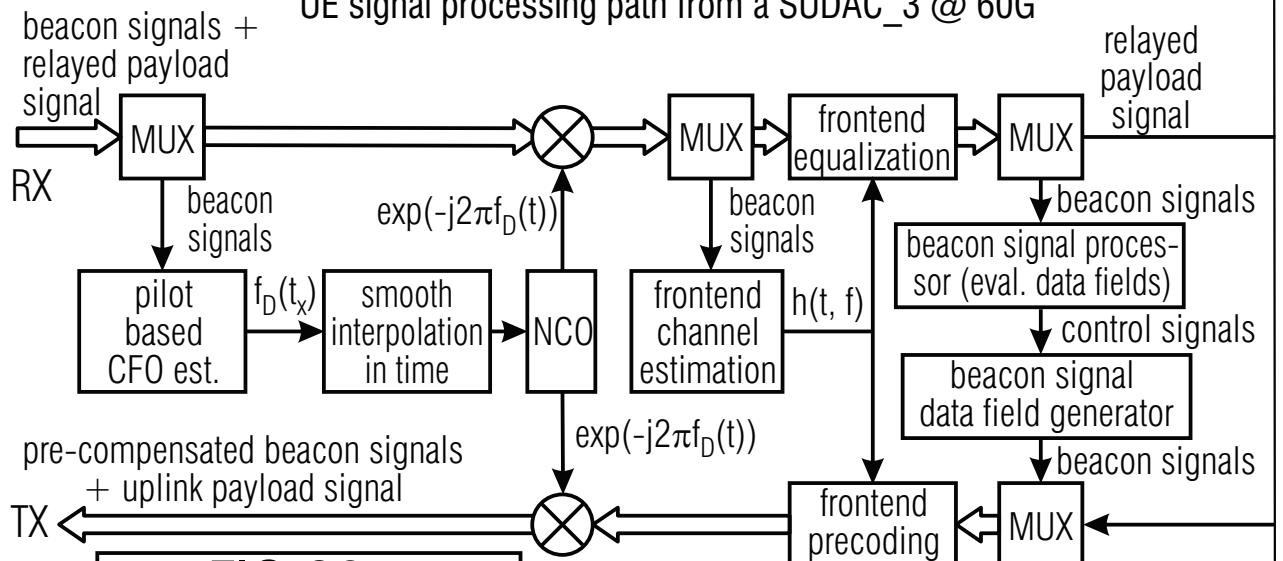


FIG 23
FIG 23A | FIG 23B

FIG 23A

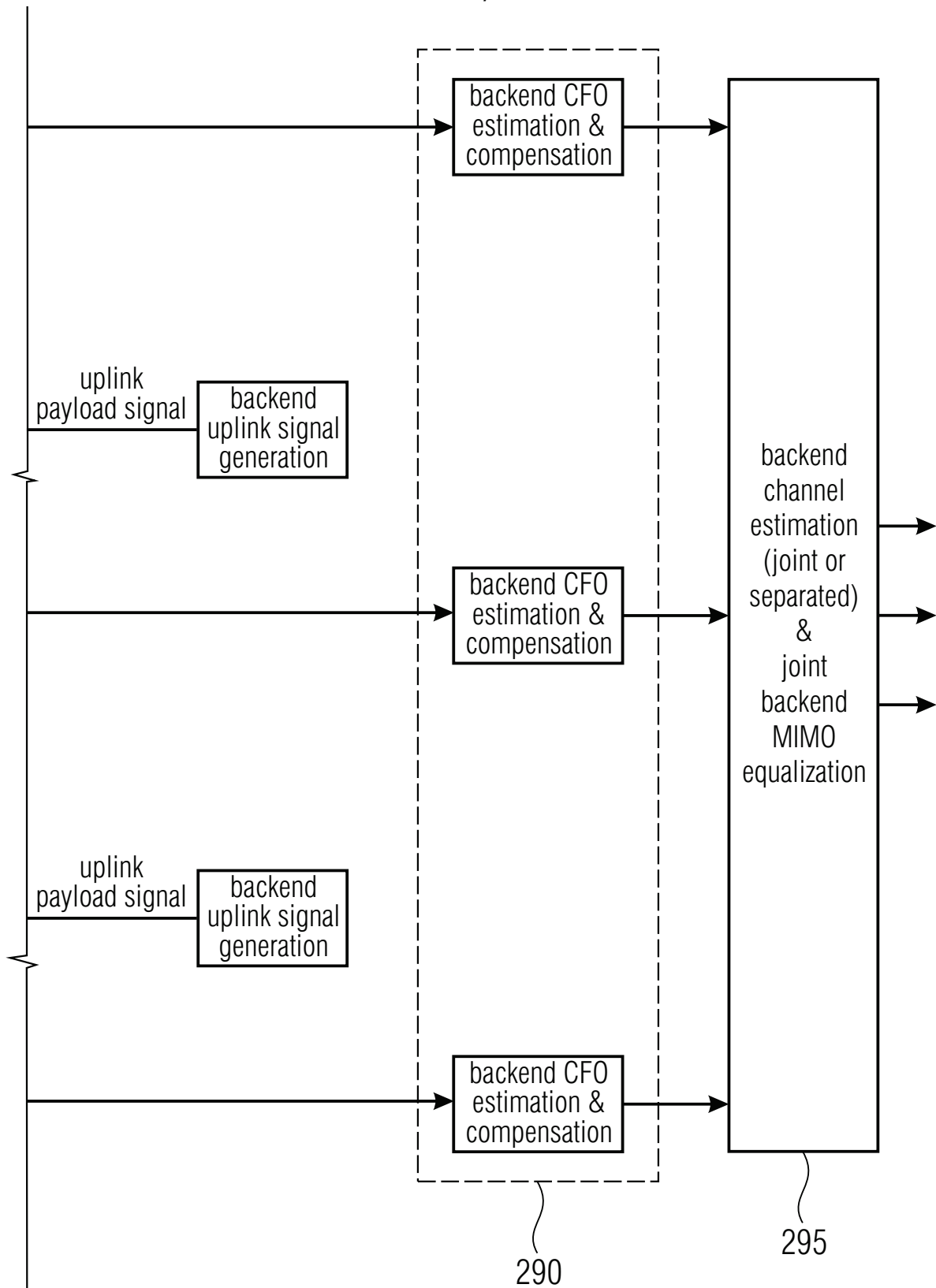


FIG 23B

FIG 23	
FIG 23A	FIG 23B

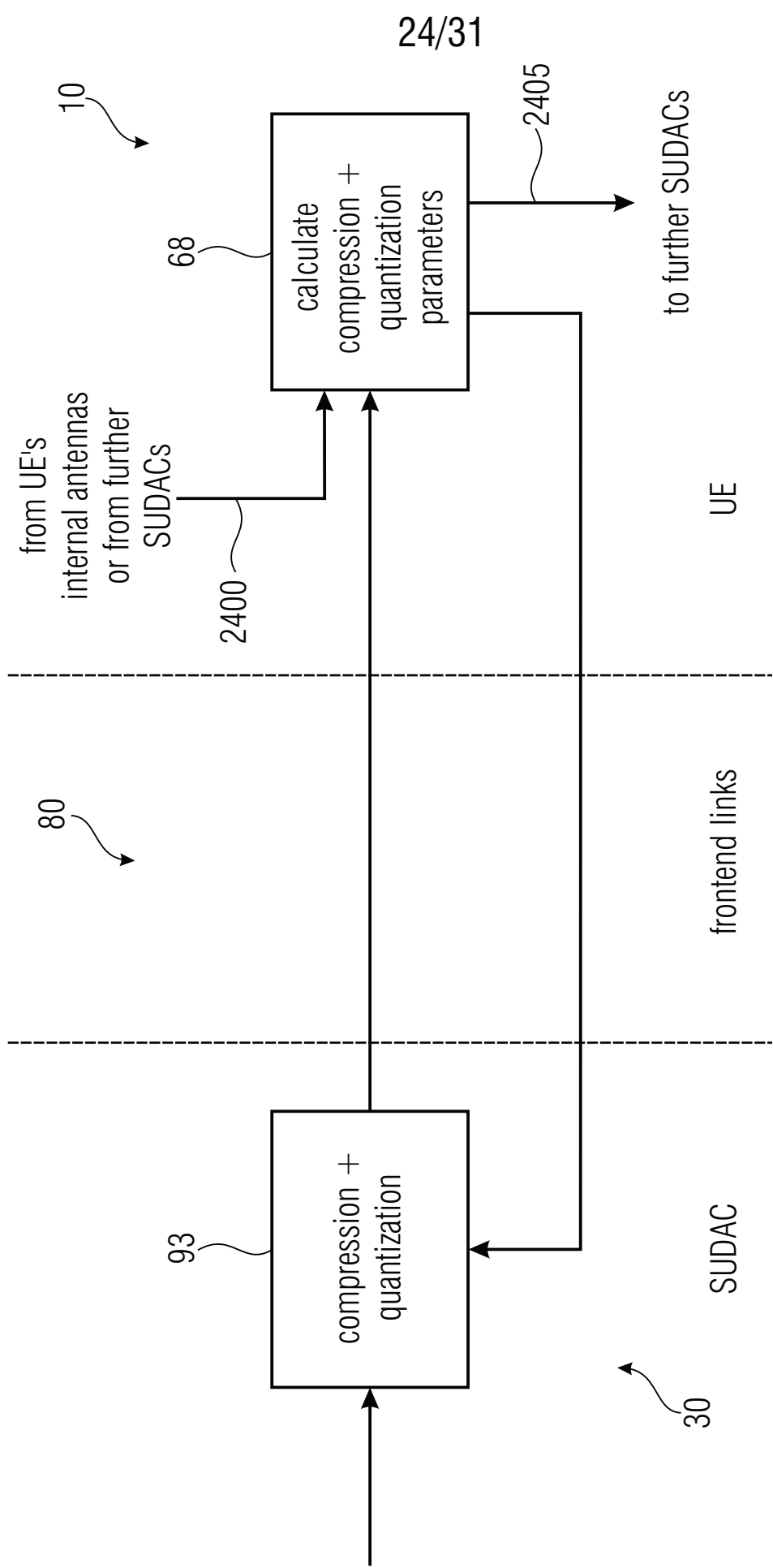
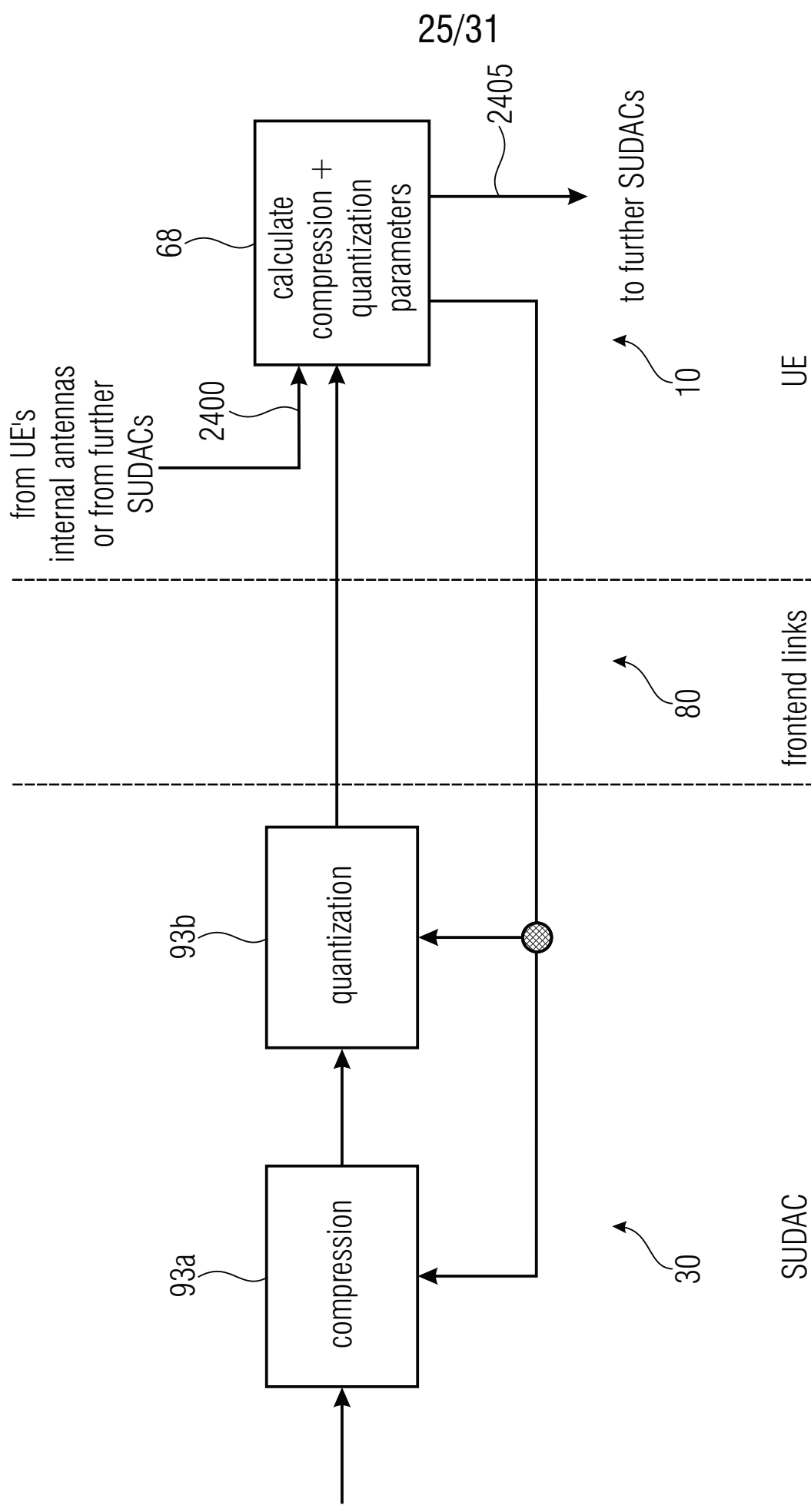


FIG 24A



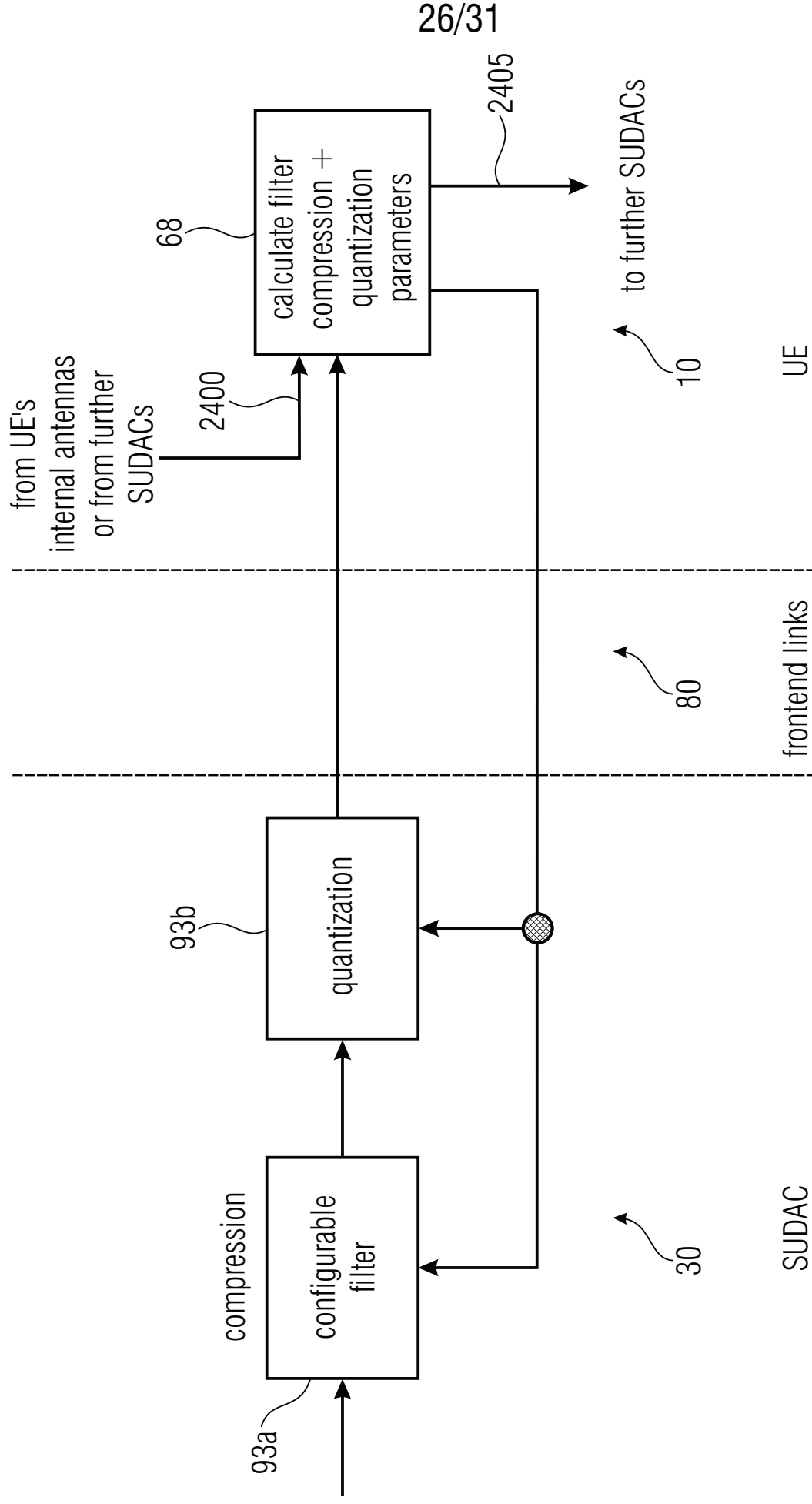


FIG 24C

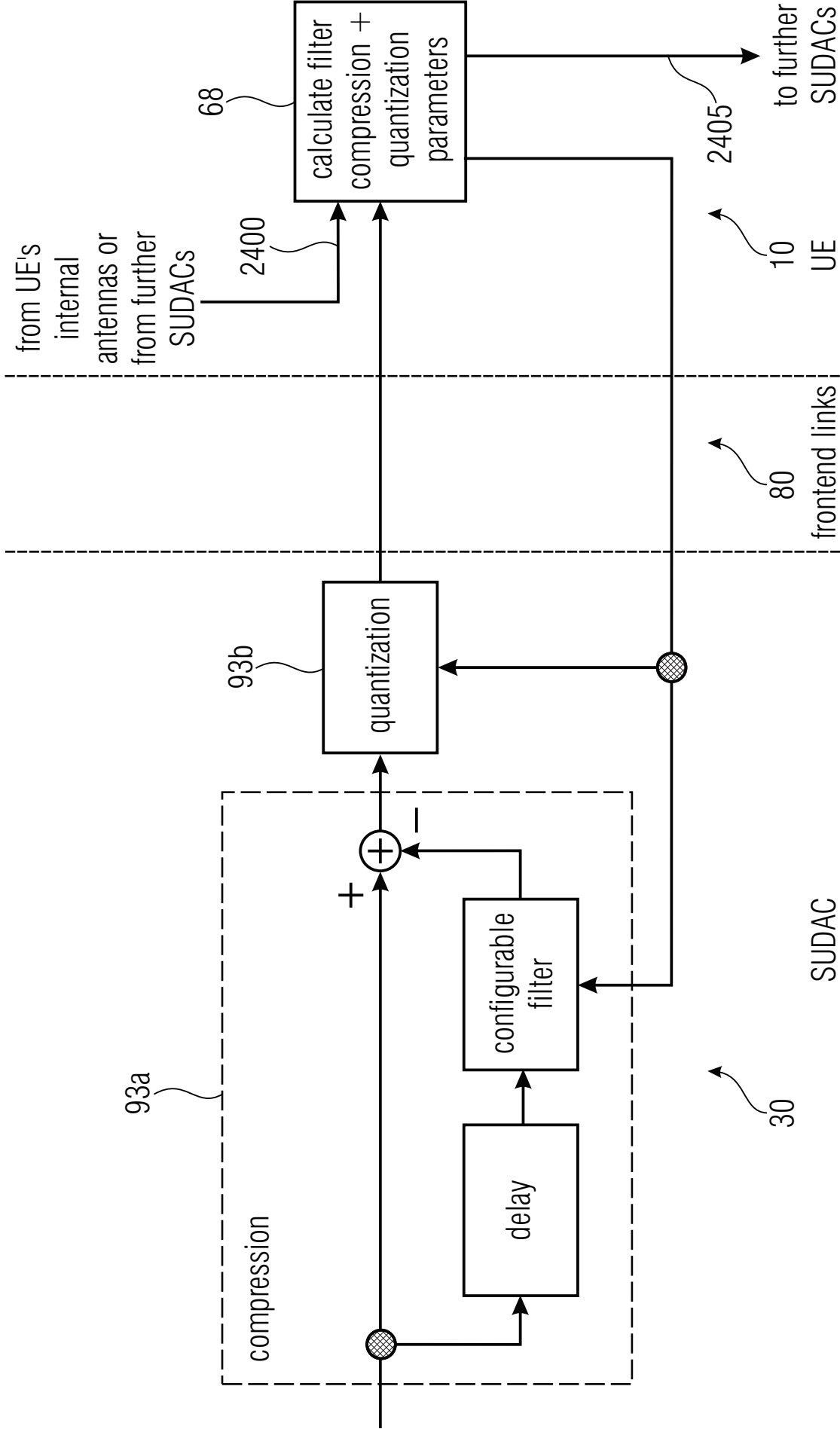


FIG 24D

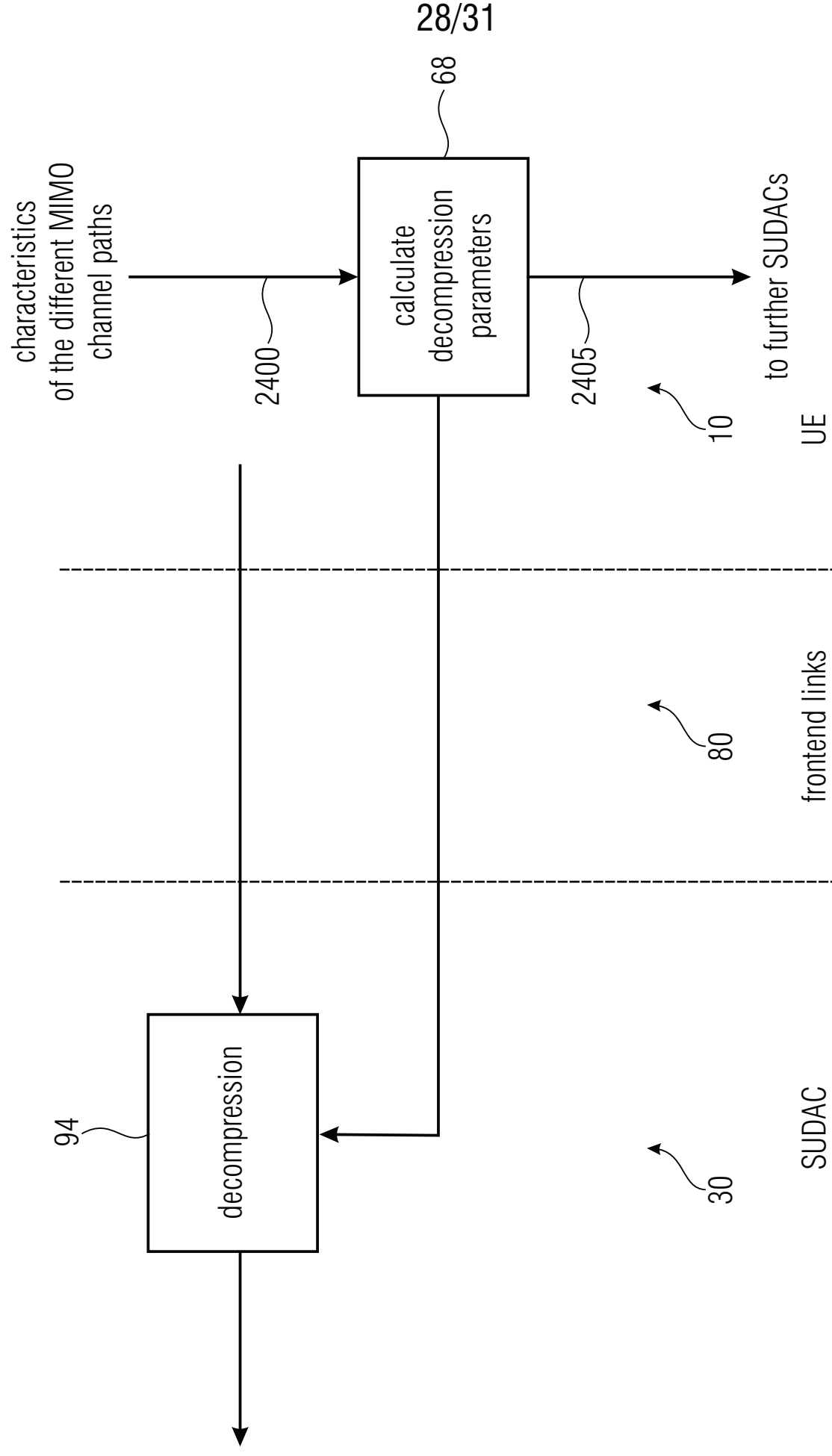


FIG 24E

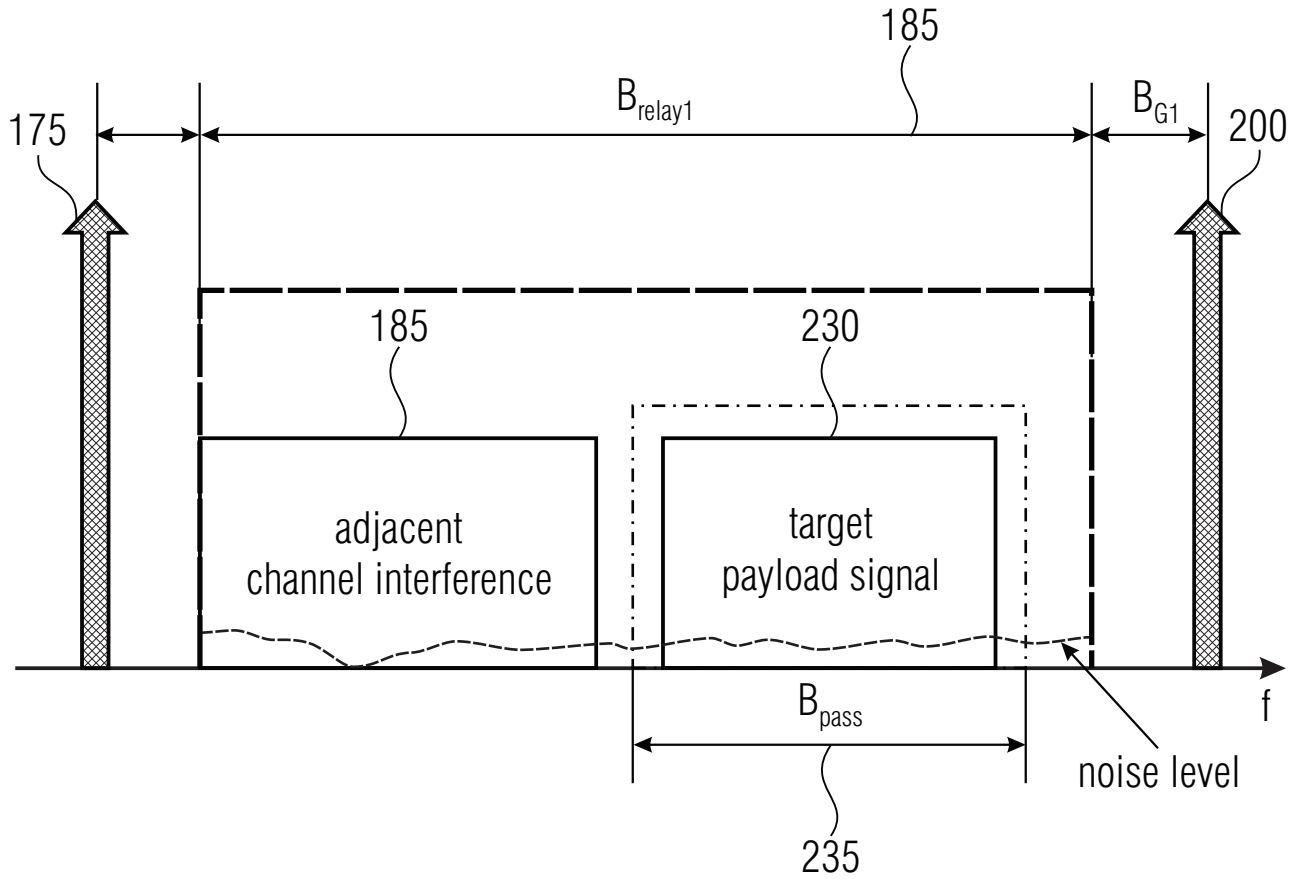


FIG 25

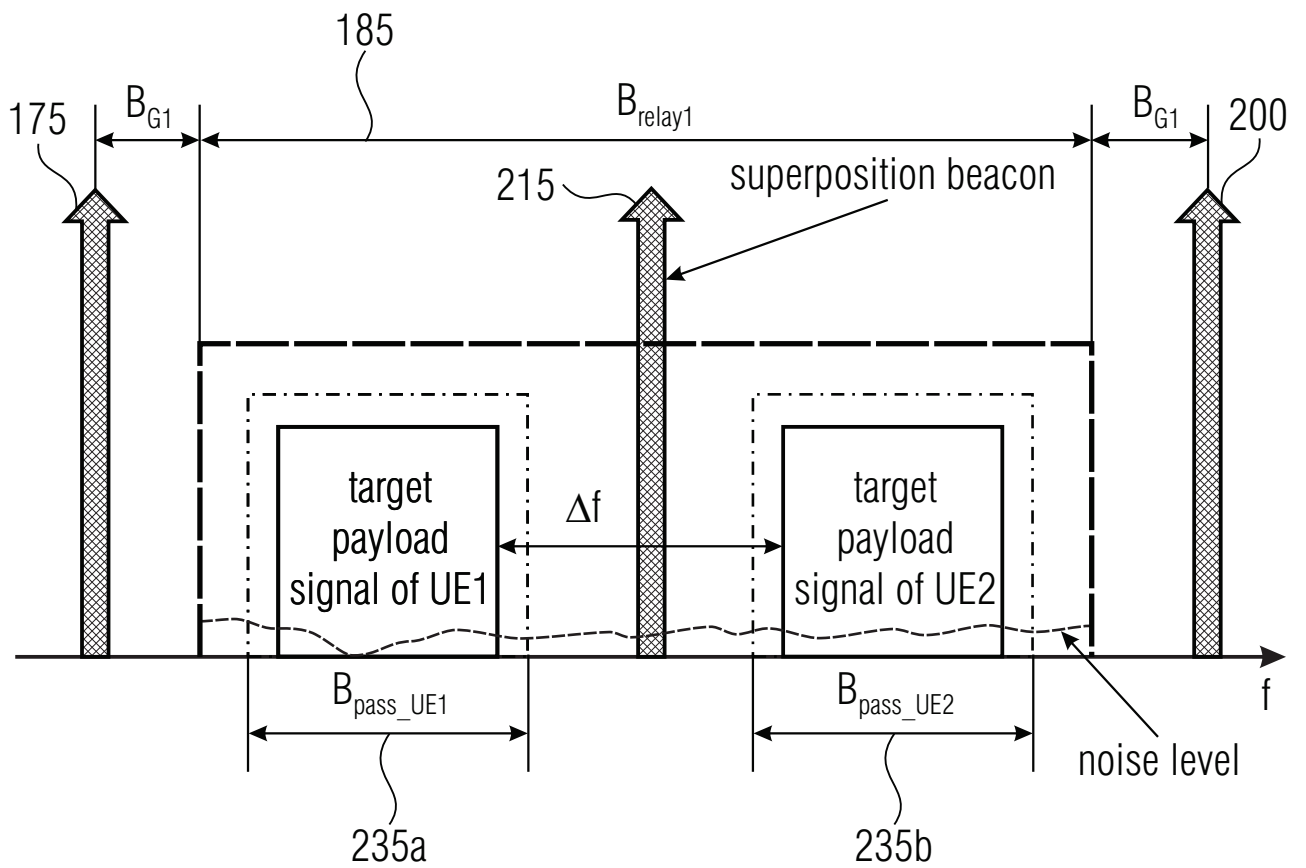


FIG 26

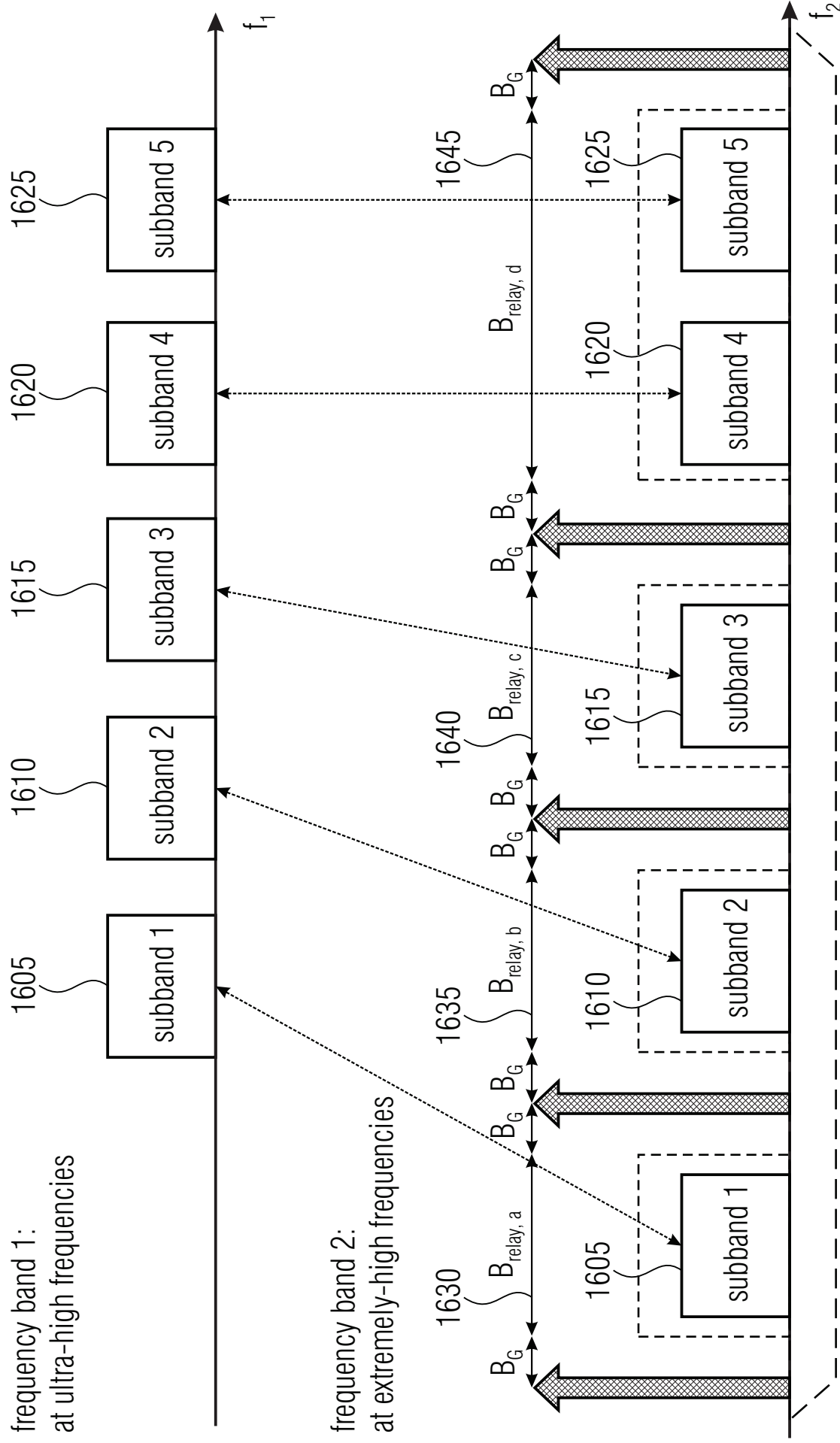


FIG 27

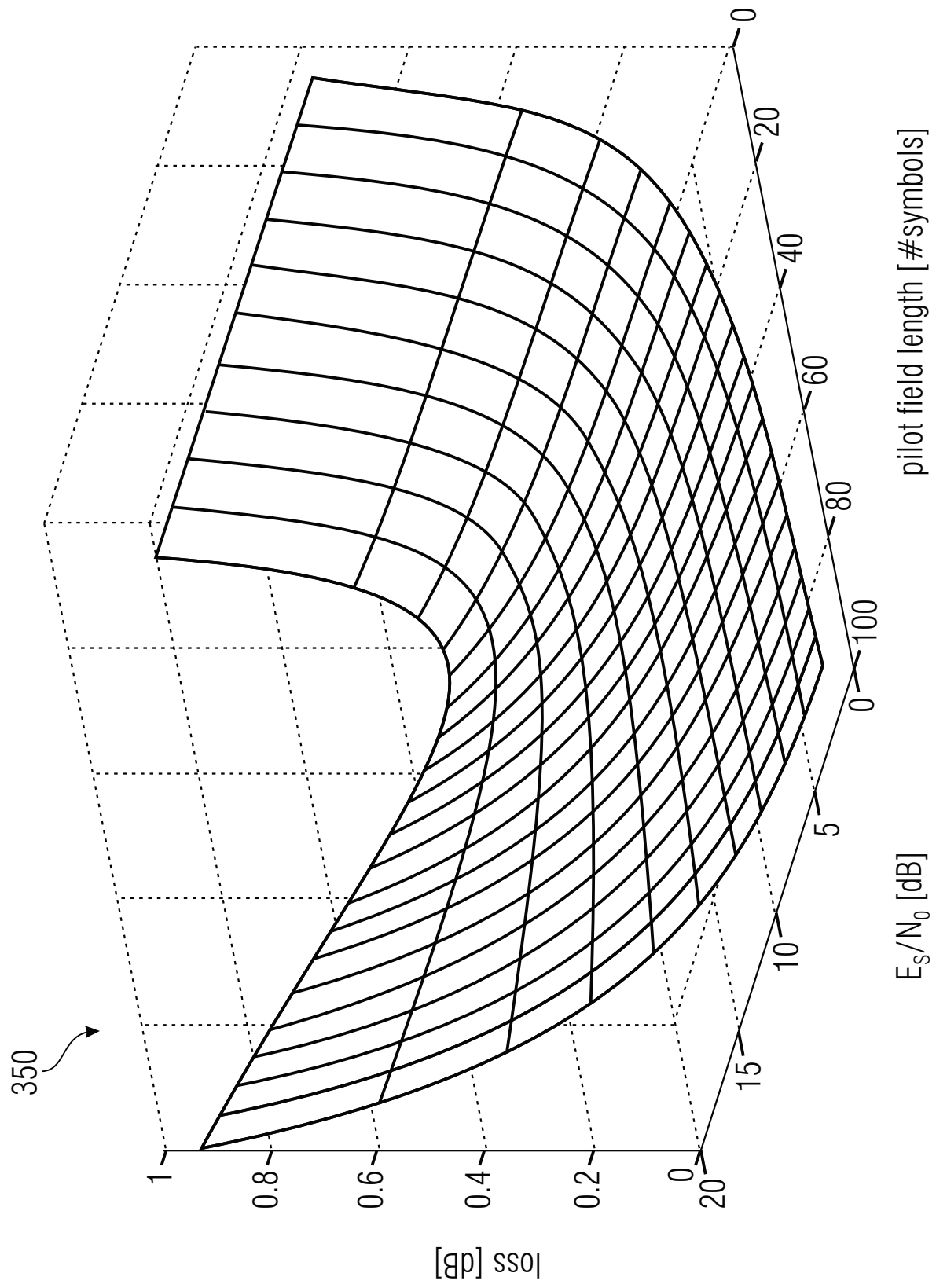


FIG 28