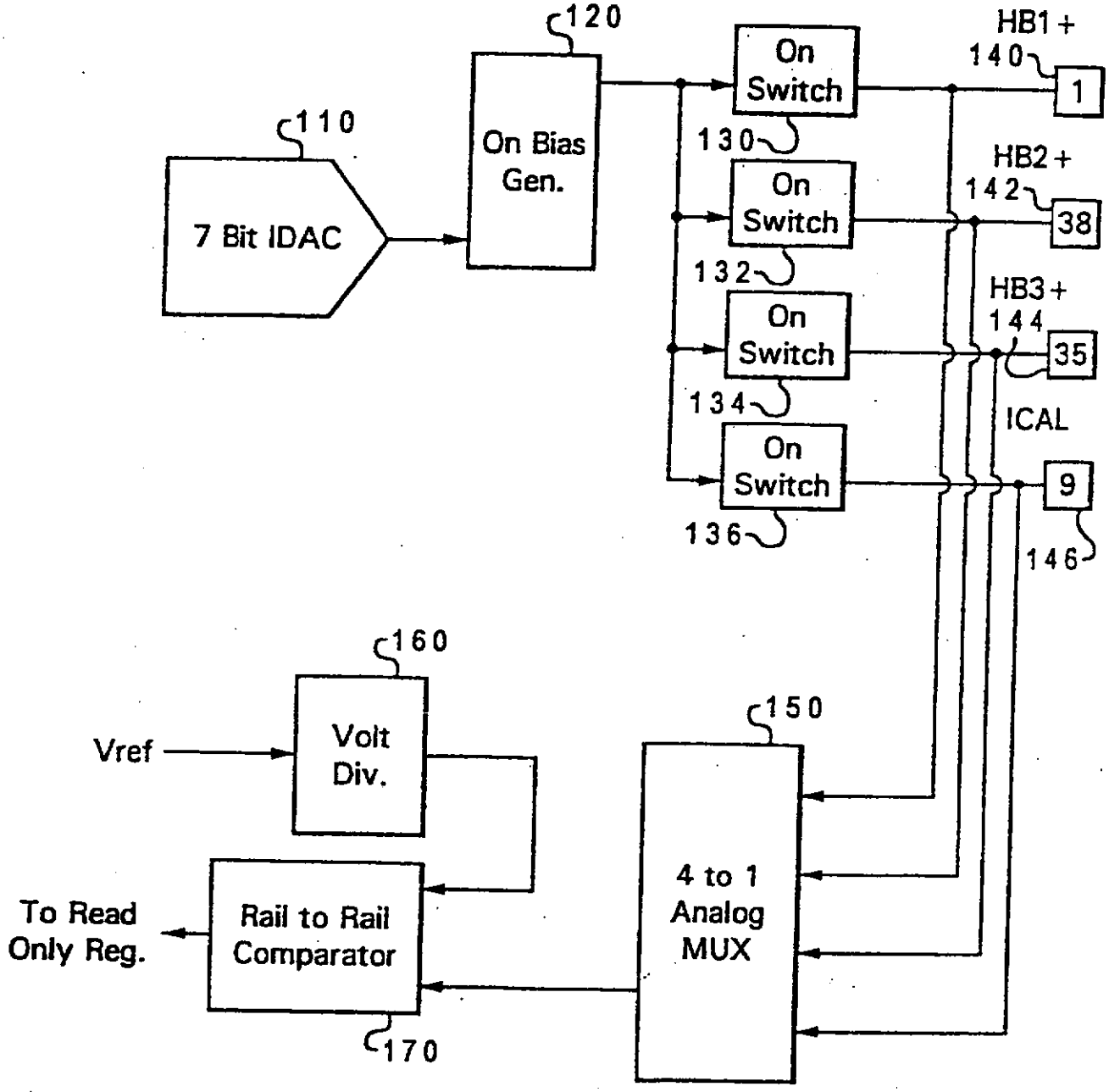

120
On Bias Gen.
110
7 Bit IDAC
On Switch
130
On Switch
132
On Switch
134
On Switch
136
HB1+
140
1
HB2+
142
38
HB3+
144
35
ICAL
9
146
160
Volt Div.
Vref
150
4 to 1 Analog MUX
To Read Only Reg.
Rail to Rail Comparator
170

Fig. 1

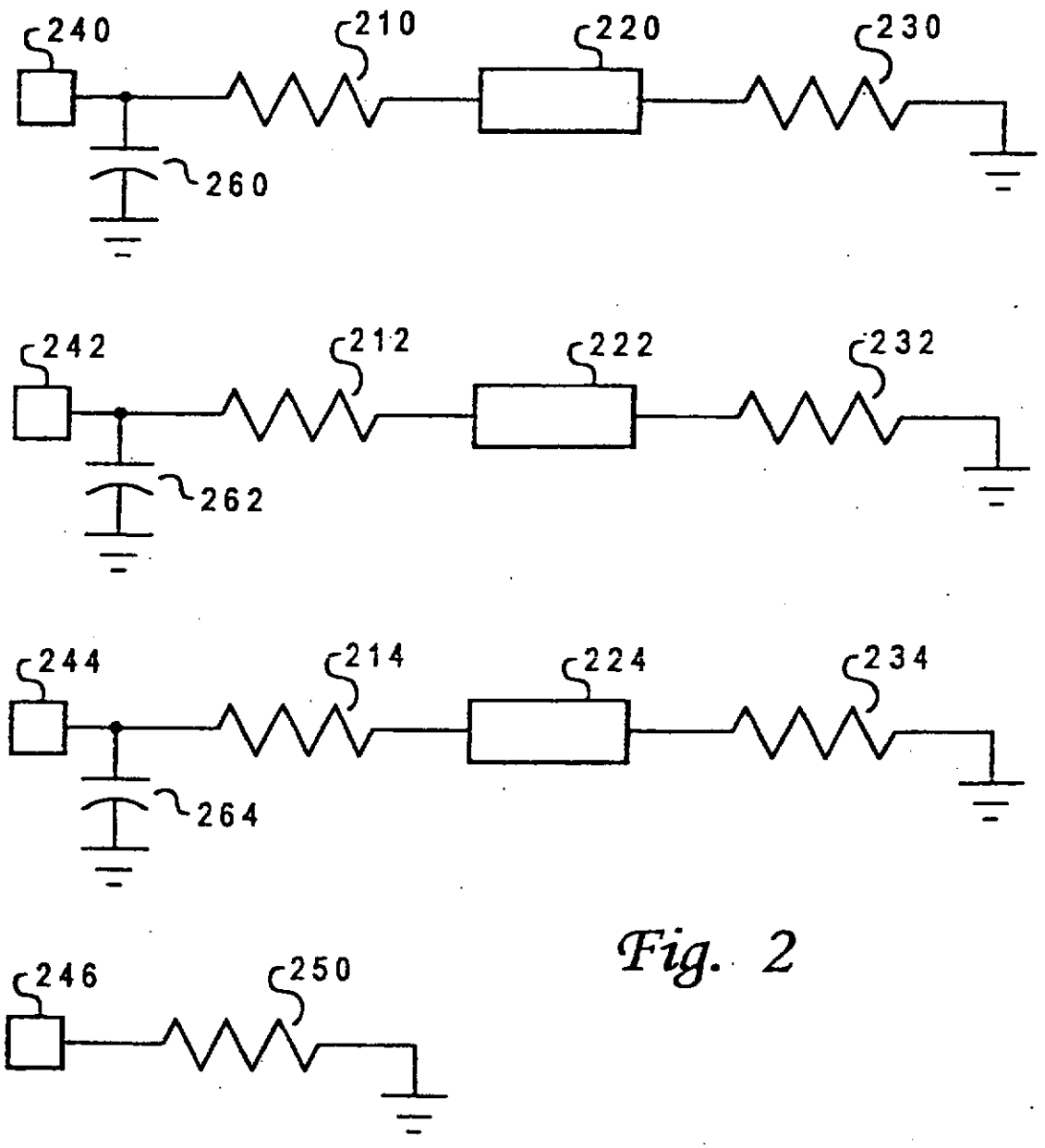

240
210
220
230
260
242
212
222
232
262
244
214
224
234
264
246
250

Fig. 2

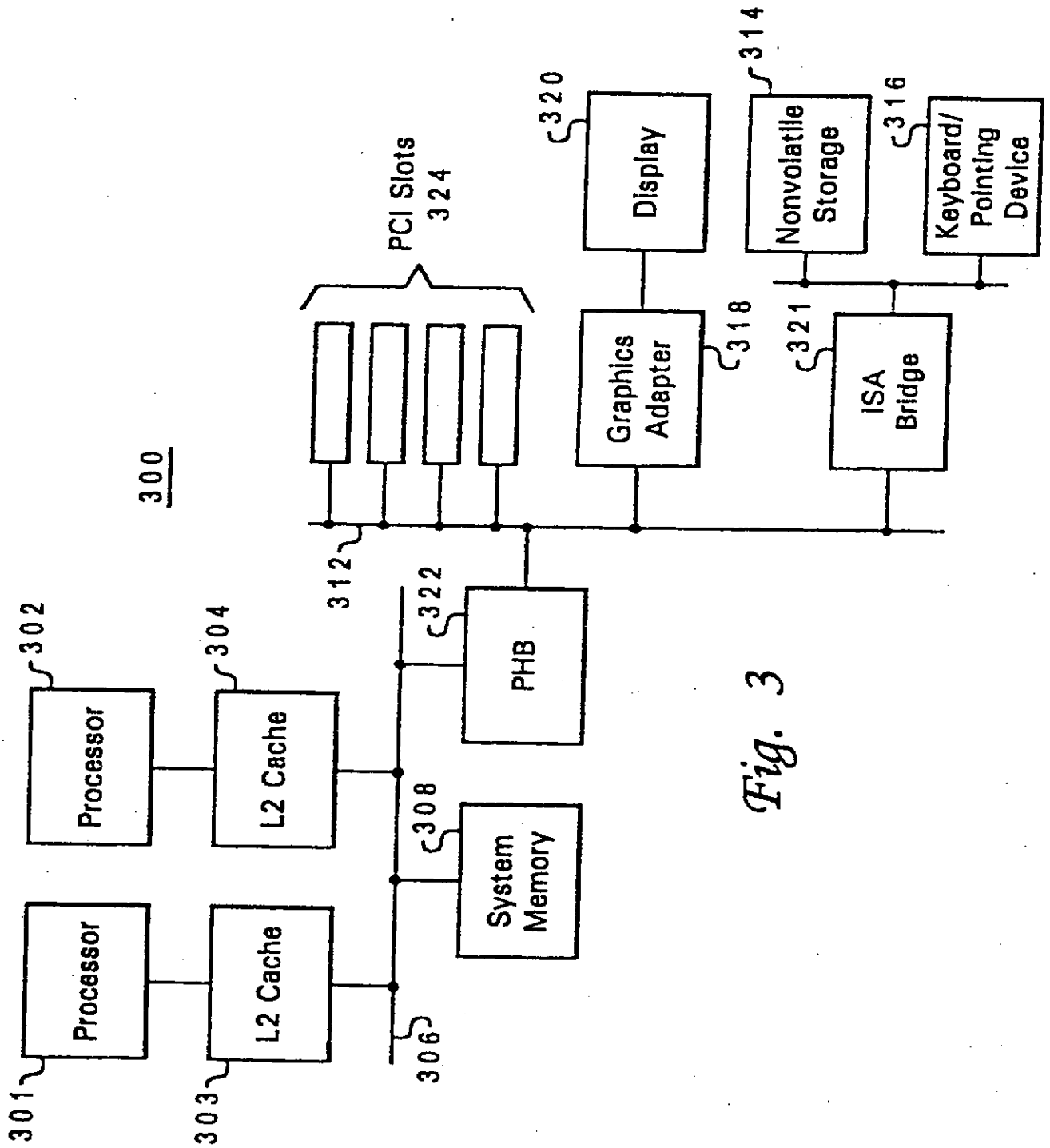

300
Processor
L2 Cache
Processor
L2 Cache
PHB
System Memory
PCI Slots
Display
Nonvolatile Storage
Keyboard/ Pointing Device
Graphics Adapter
ISA Bridge
301
302
303
304
306
308
312
314
316
318
320
321
322
324

Fig. 3

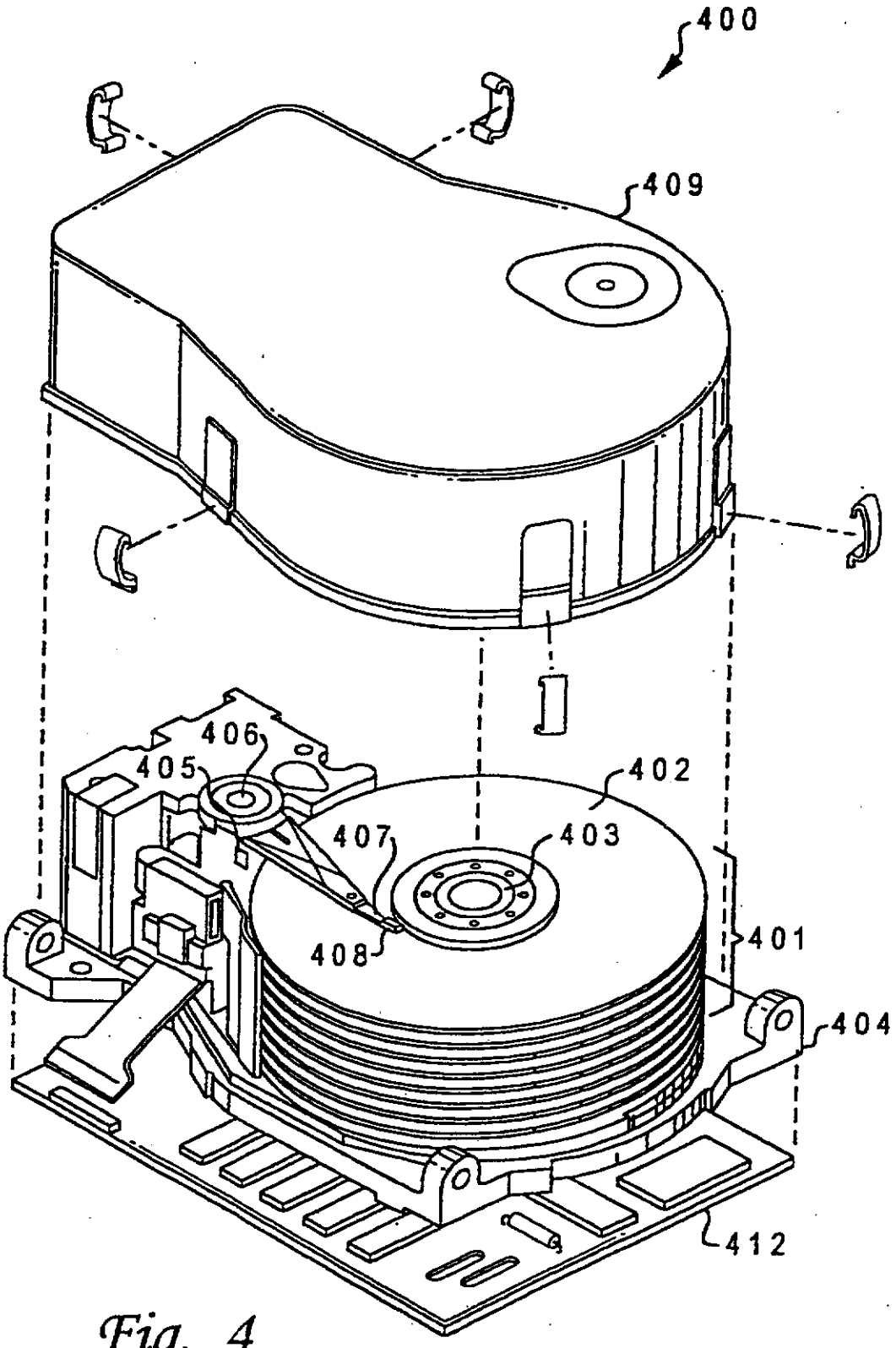


Fig. 4