

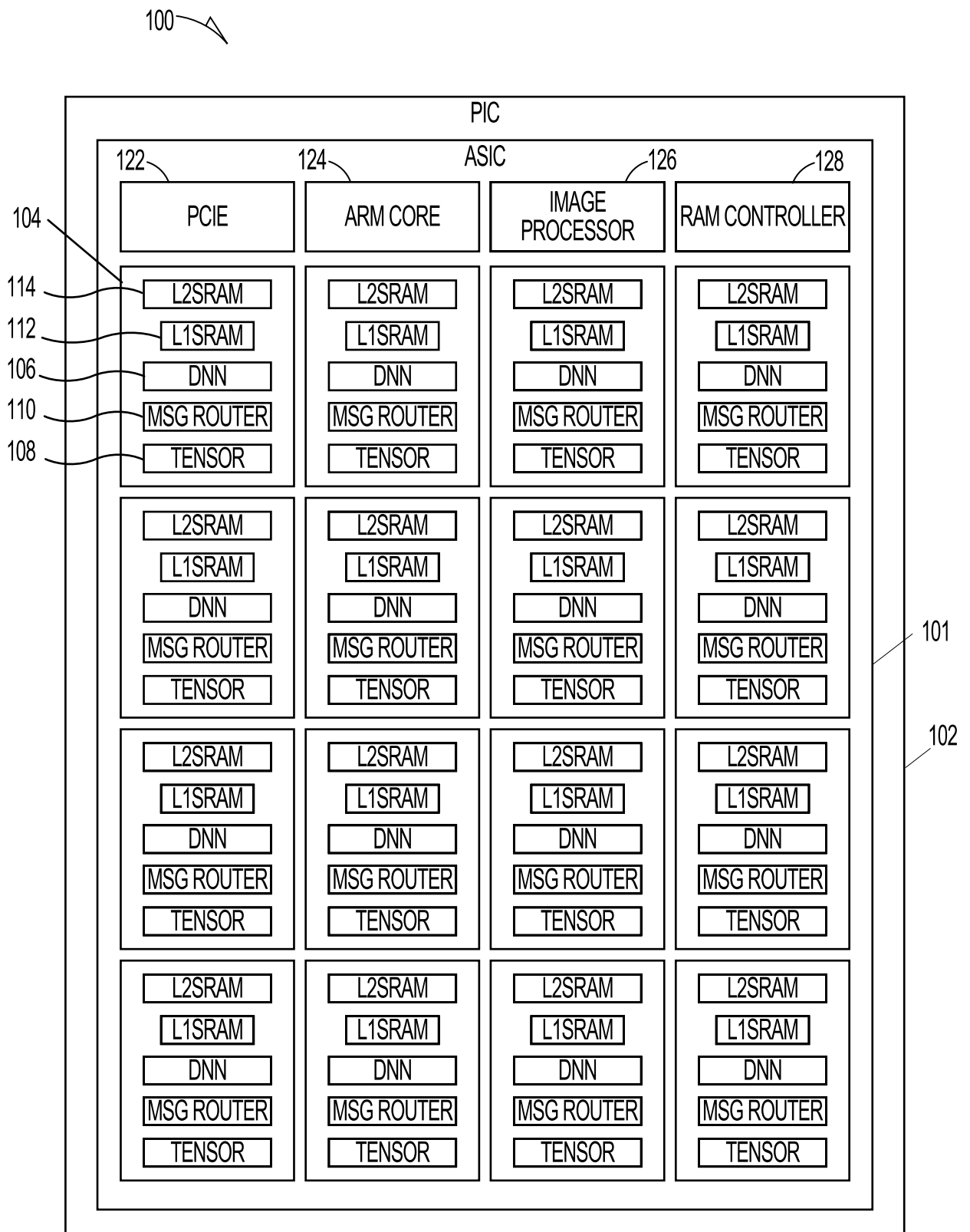


FIG. 1A

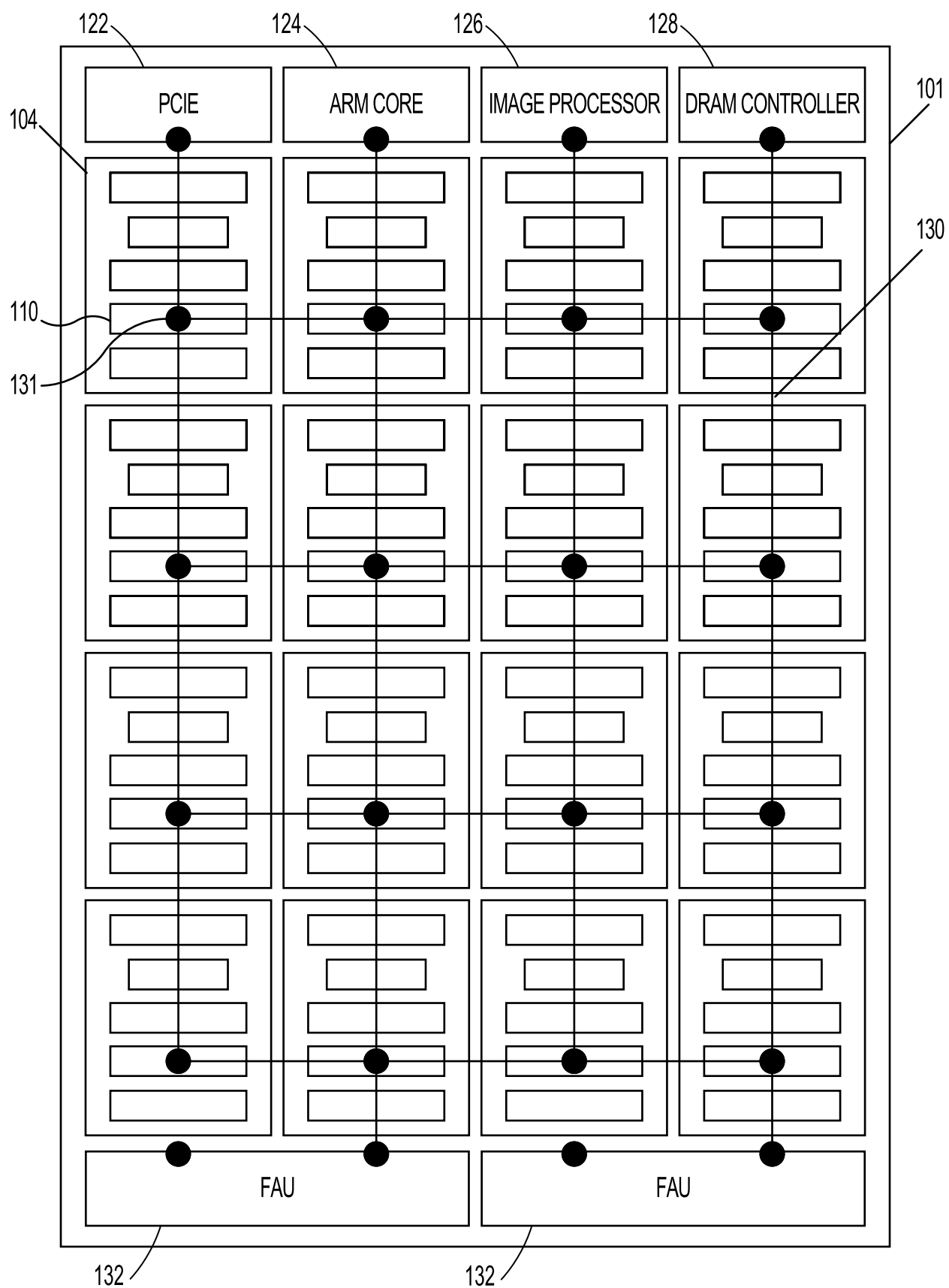


FIG. 1B

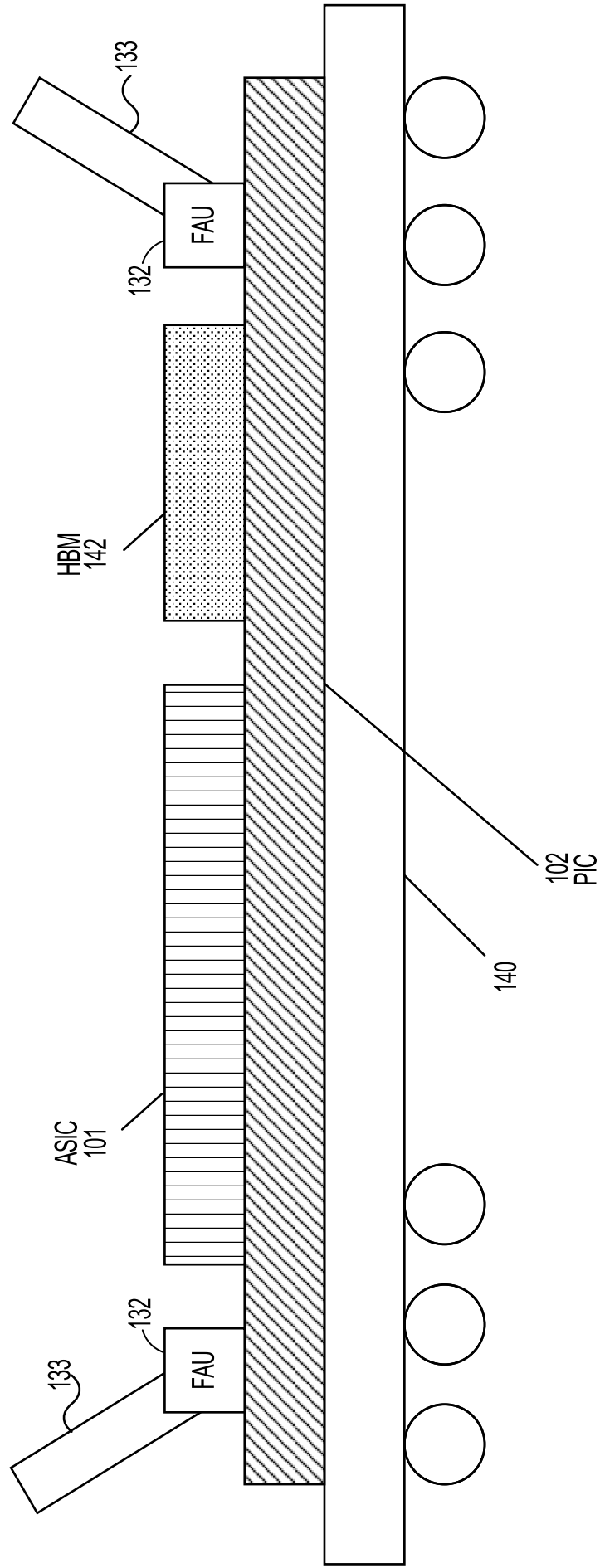
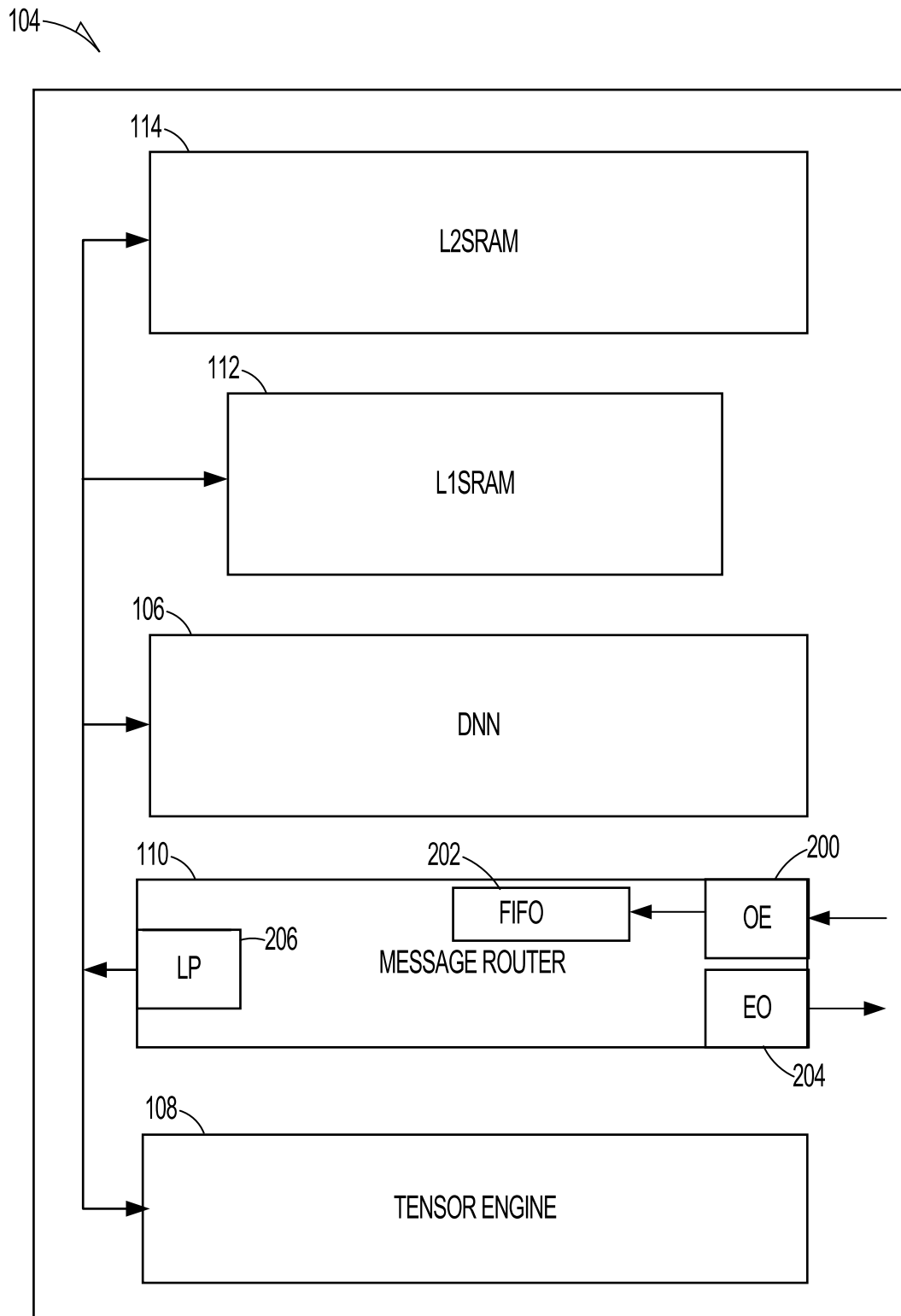
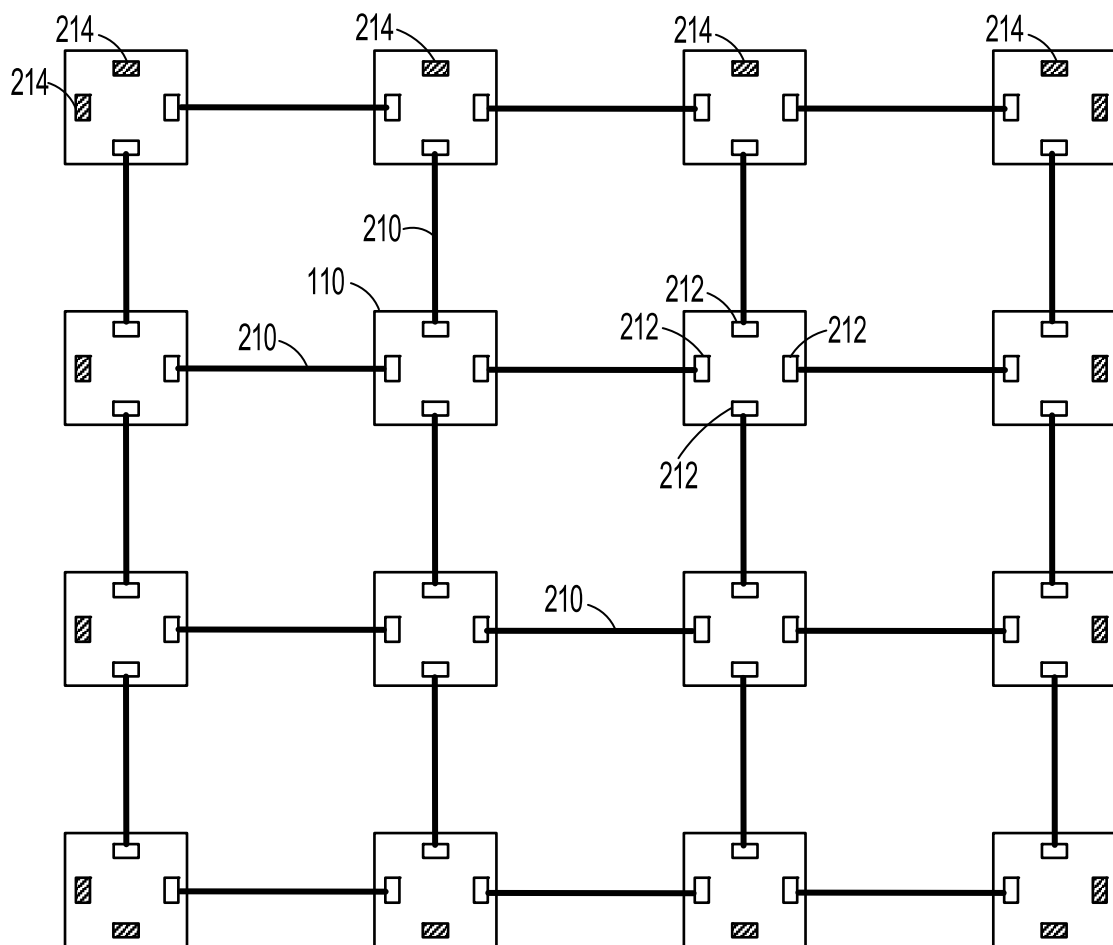


FIG. 1C

**FIG. 2A**

*FIG. 2B*

108

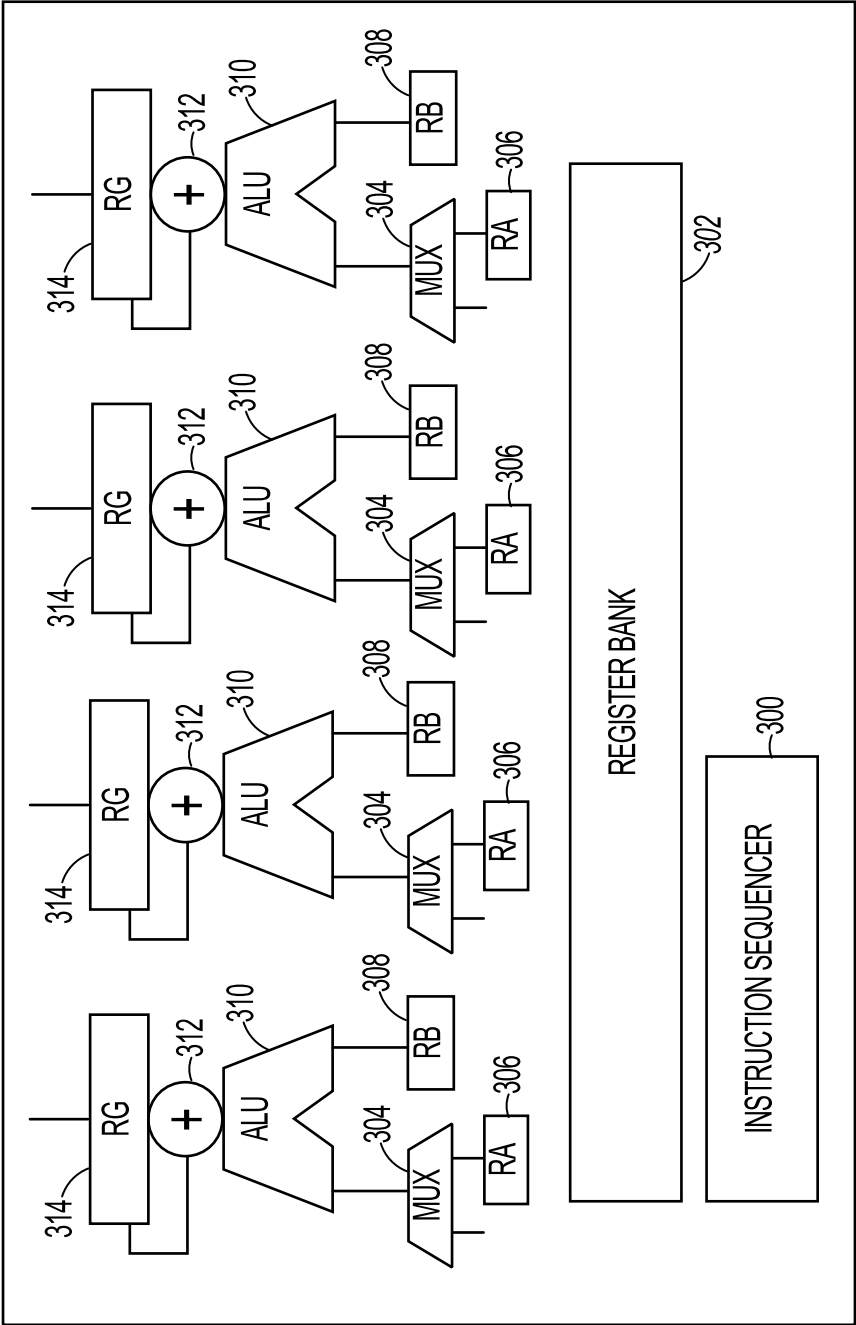


FIG. 3

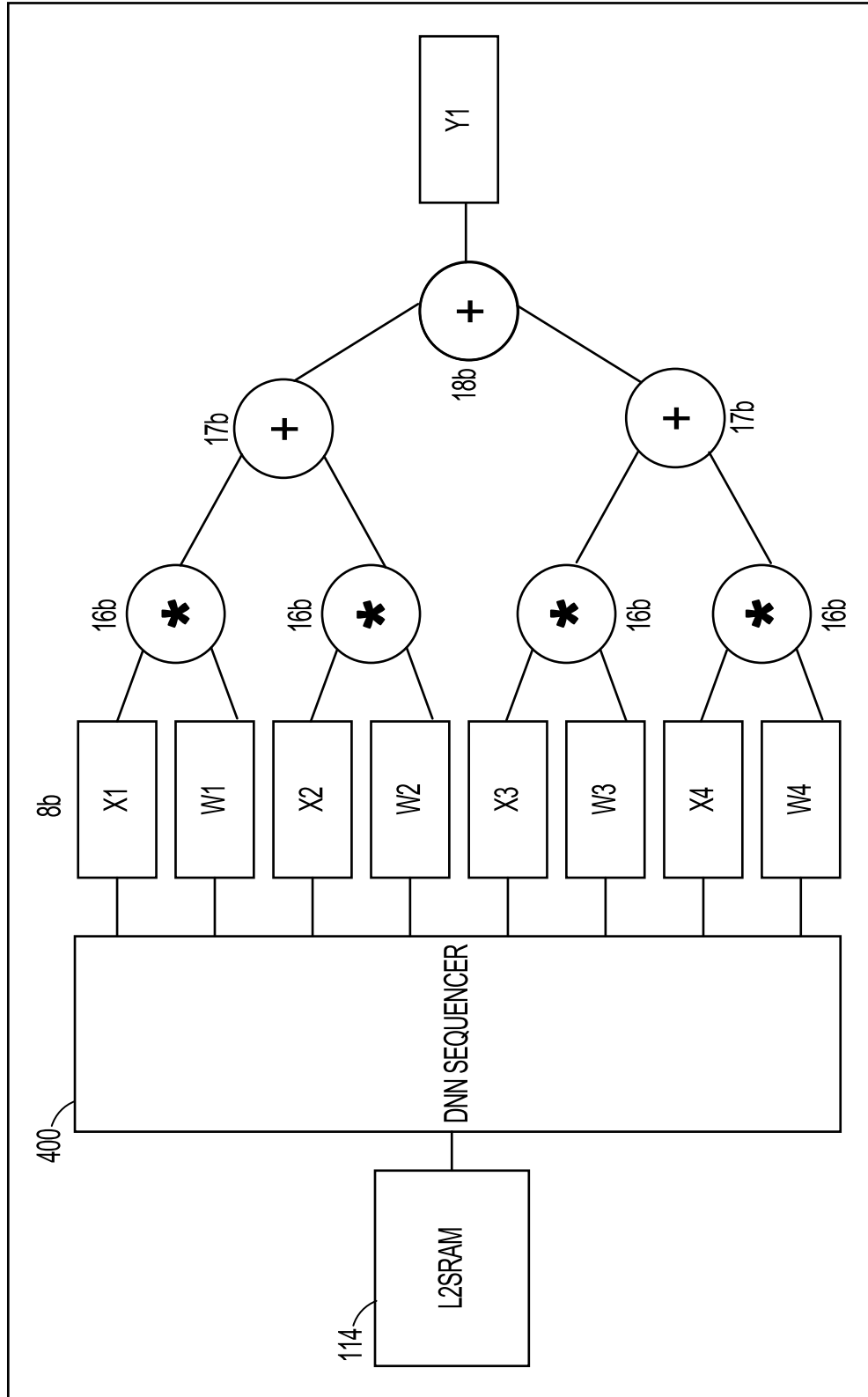


FIG. 4

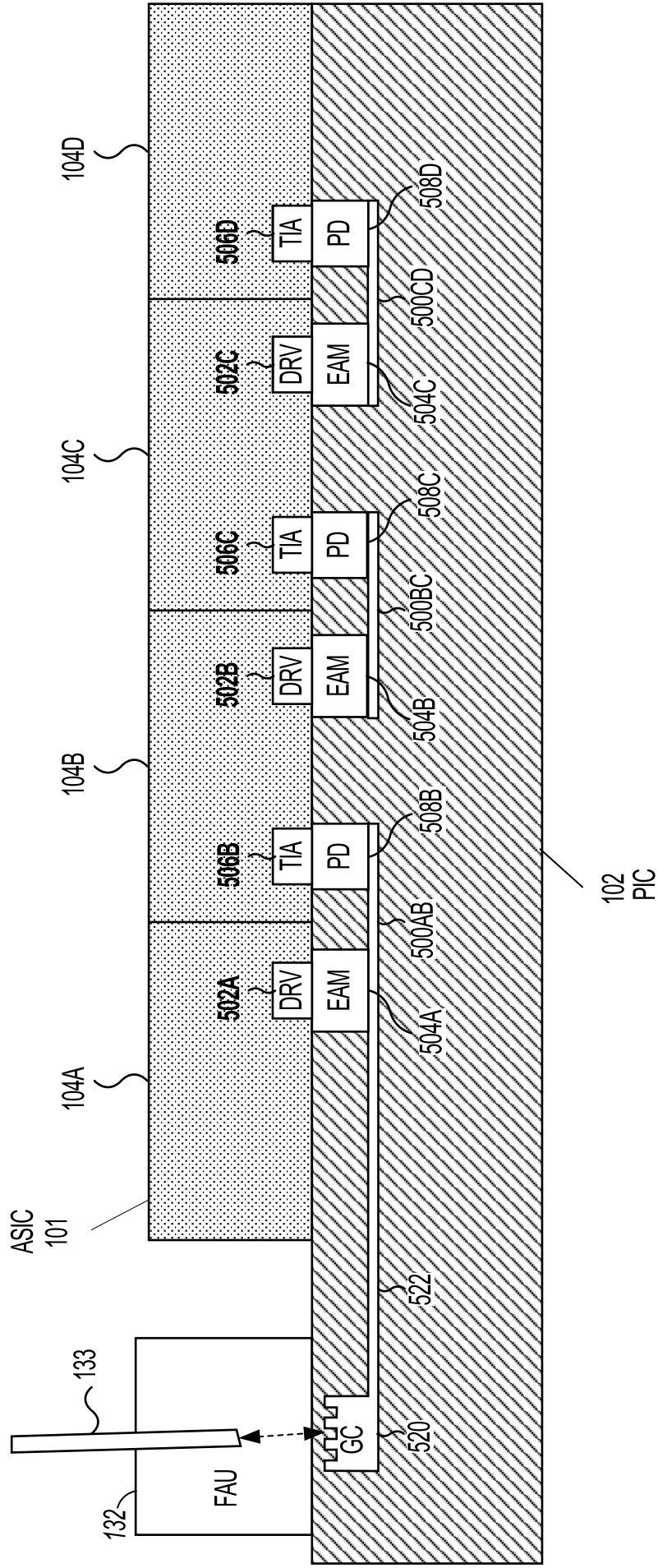


FIG. 5A

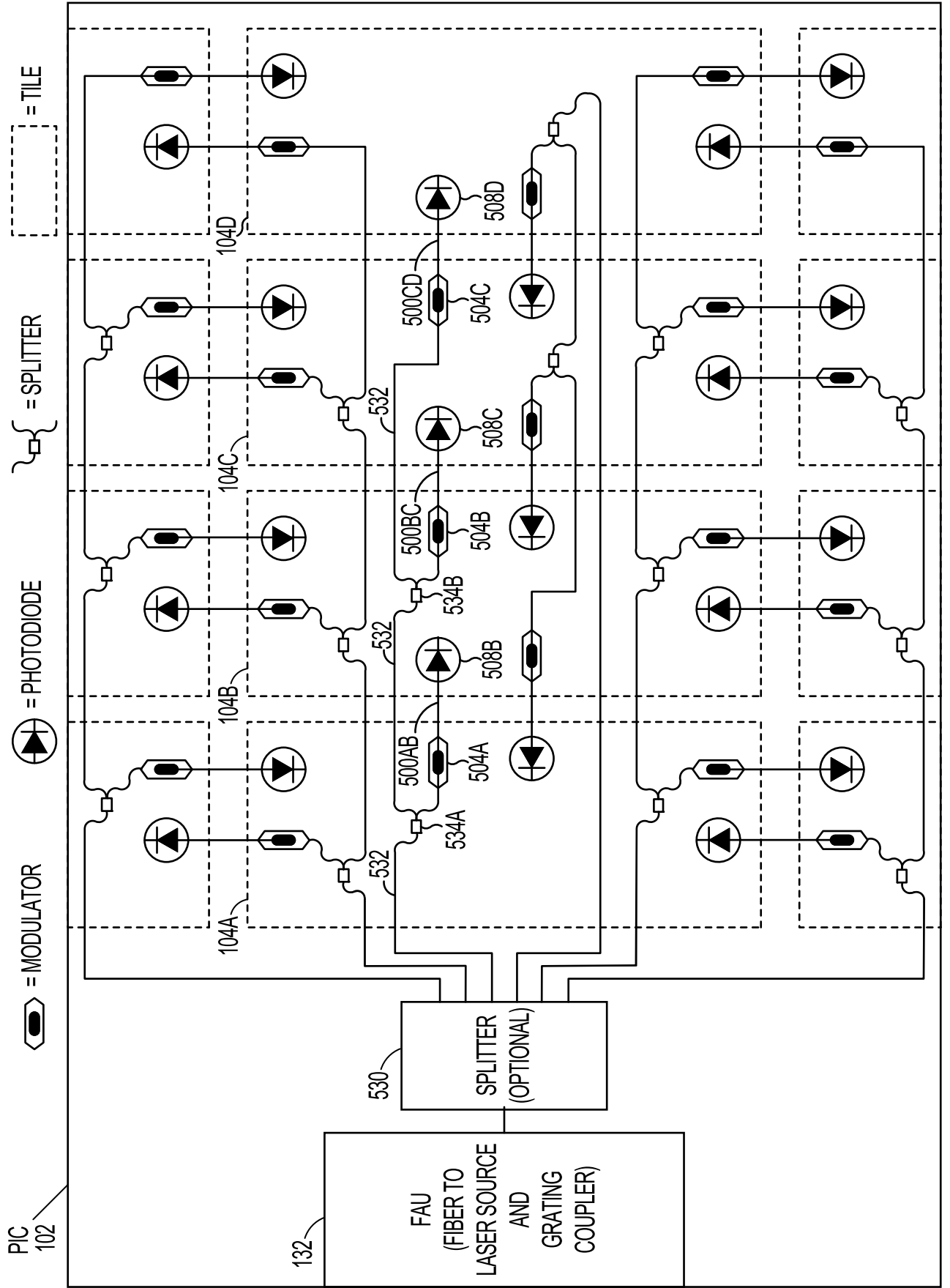


FIG. 5B

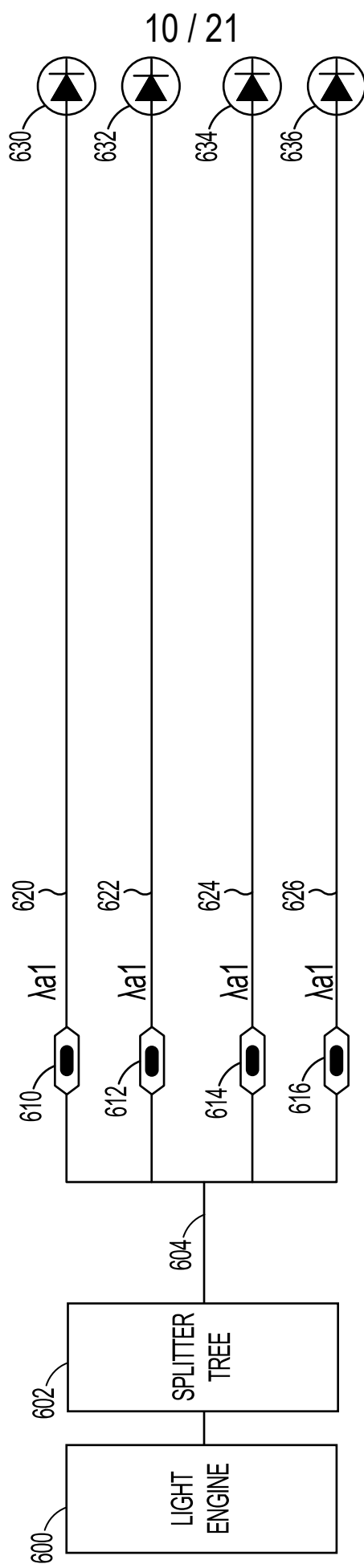


FIG. 6A
(ON-CHIP, NO WDM)

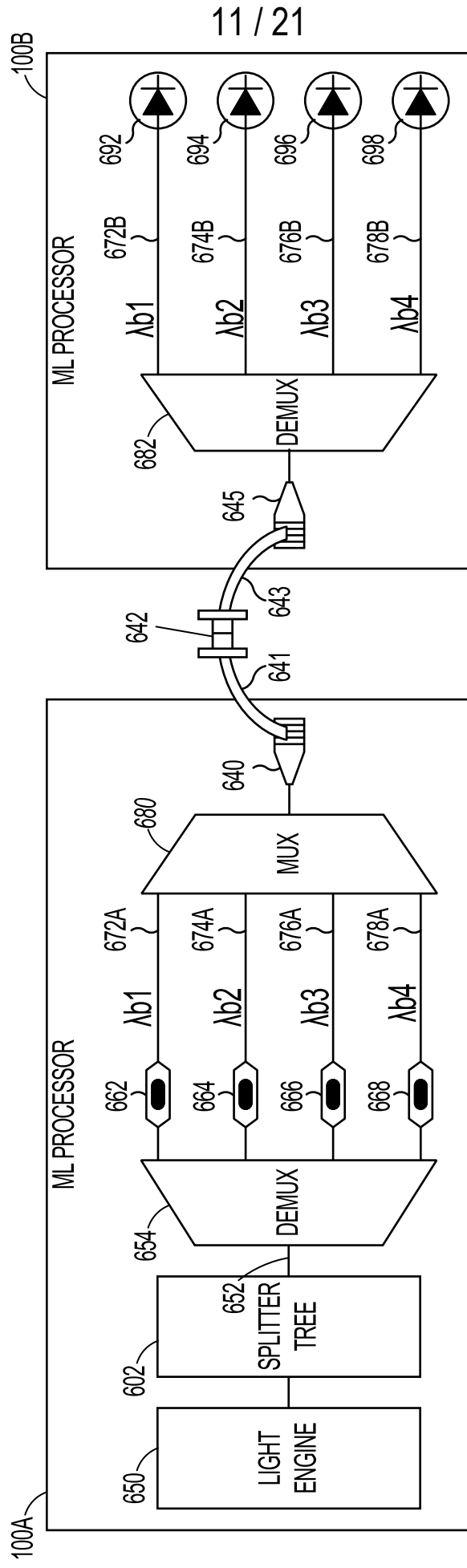


FIG. 6B
(CHIP-TO-CHIP, WDM)

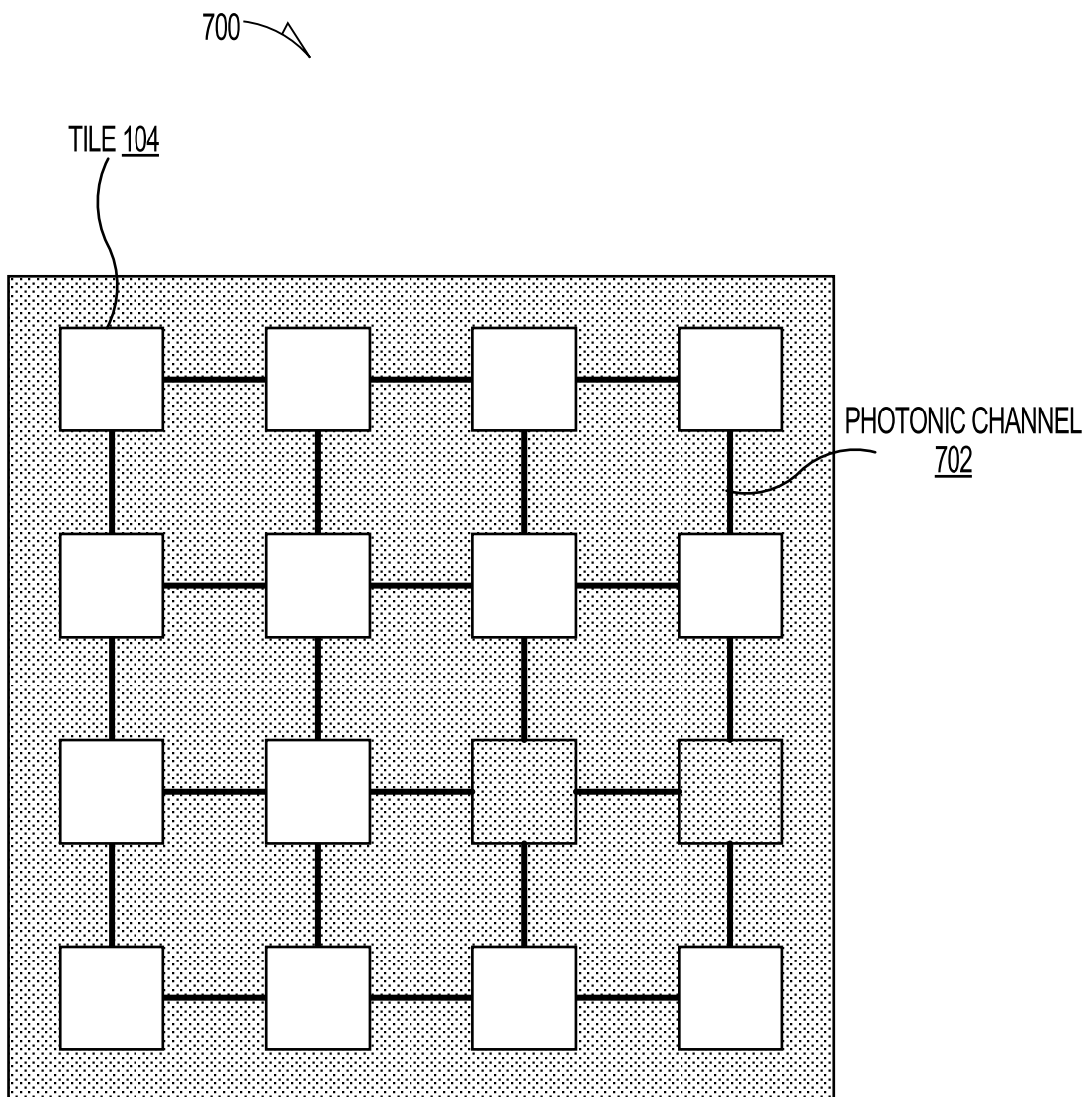


FIG. 7

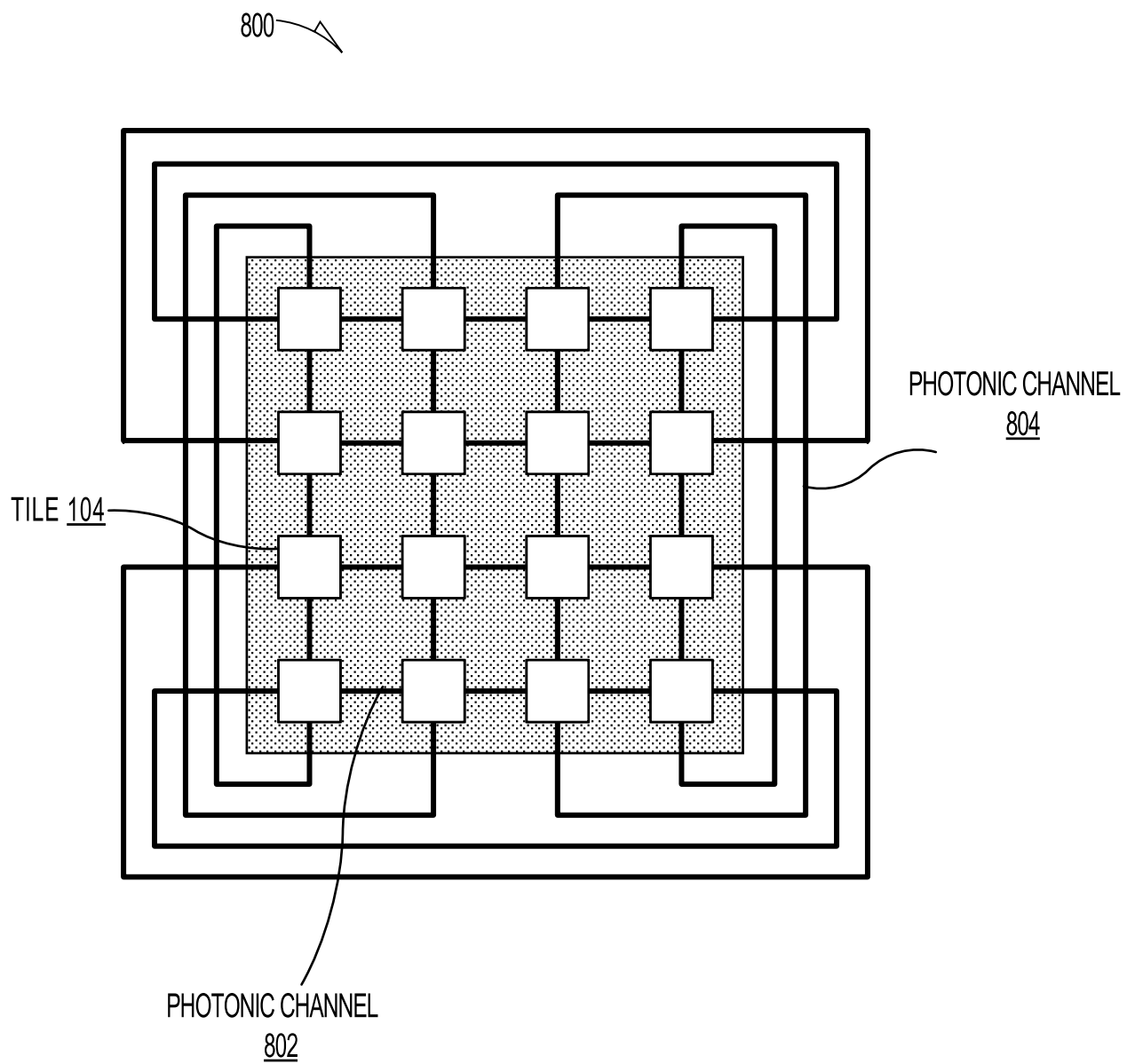


FIG. 8

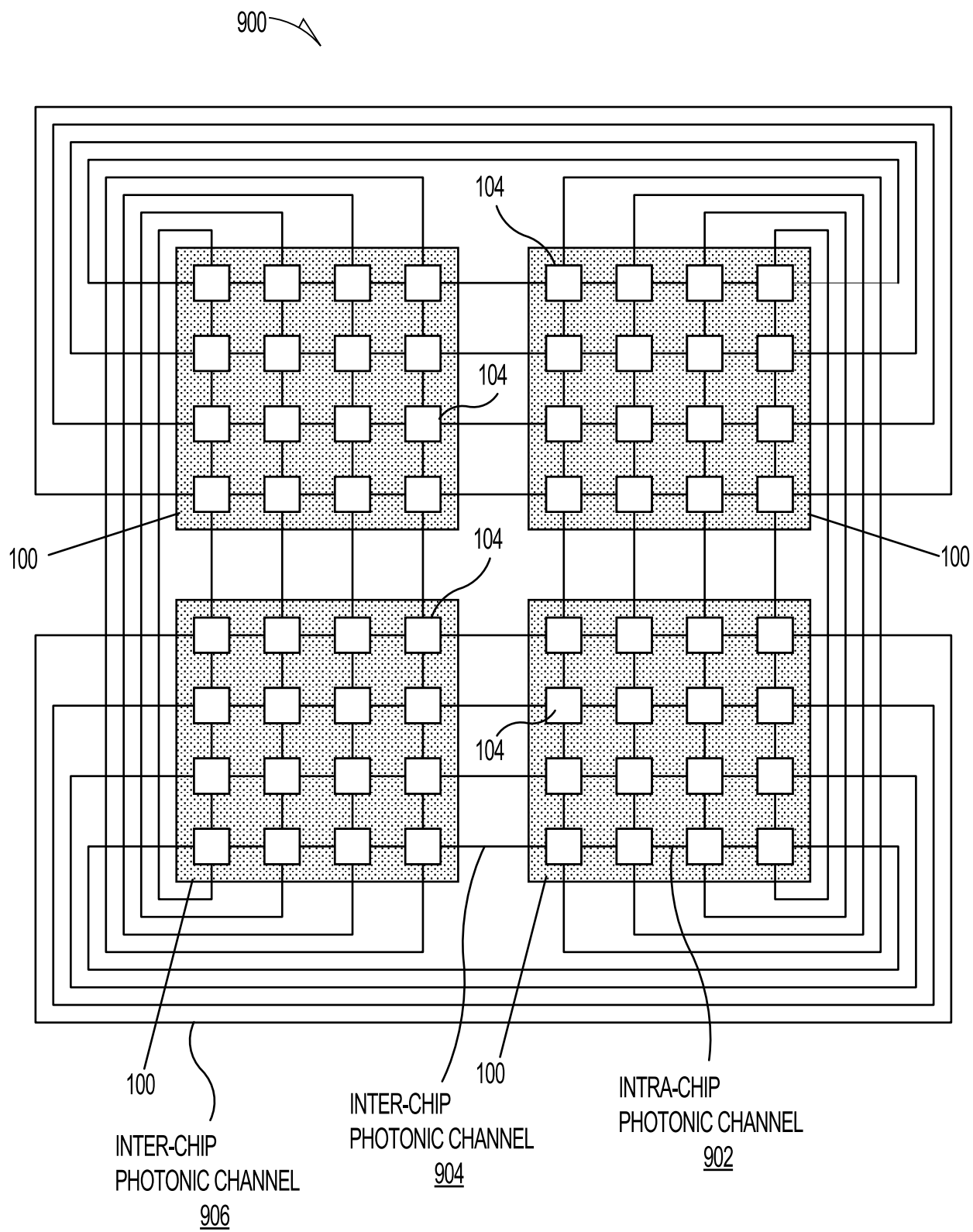


FIG. 9

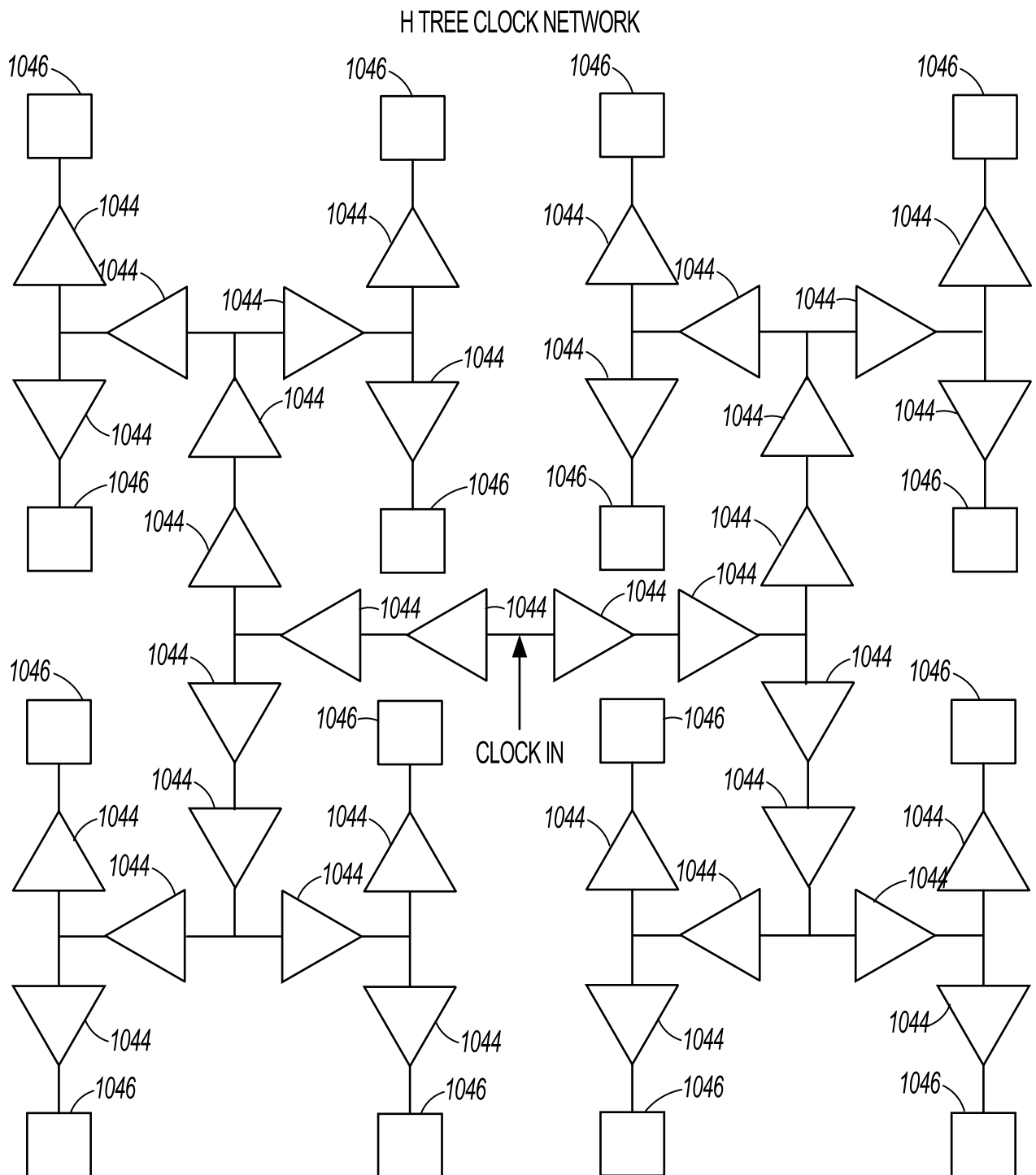


FIG. 10
(PRIOR ART)

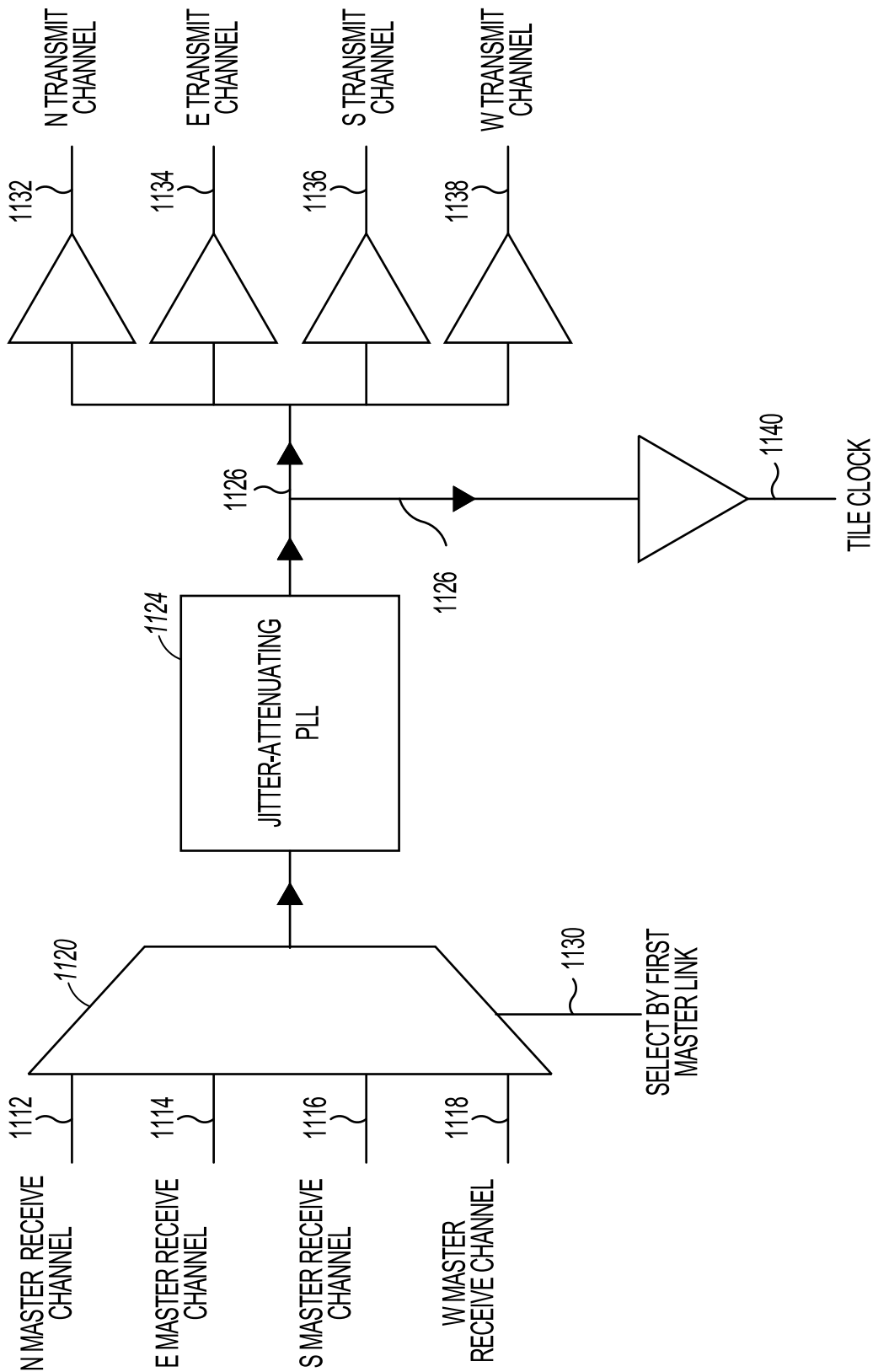


FIG. 11

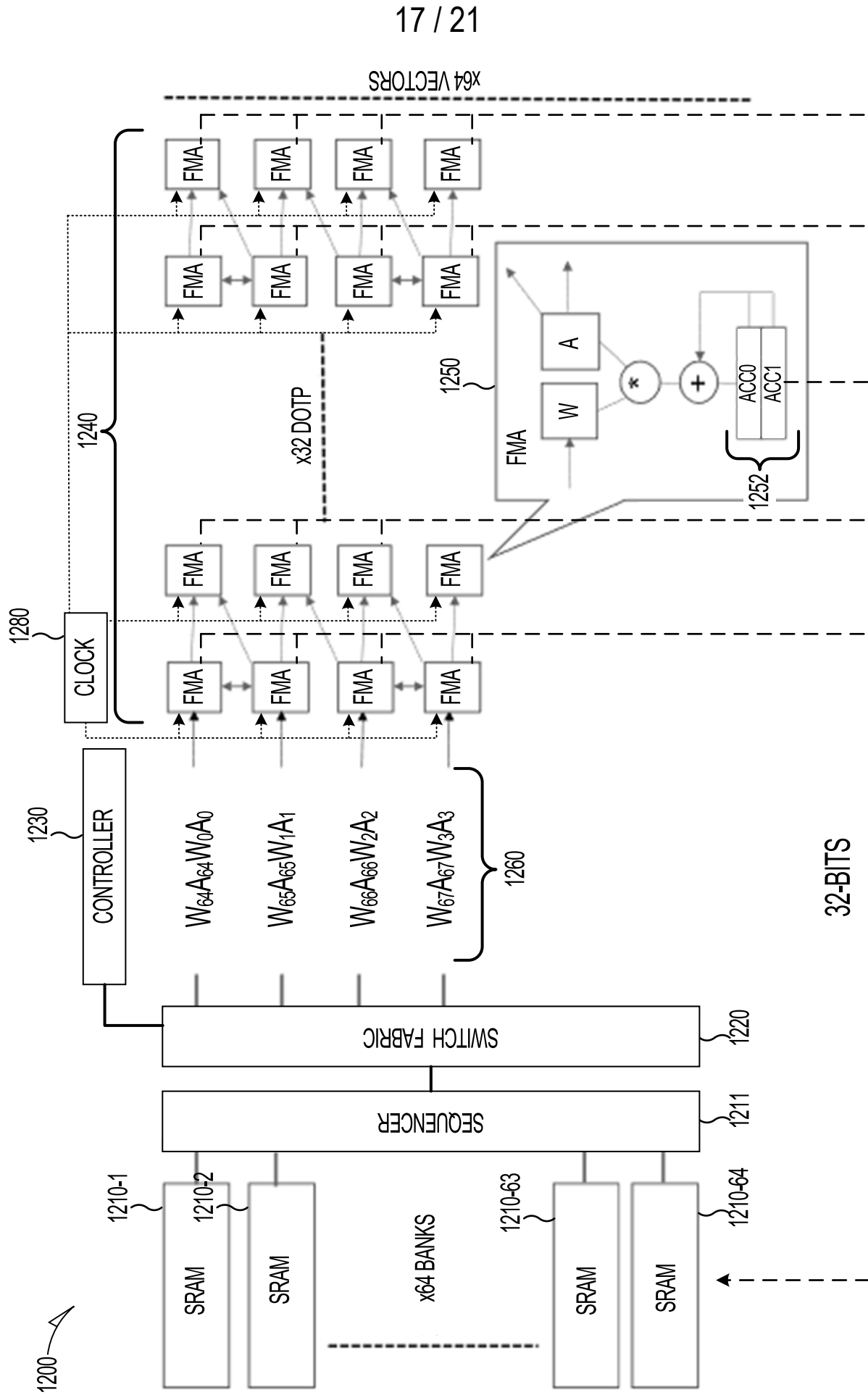


FIG. 12

1300

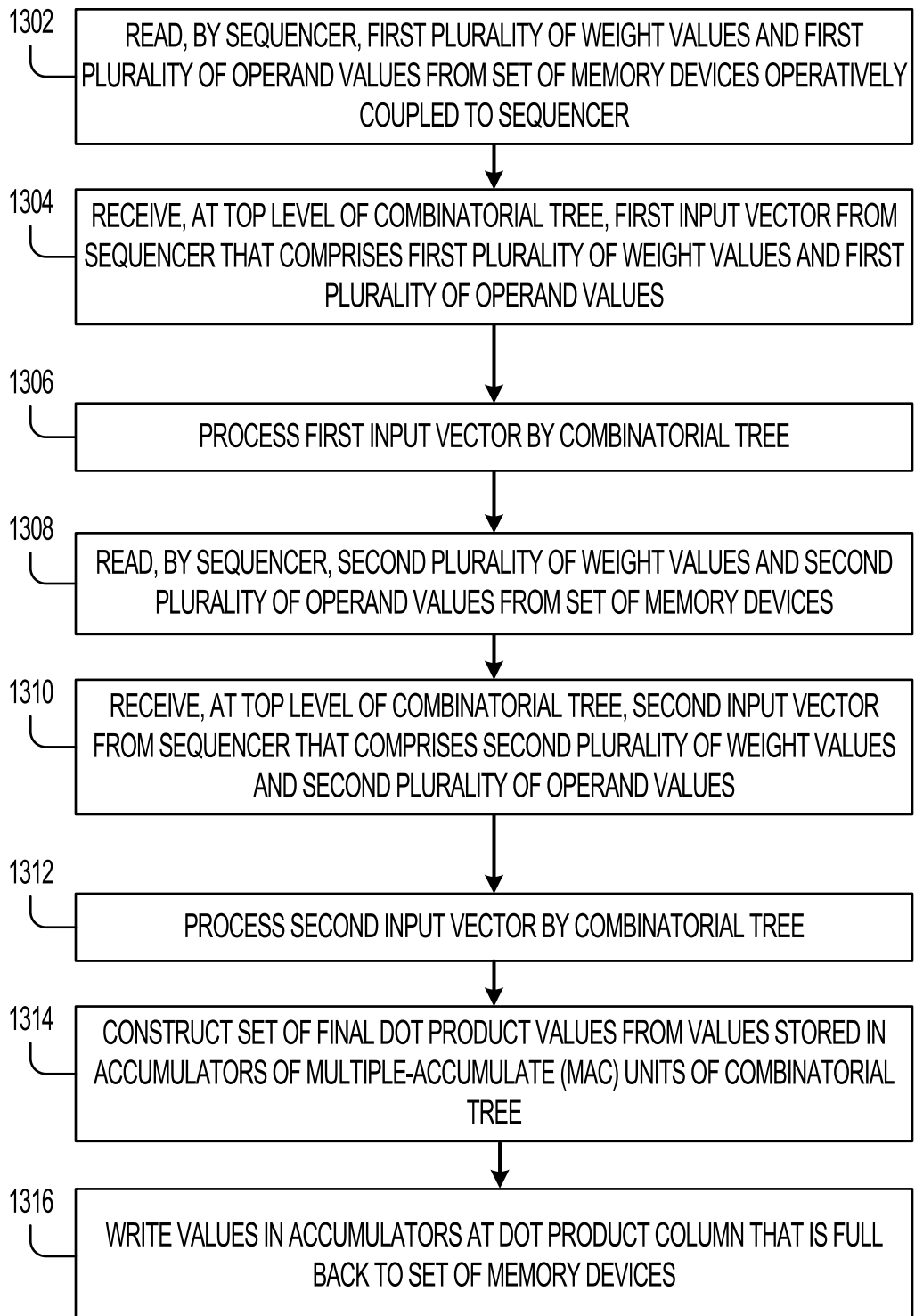


FIG. 13

1400

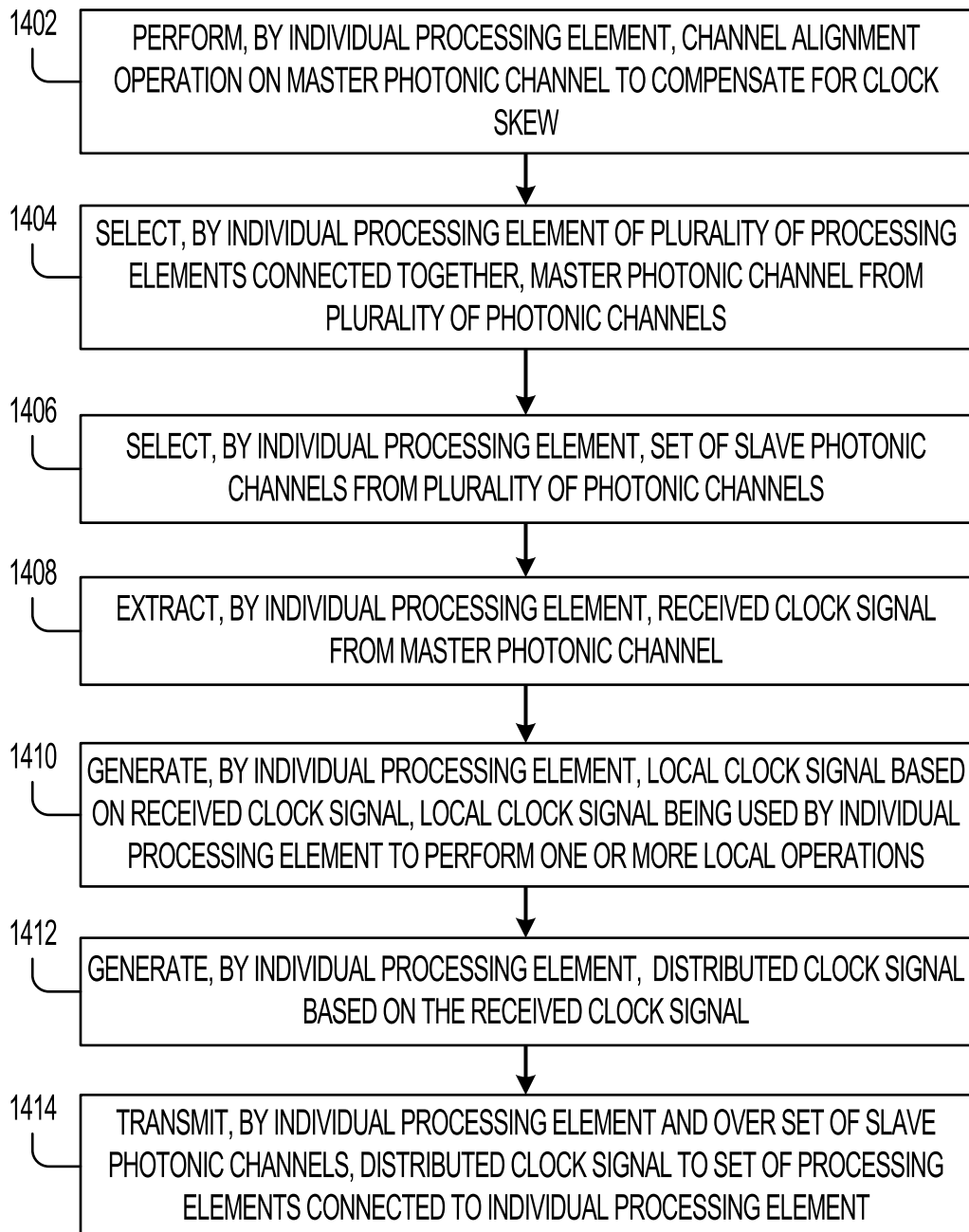


FIG. 14

1500

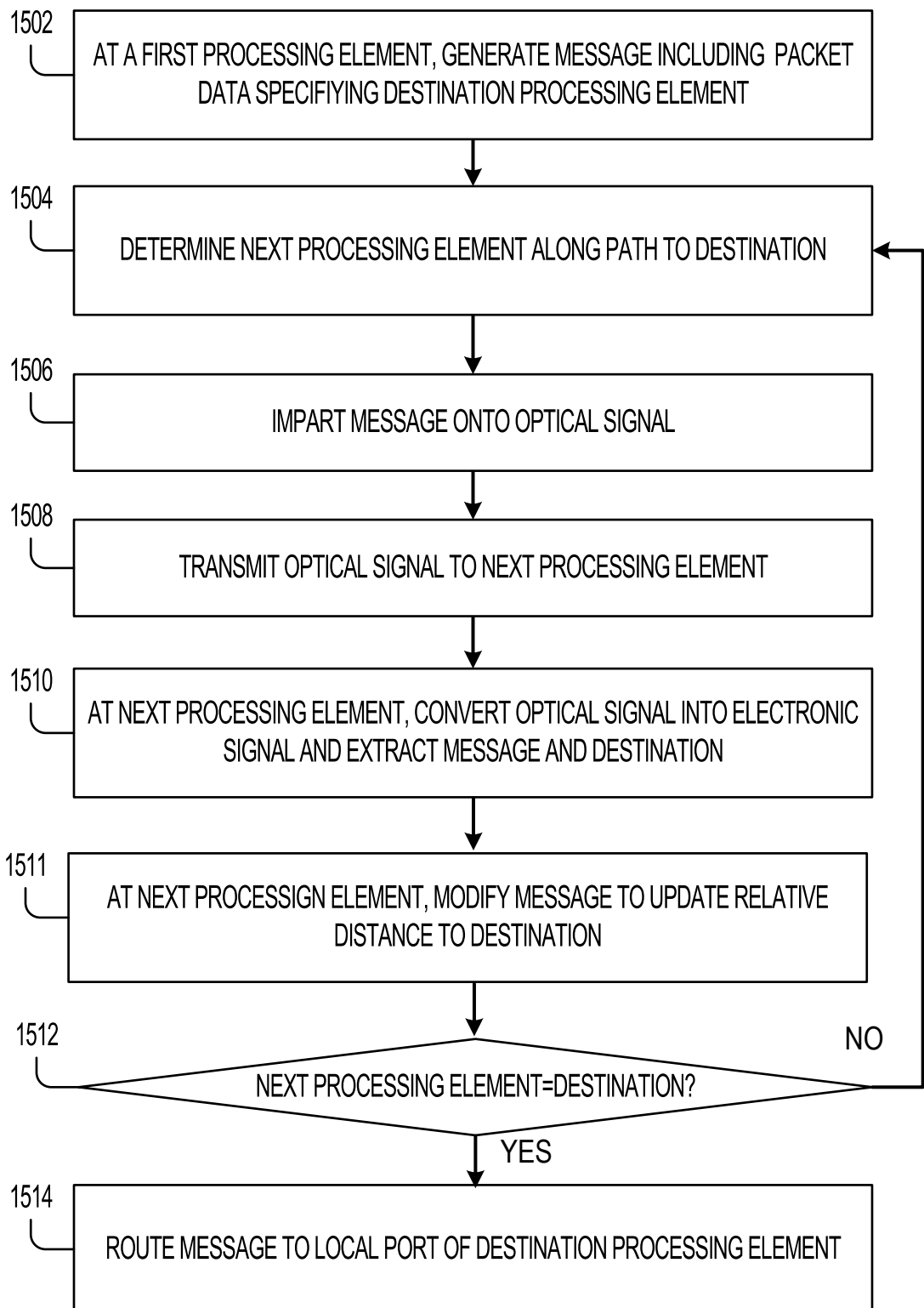


FIG. 15

1600

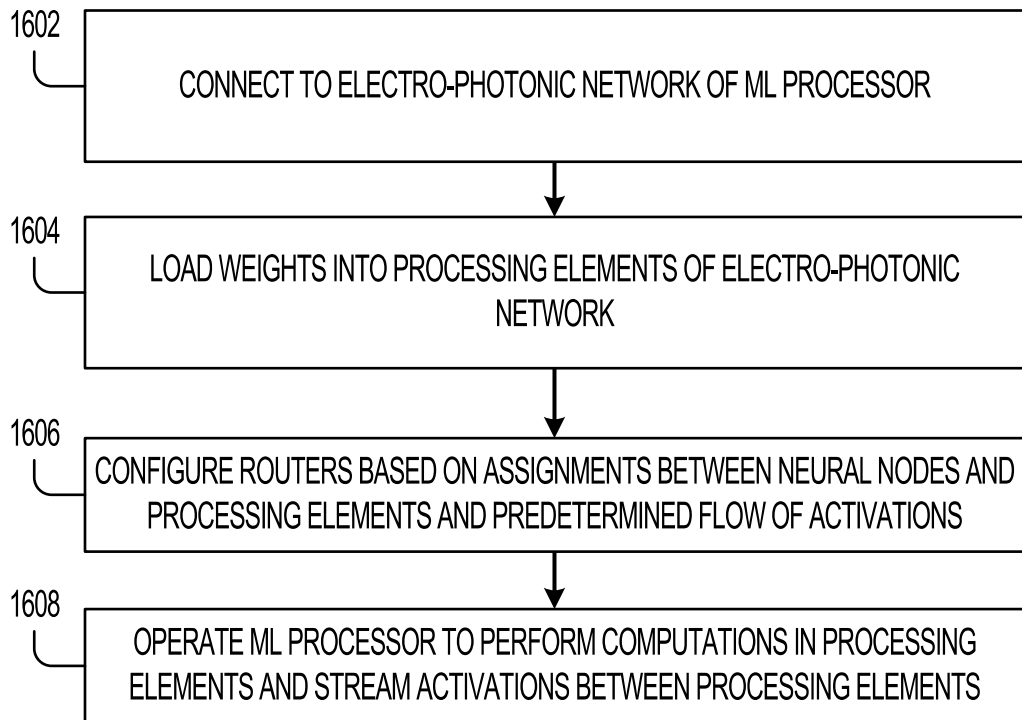


FIG. 16