

Fig.2

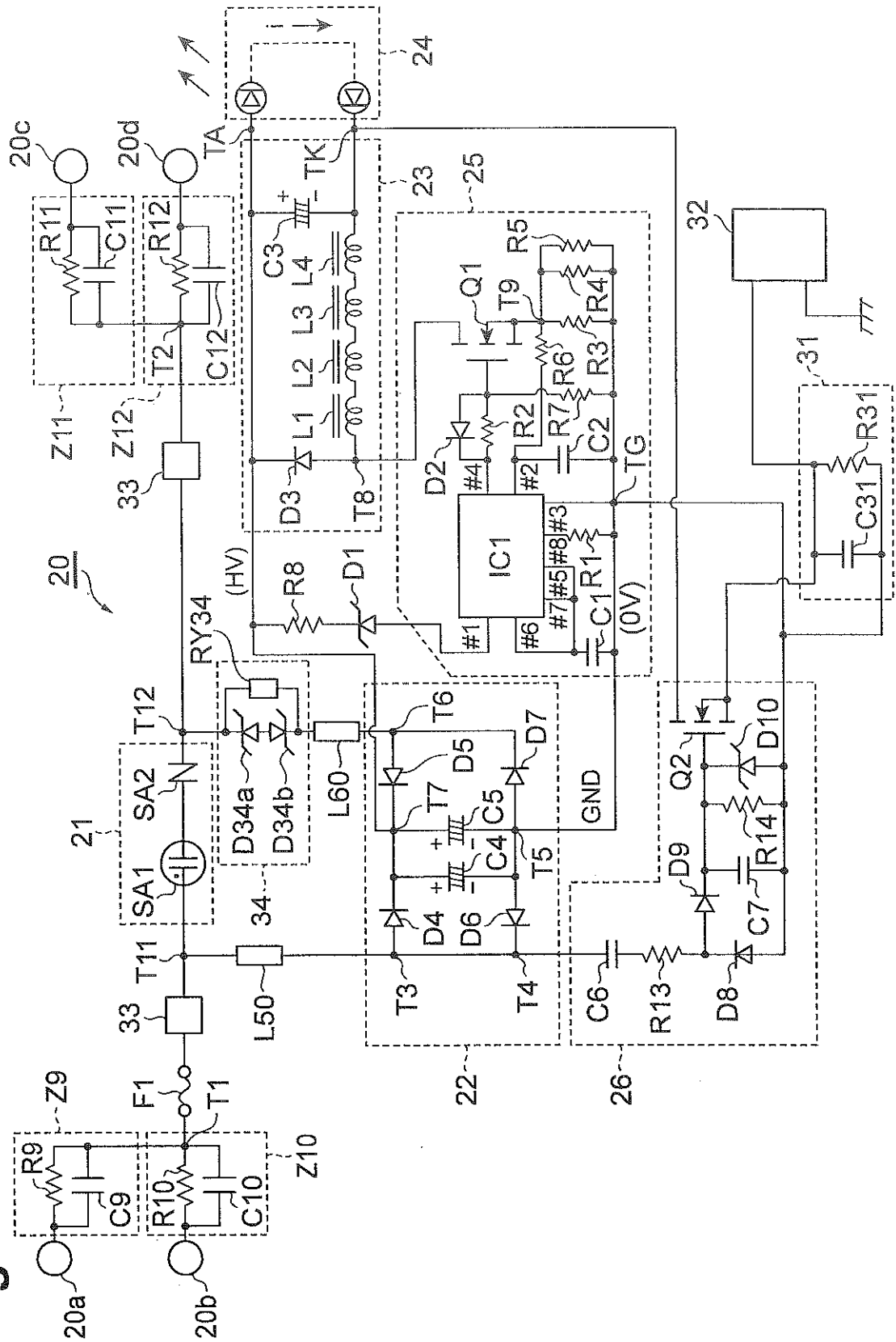


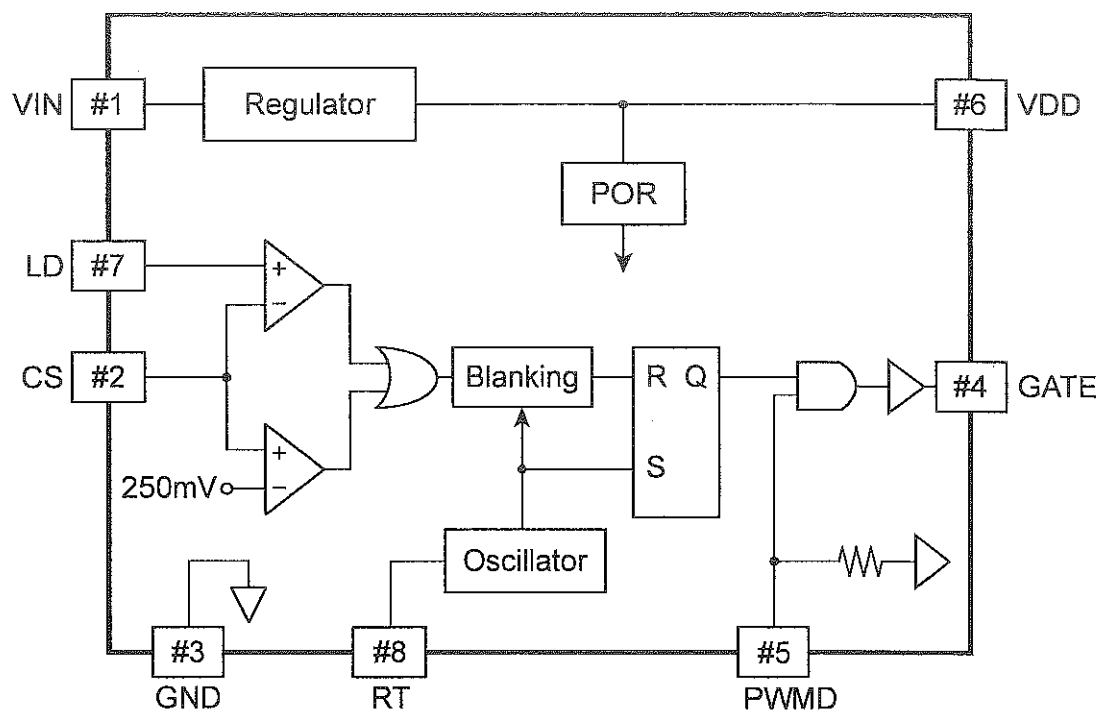
Fig.3

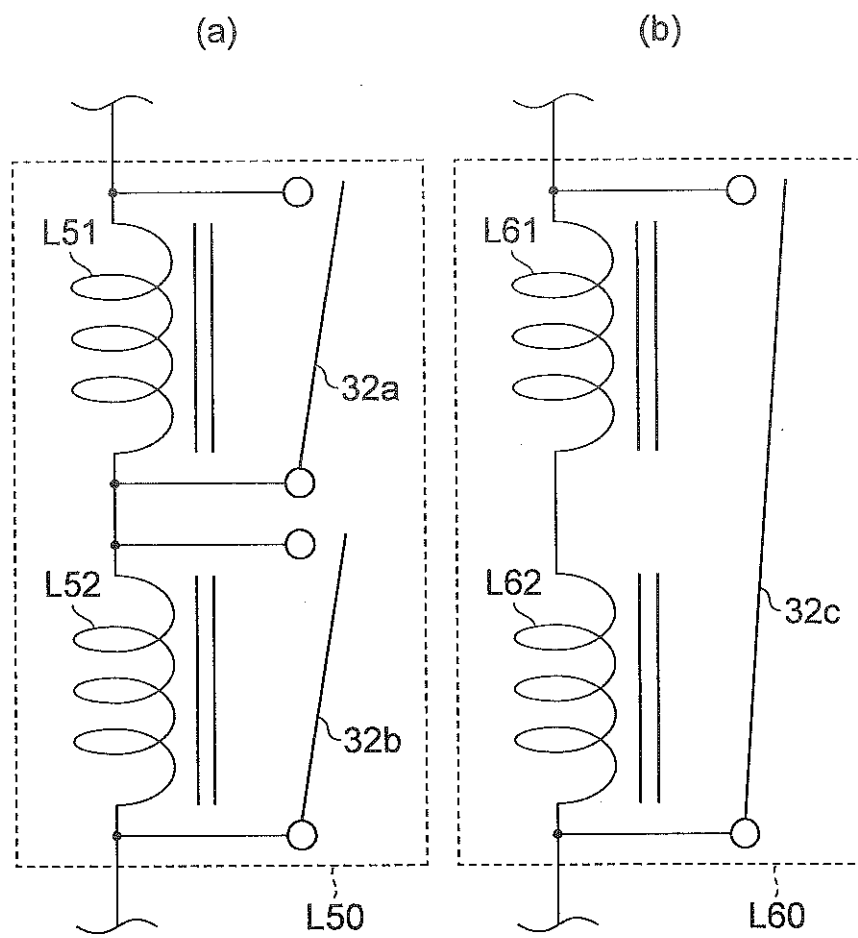
Fig.4

Fig.5

SWITCH	PATTERN 1	PATTERN 2	PATTERN 3
32a	OFF	ON	ON
32b	OFF	OFF	ON
32c	OFF	OFF	ON
L VALUE	TOTAL OF L51,L52, L61,L62	TOTAL OF L52,L61, L62	0 μ H

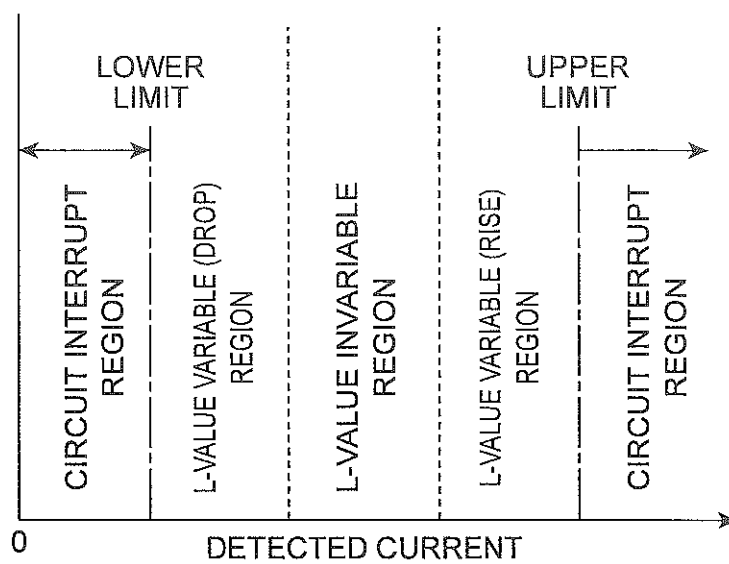
Fig.6

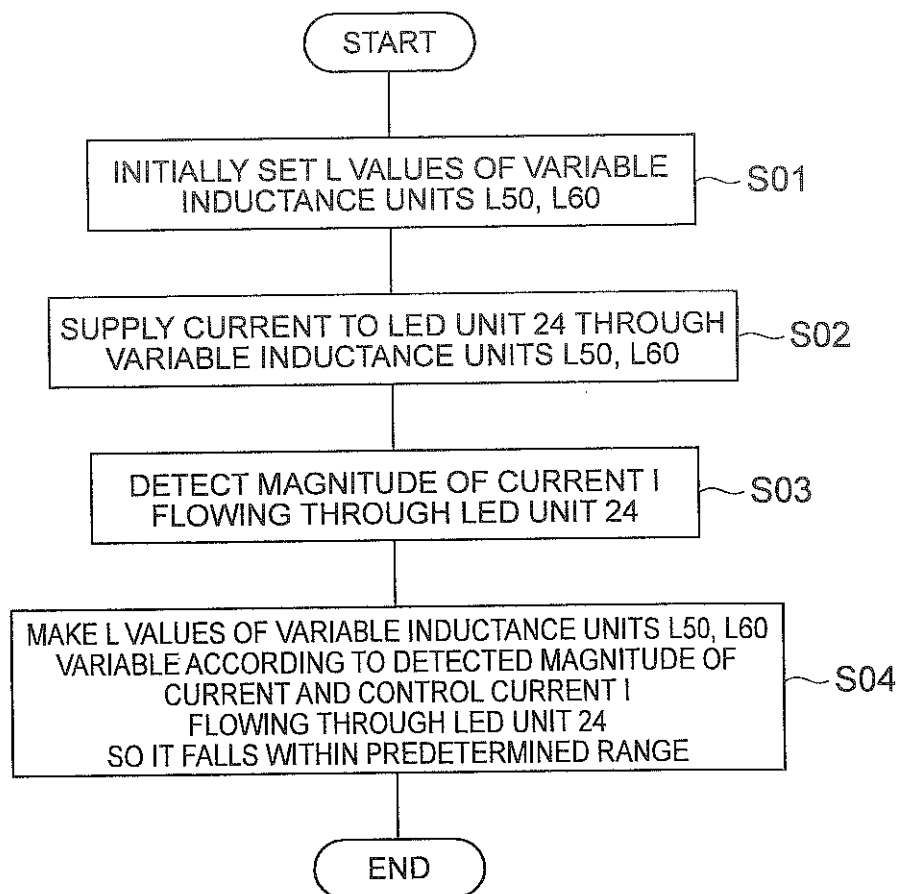
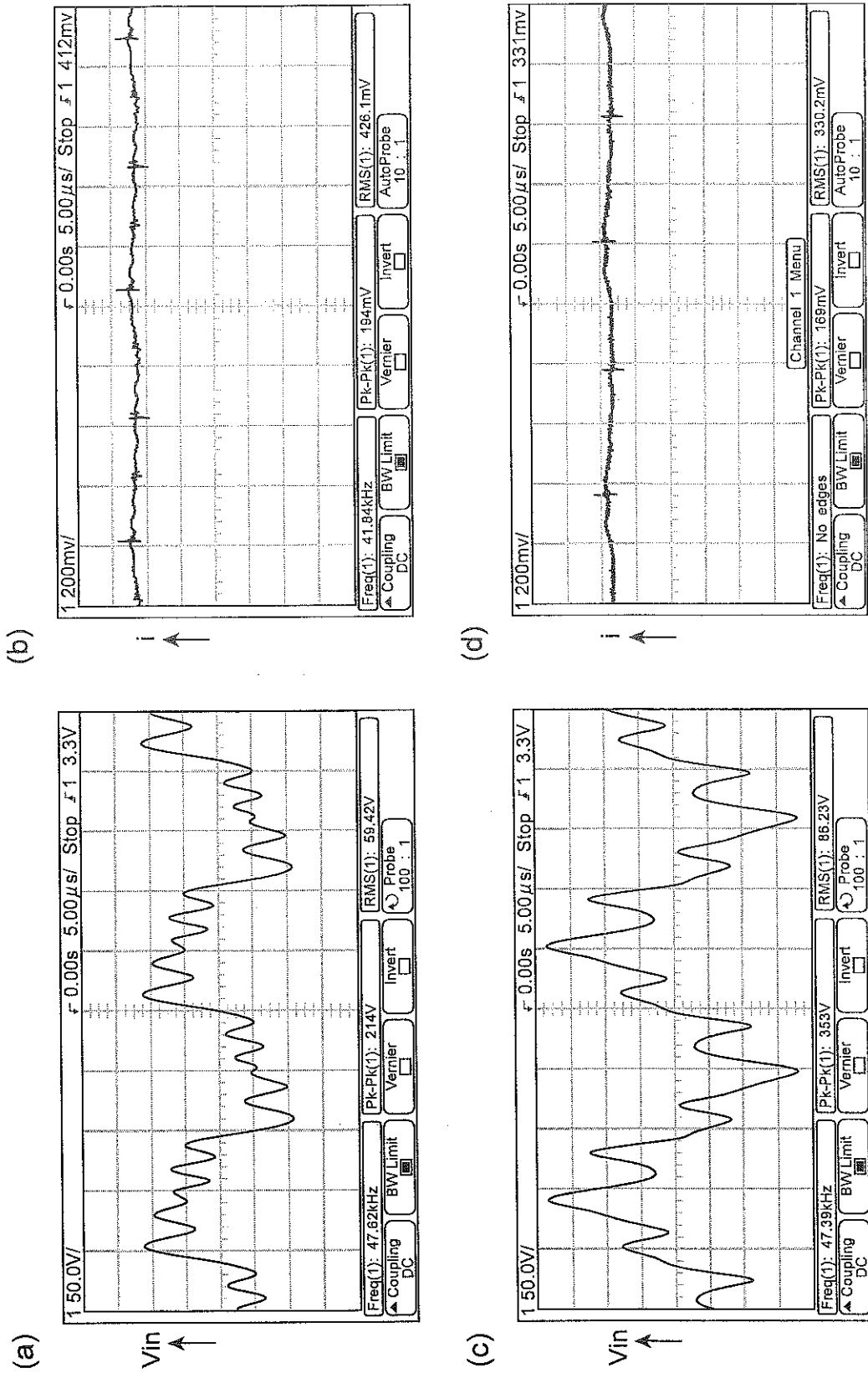
Fig.7

Fig.8



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Fig.9

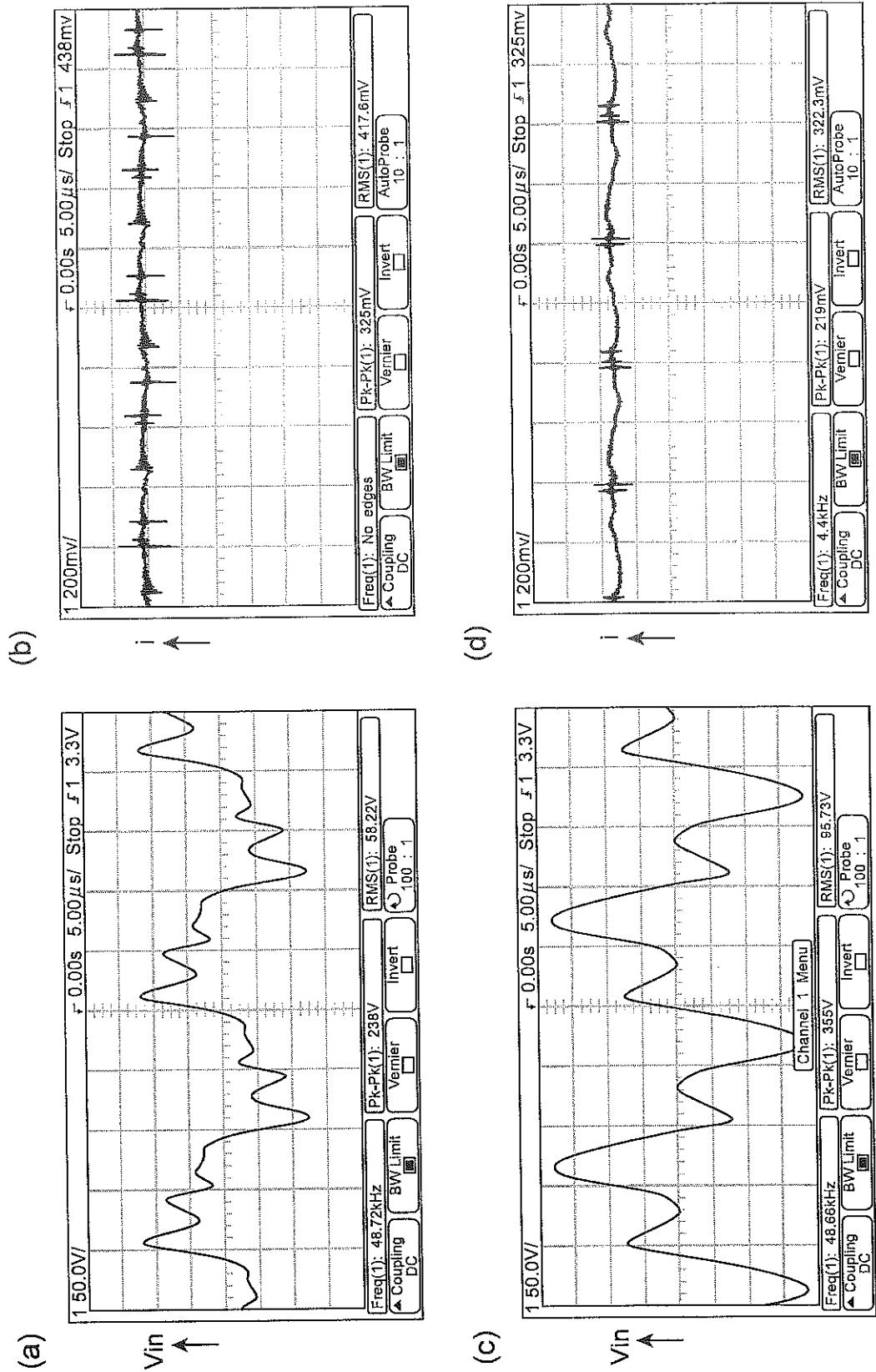


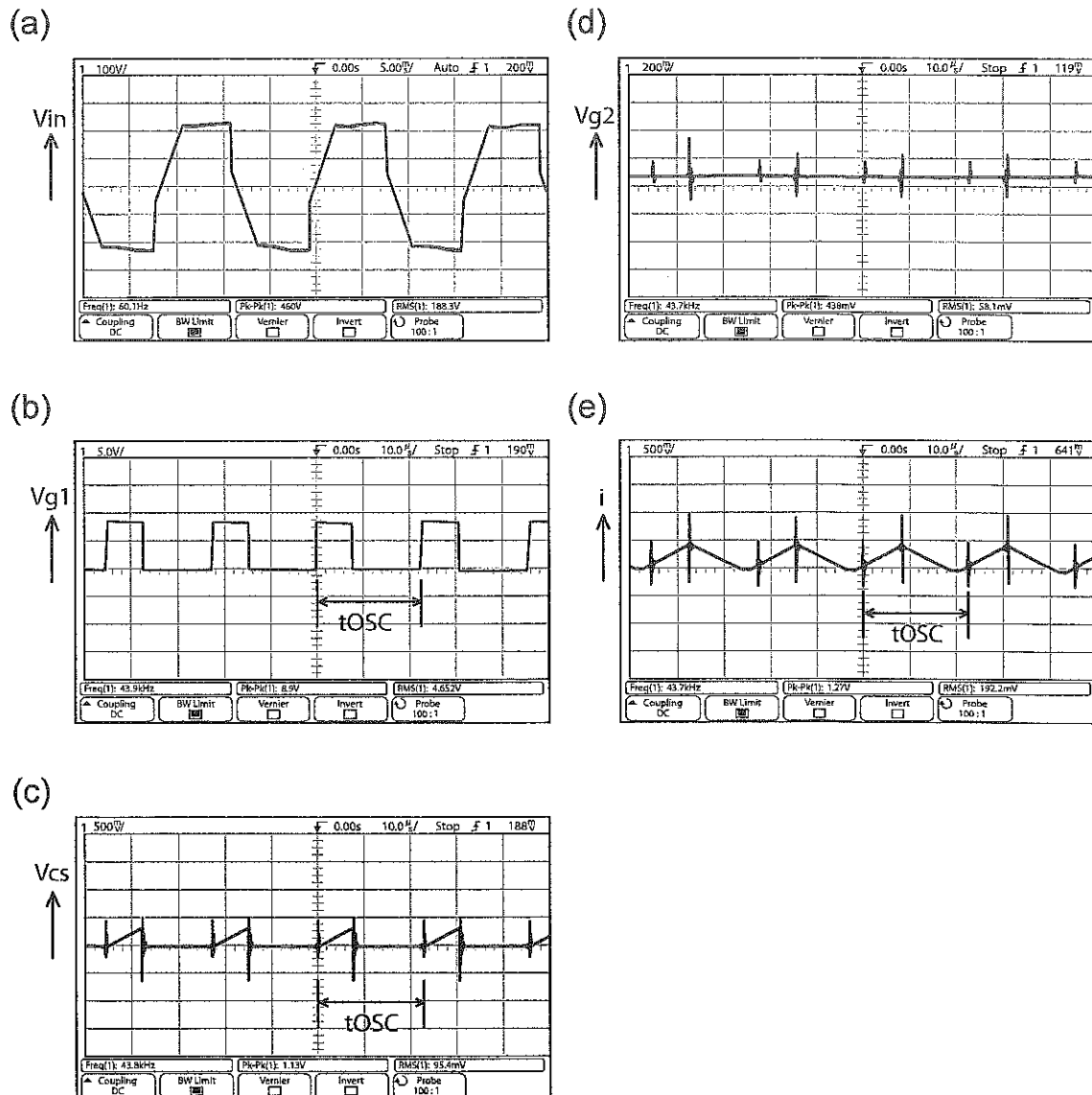
Fig.10

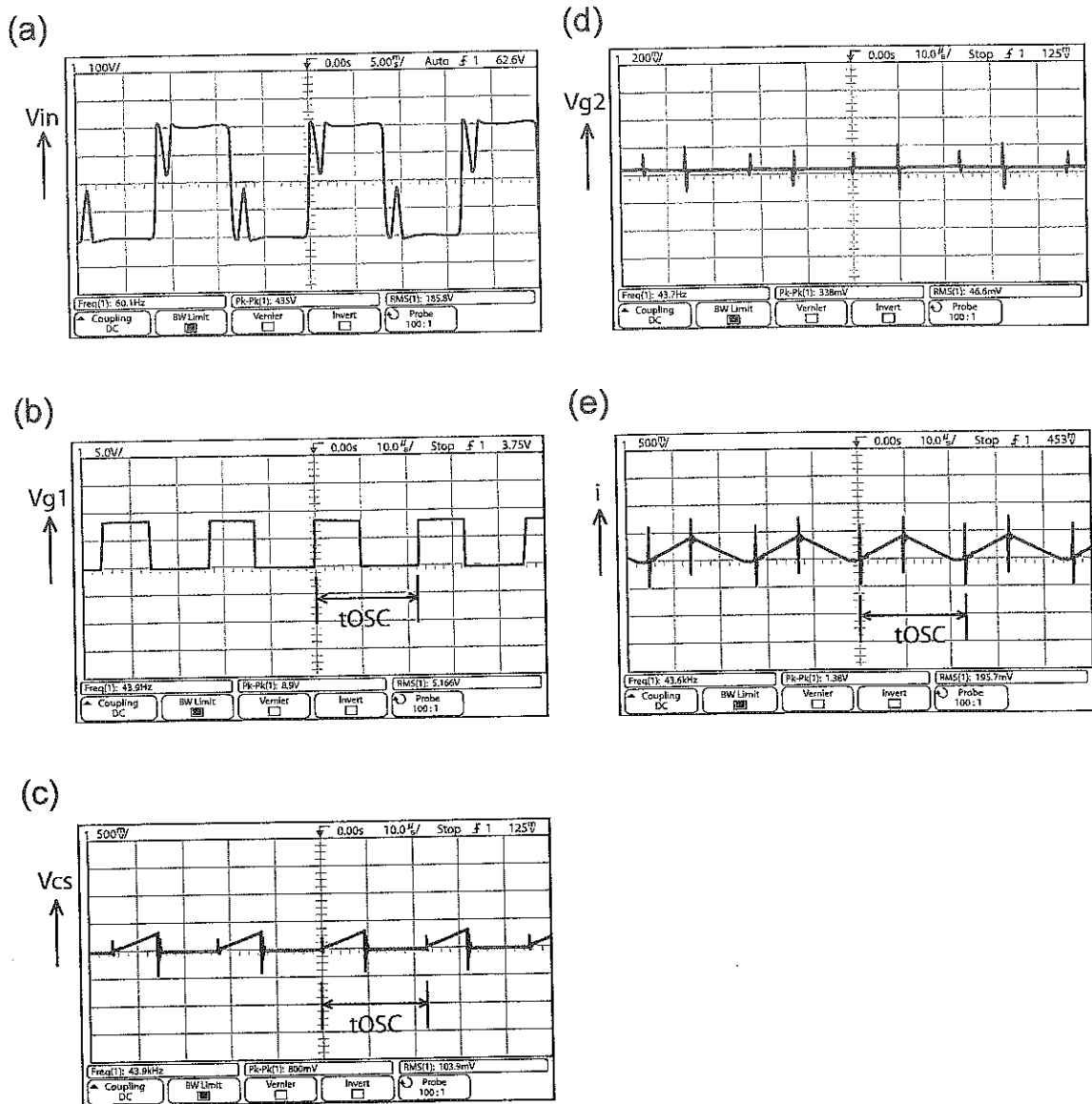
Fig.11

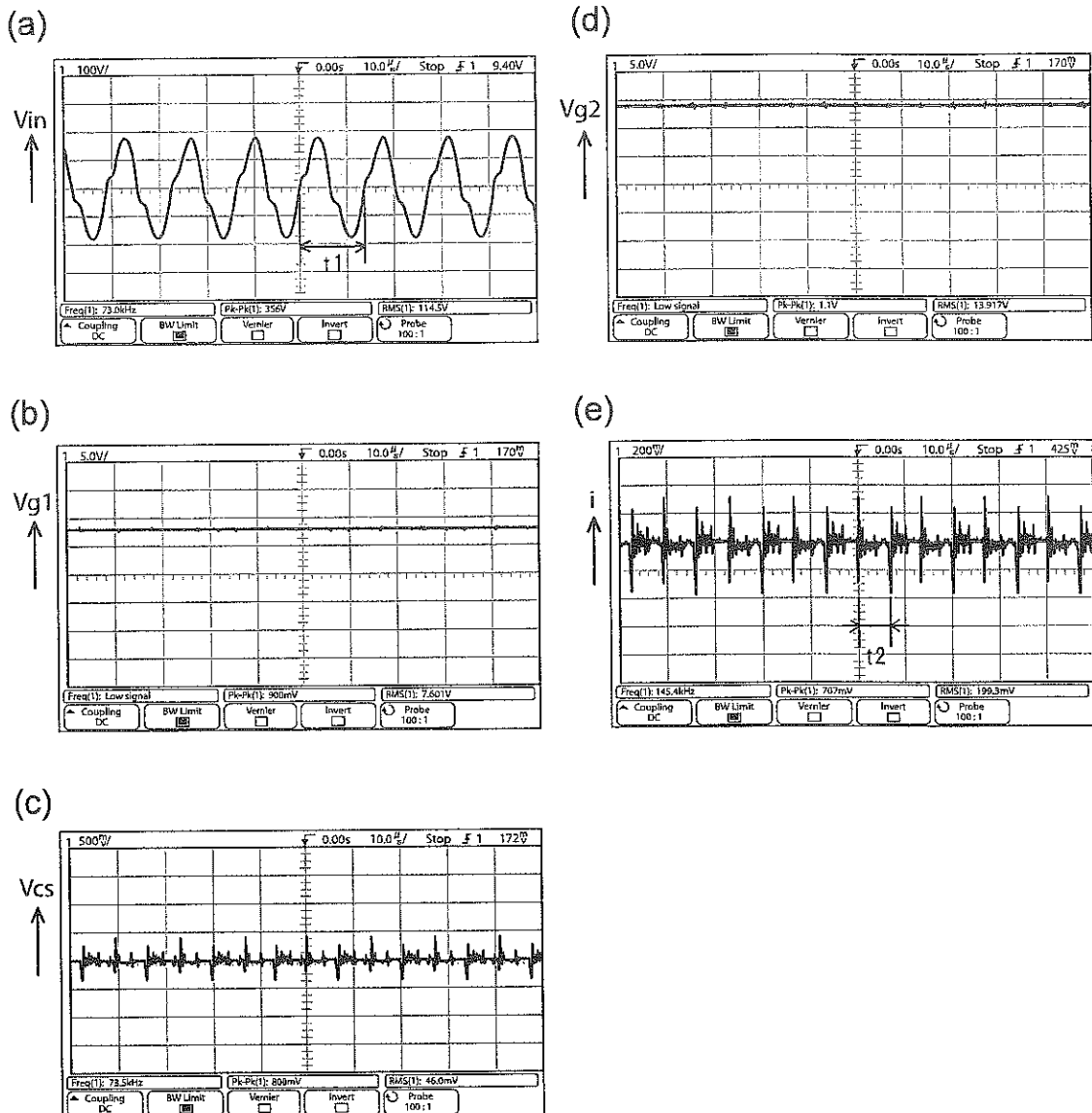
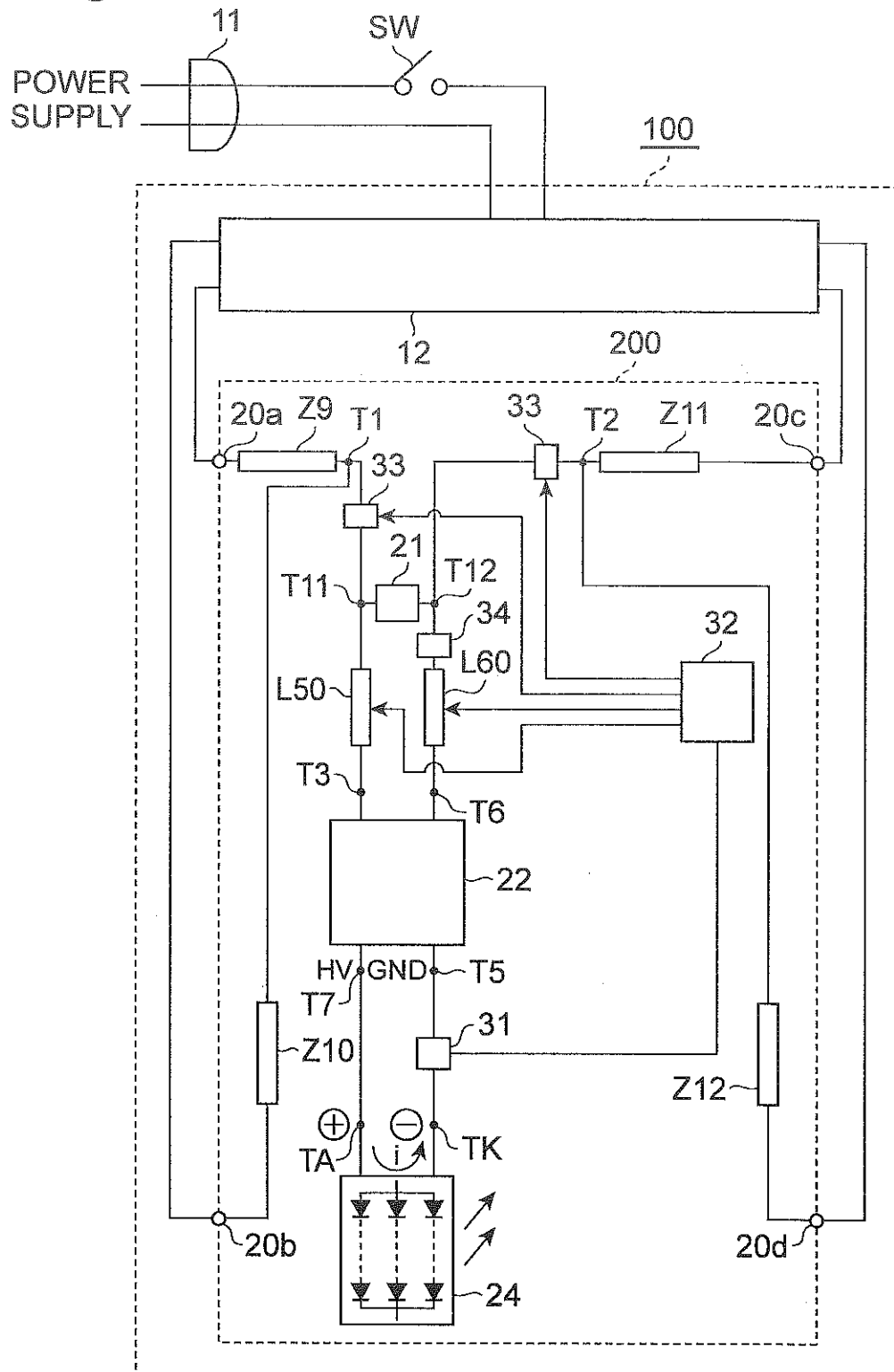
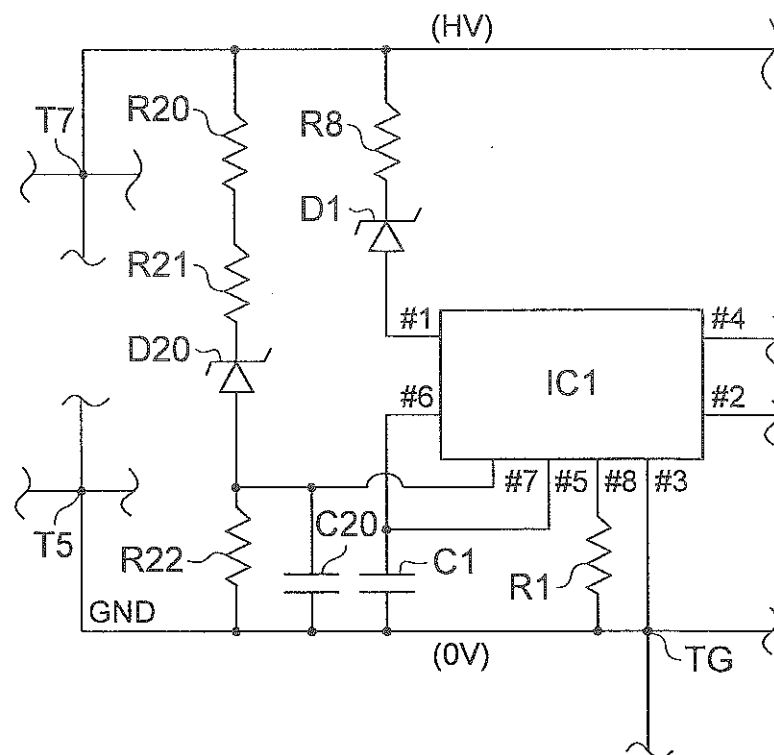
Fig.12

Fig.13

(a)



(b)

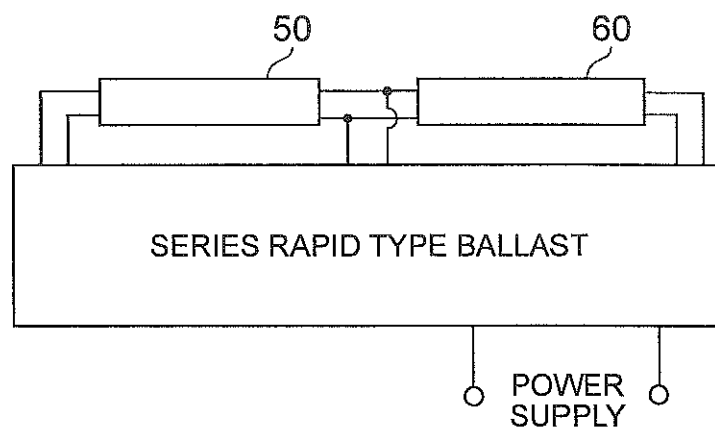
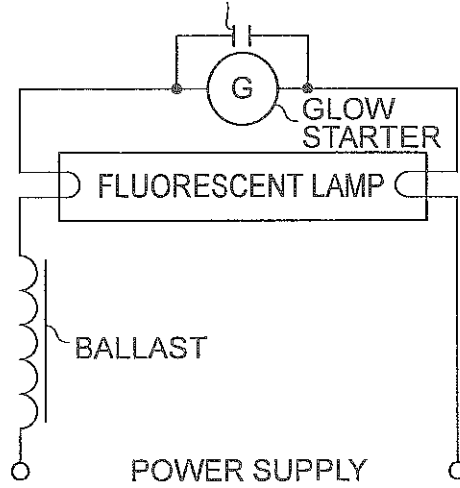


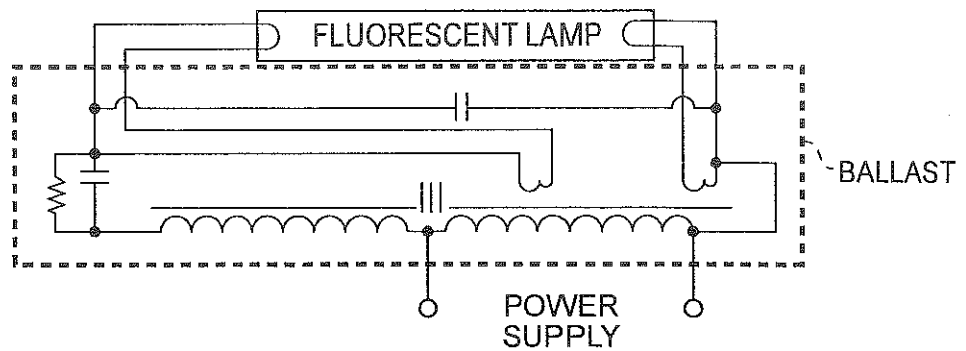
Fig.15

(a)

NOISE REDUCTION CAPACITOR



(b)



(c)

ELECTRONIC BALLAST

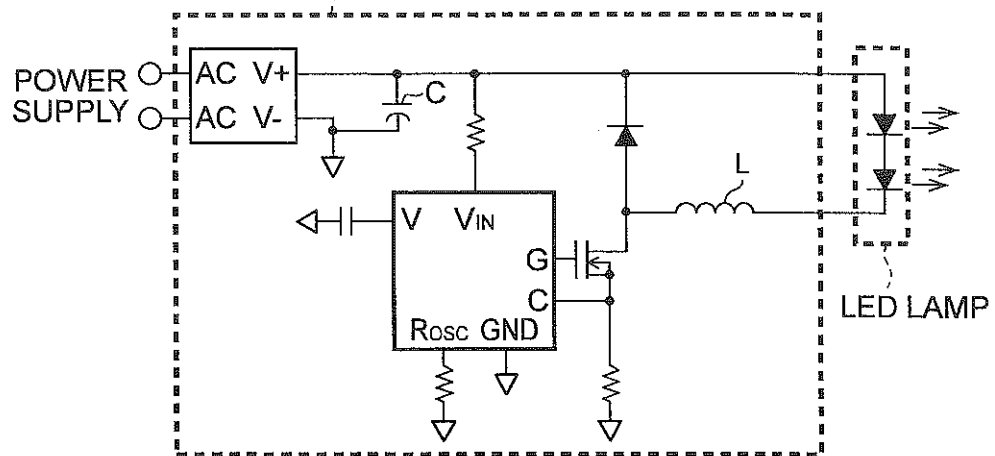


Fig.16

