

ABSTRACT OF THE DISCLOSURE

DEVICE SUBSTRATE AND SPLICED ELECTRONIC APPARATUS

A device substrate includes a carrier, a device array, first fan-out lines, and second fan-out lines. The carrier has a first side, a second side, a third side, and a fourth side. The first side is opposite to the second side. The third side is opposite to the fourth side. The device array is disposed on a first surface of the carrier. The device array includes sub-pixels. Each of the sub-pixels includes a switching element and an optoelectronic element electrically connected with the switching element. The first fan-out lines are extending from the first side to the first surface and electrically connected with the device array. The second fan-out lines are extending from the second side to the first surface and electrically connected with the device array. The first fan-out lines and the second fan-out lines are asymmetrically disposed on the first side and the second side, respectively.

Fig. 3A