

ELECTRONIC CIRCUIT BREAKER

The invention relates to an electronic circuit breaker (1) with a semiconductor switch (2), the drain connection (D) of said semiconductor switch and the source connection (S) of said semiconductor switch being connected between a voltage input (5) and a load output (6), and the gate connection (G) of said semiconductor switch being connected to a control device (9) which is supplied, at the input end, with a signal (S_i) which represents the load current (I_L , I_{ds}), wherein the control device (9) determines the drain-source voltage (V_{ds}) and, from this and from the detected load current (I_L , I_{ds}), generates a control signal (S_G) which is routed to the gate connection (G) of the semiconductor switch (2) and by means of which the power (P_{FET}) of the semiconductor switch (2) is adjusted to less than or equal to a maximum power value (P_{max}).