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(54) Title: INTEGRATED CIRCUIT WITH ADDRESS DRIVERS FOR FLUIDIC DIE

(57) Abstract: An integrated circuit for a fluidic die including an address bus to communicate a set of addresses, a first group of die configuration functions including a first address driver to drive a first portion of an address of the set of addresses on the address bus, a second group of die configuration functions including a second address driver to drive a second portion of the address of the set of addresses on the address bus, and an array of fluid actuating devices addressable by the set of addresses communicated via the address bus.

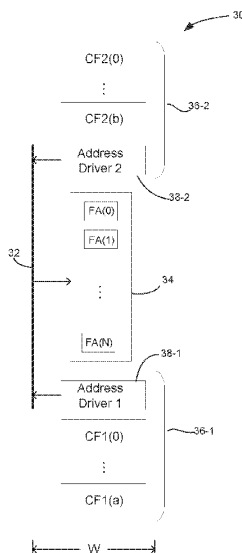


Fig. 1