

PHASE LOCKED LOOP CIRCUIT AND REPRODUCING APPARATUS.
ABSTRACT OF THE DISCLOSURE

A phase locked loop circuit for absorbing characteristic variations of phase comparator components beforehand in adjusting mode. With the variations thus adjusted and co-opted, any intrinsic error of the phase comparator is prevented from appearing as a phase difference upon transition from adjusting mode to normal operation mode. The scheme inhibits lock range deviations or capture range variations in normal operation mode, permitting a stable phase locked loop function.

FIGURE 2