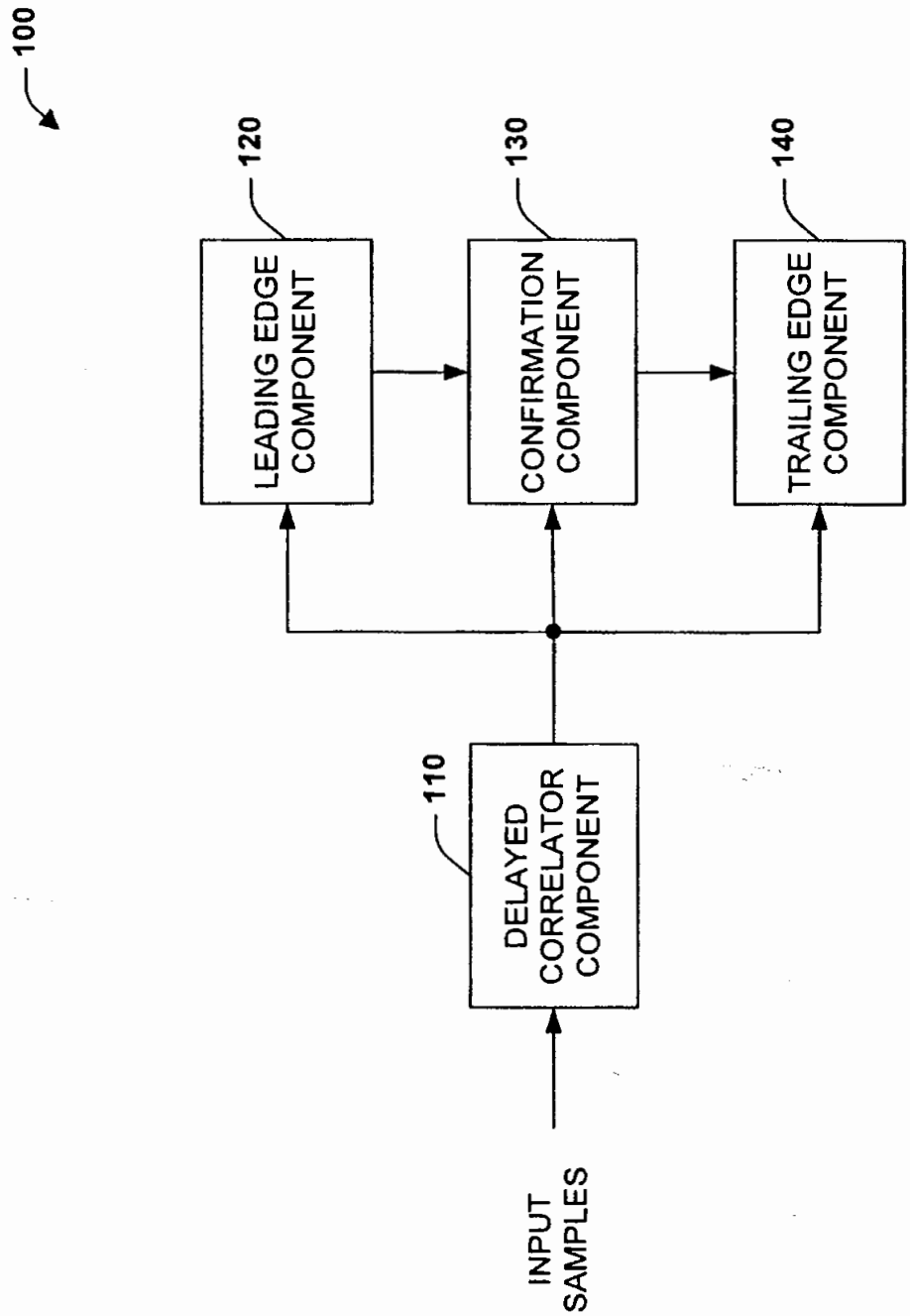

100
120
LEADING EDGE COMPONENT
130
CONFIRMATION COMPONENT
140
TRAILING EDGE COMPONENT
110
DELAYED CORRELATOR COMPONENT
INPUT SAMPLES

Fig. 1

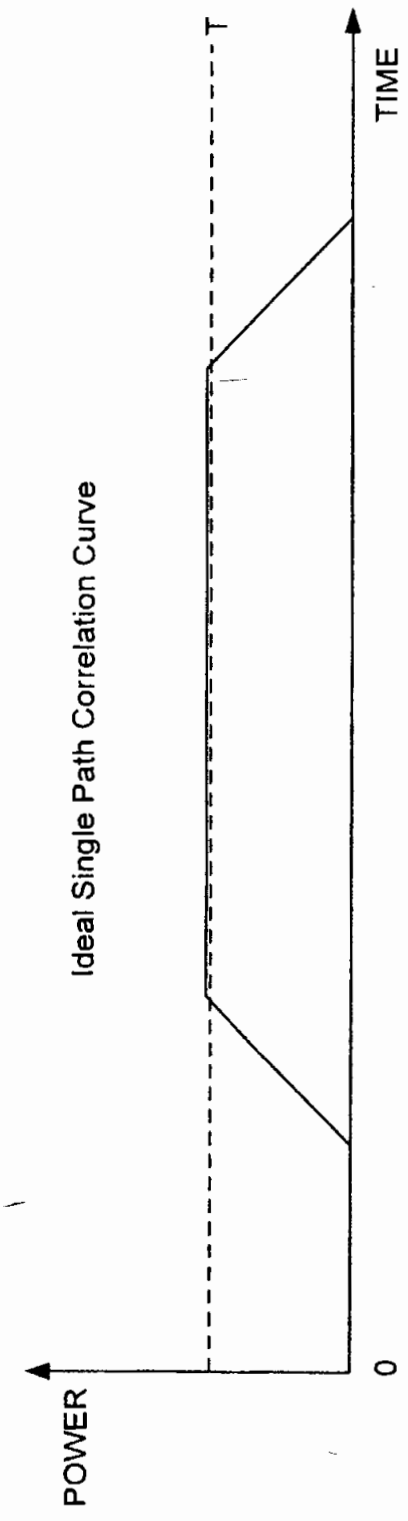

Ideal Single Path Correlation Curve
T
TIME
POWER
0

Fig. 2a

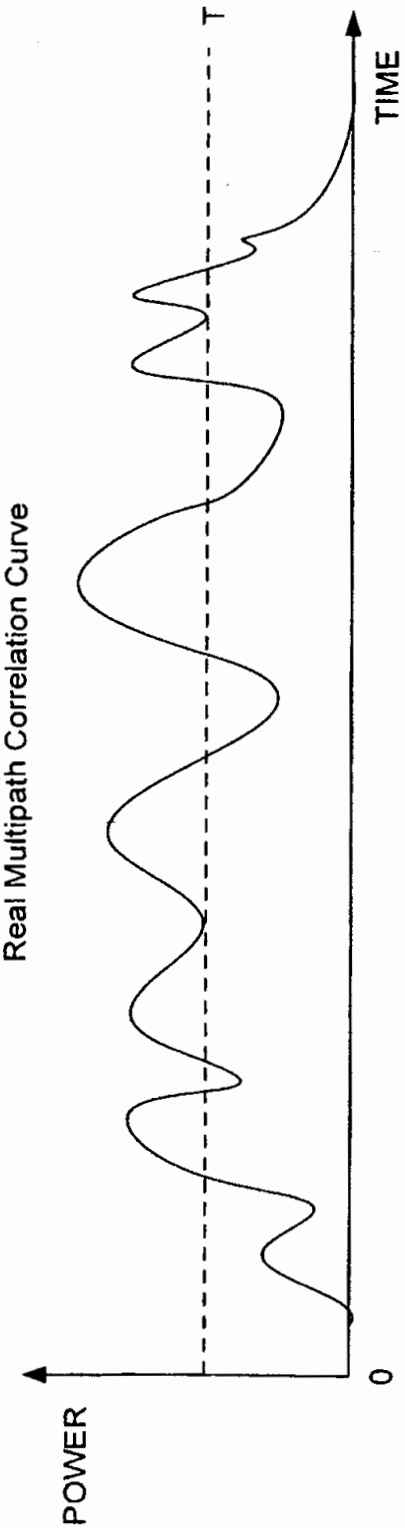

Real Multipath Correlation Curve
T
TIME
POWER
0

Fig. 2b

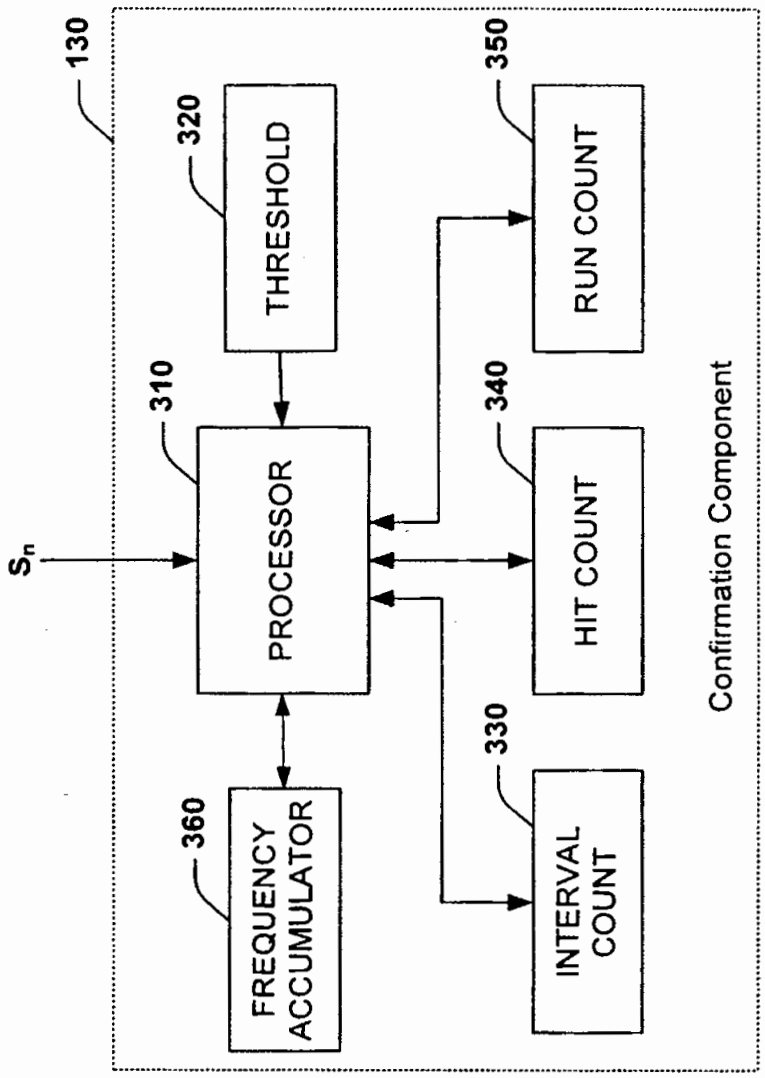

130
Confirmation Component
320
THRESHOLD
350
RUN COUNT
310
PROCESSOR
340
HIT COUNT
330
INTERVAL COUNT
360
FREQUENCY ACCUMULATOR
S_n

Fig. 3

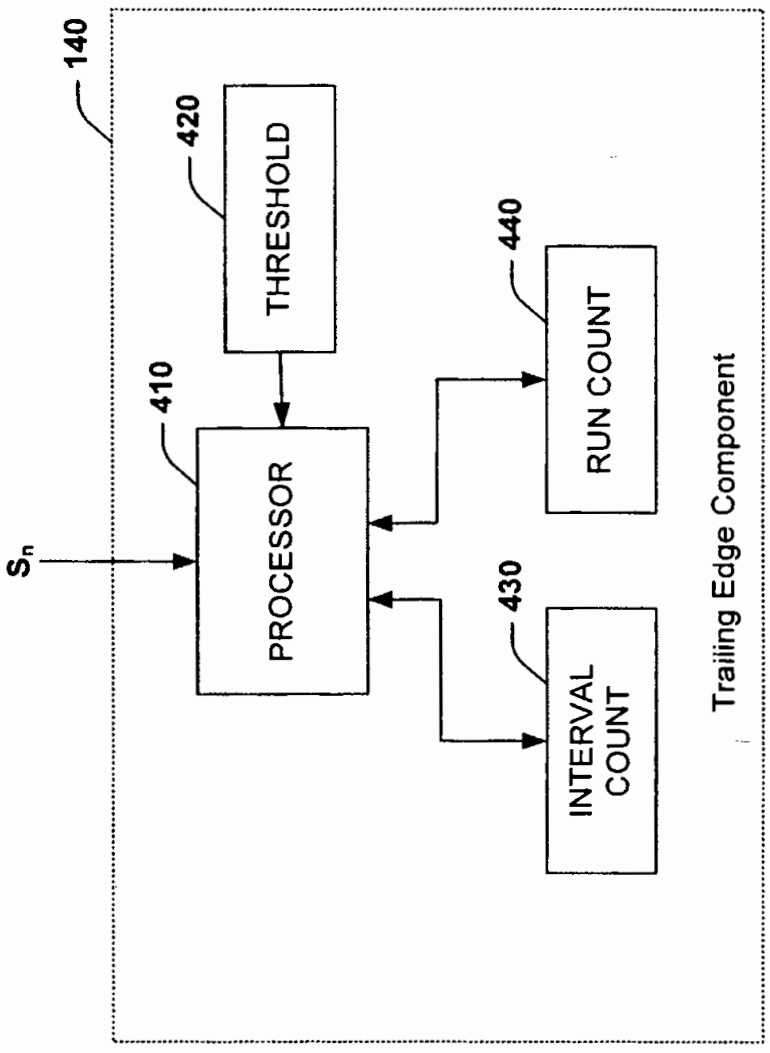

140
420
THRESHOLD
440
RUN COUNT
410
PROCESSOR
S_n
430
INTERVAL COUNT
Trailing Edge Component

Fig. 4

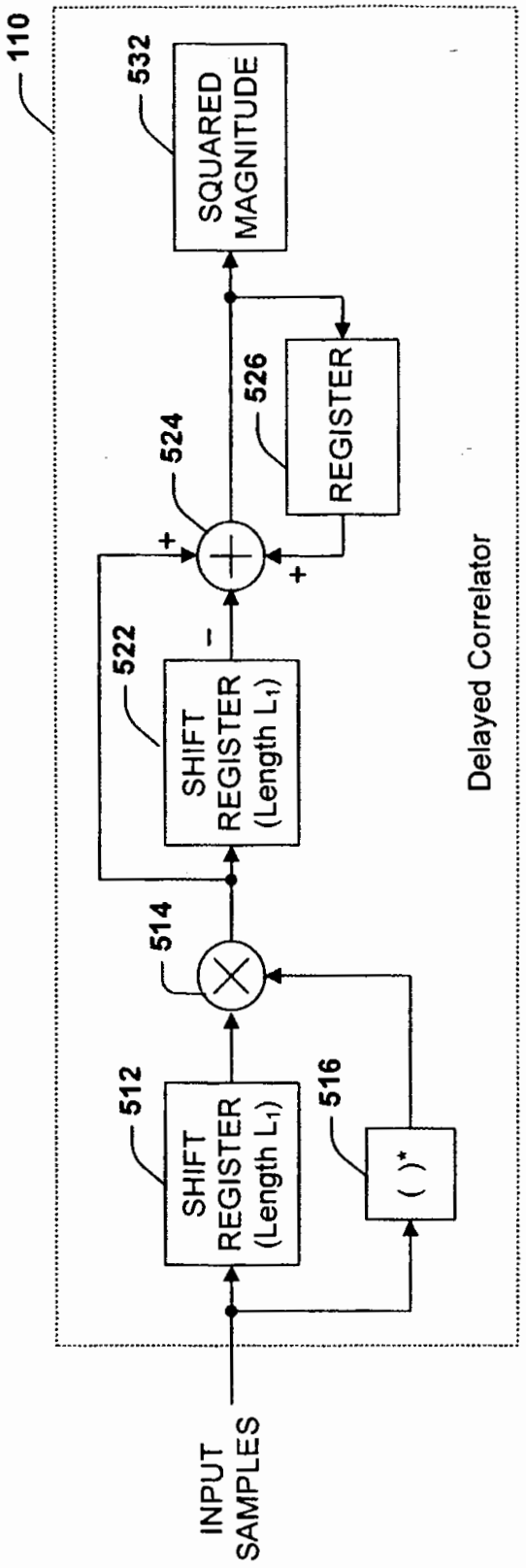

110
532
SQUARED MAGNITUDE
526
REGISTER
524
+
+
522
SHIFT REGISTER (Length L_1)
-
514
512
SHIFT REGISTER (Length L_1)
516
( )*
Delayed Correlator
INPUT SAMPLES

Fig. 5

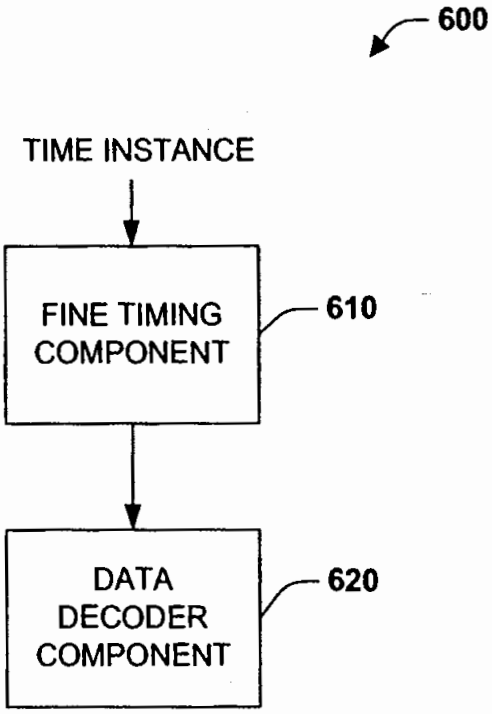

600
TIME INSTANCE
FINE TIMING COMPONENT
610
DATA DECODER COMPONENT
620

Fig. 6

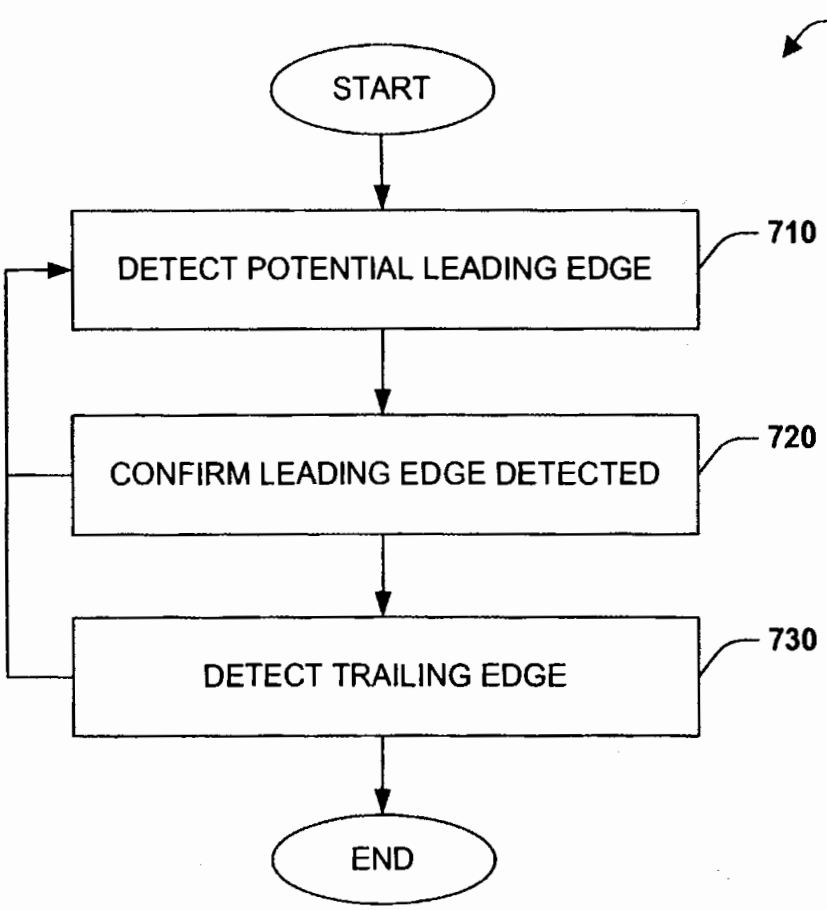

700
START
DETECT POTENTIAL LEADING EDGE
710
CONFIRM LEADING EDGE DETECTED
720
DETECT TRAILING EDGE
730
END

Fig. 7

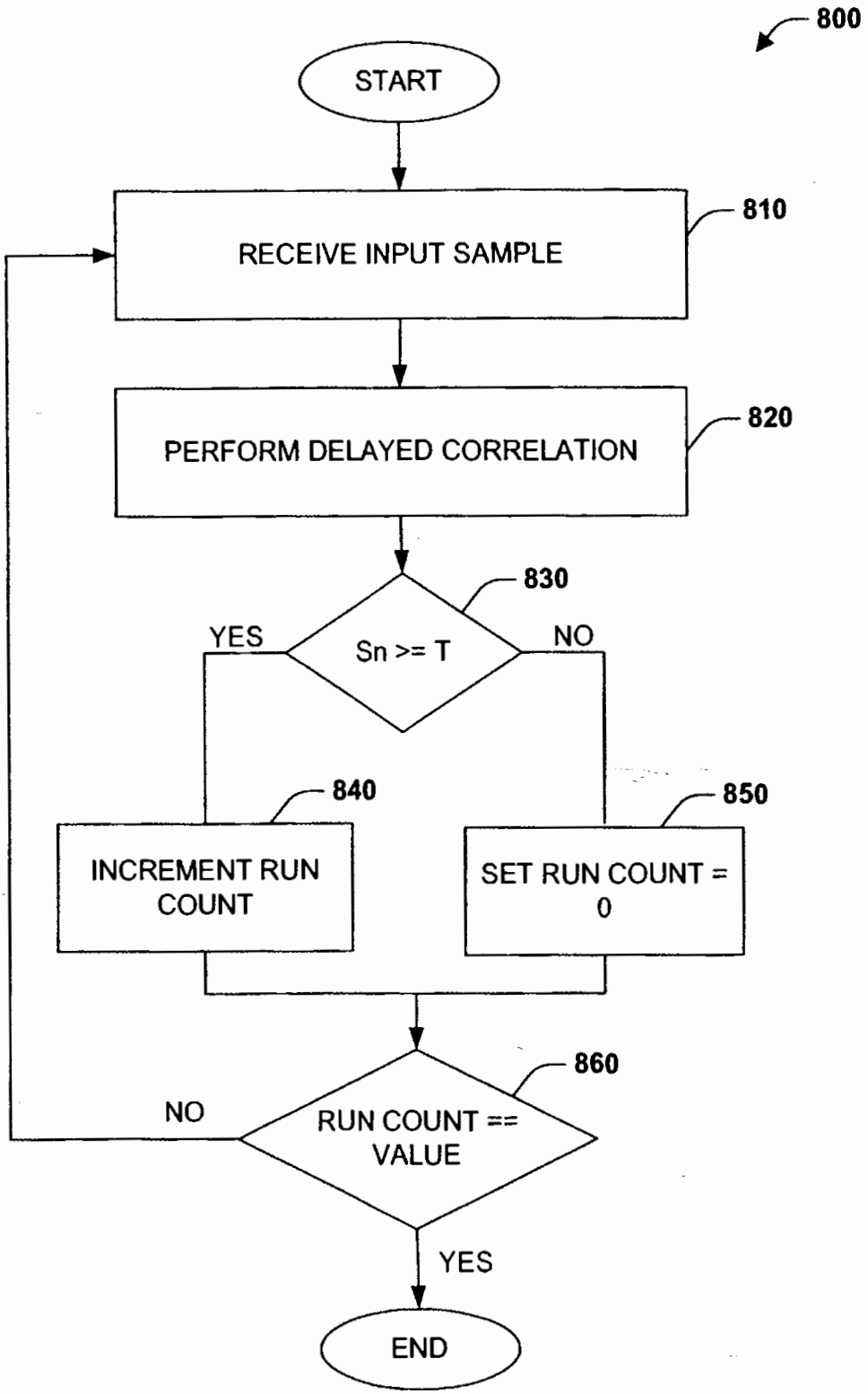

800
START
RECEIVE INPUT SAMPLE
810
PERFORM DELAYED CORRELATION
820
830
YES
Sn >= T
NO
840
INCREMENT RUN COUNT
850
SET RUN COUNT = 0
860
NO
RUN COUNT == VALUE
YES
END

Fig. 8

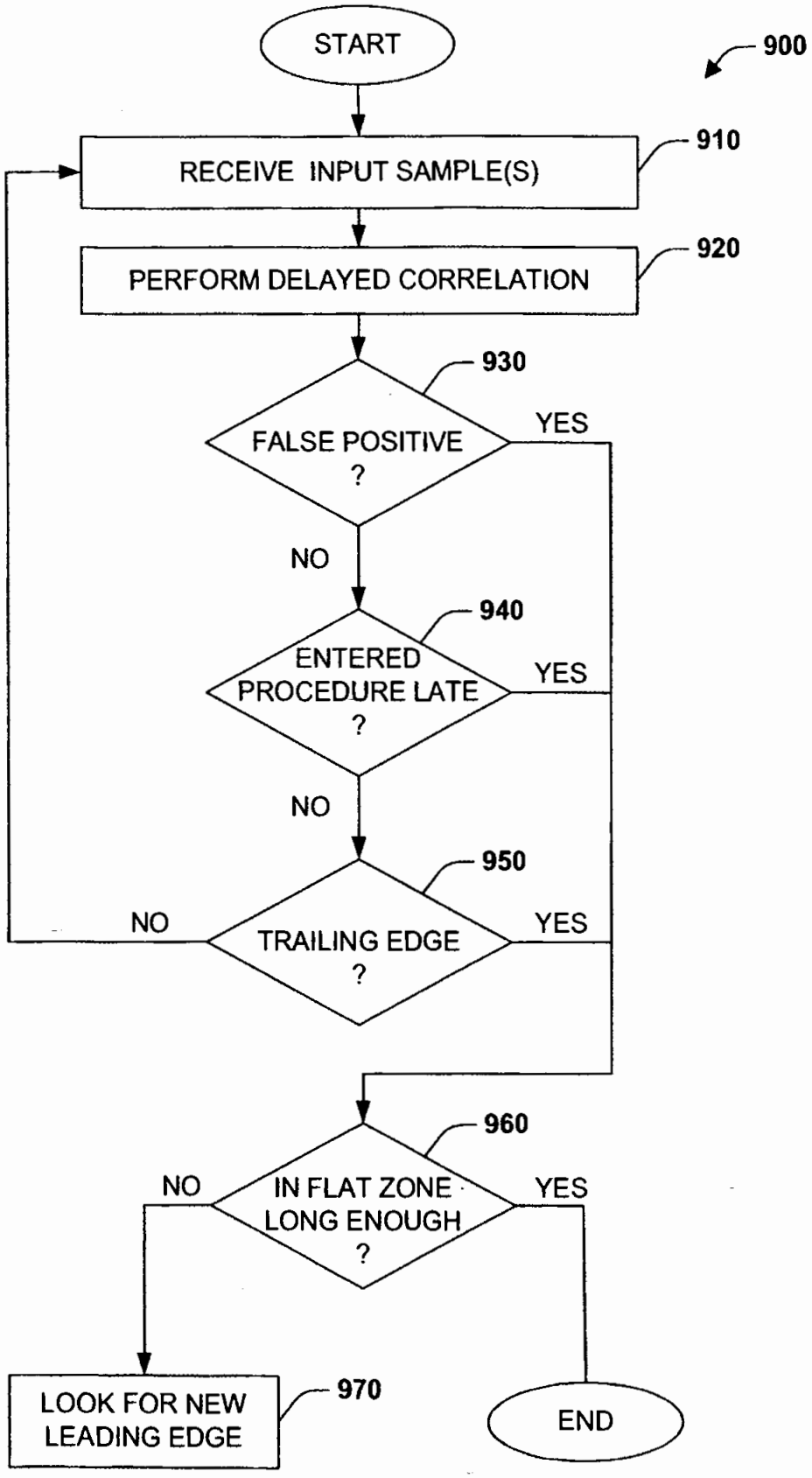

START
900
RECEIVE INPUT SAMPLE(S)
910
PERFORM DELAYED CORRELATION
920
930
FALSE POSITIVE ?
YES
NO
940
ENTERED PROCEDURE LATE ?
YES
NO
950
NO
TRAILING EDGE ?
YES
960
NO
IN FLAT ZONE LONG ENOUGH ?
YES
LOOK FOR NEW LEADING EDGE
970
END

Fig. 9

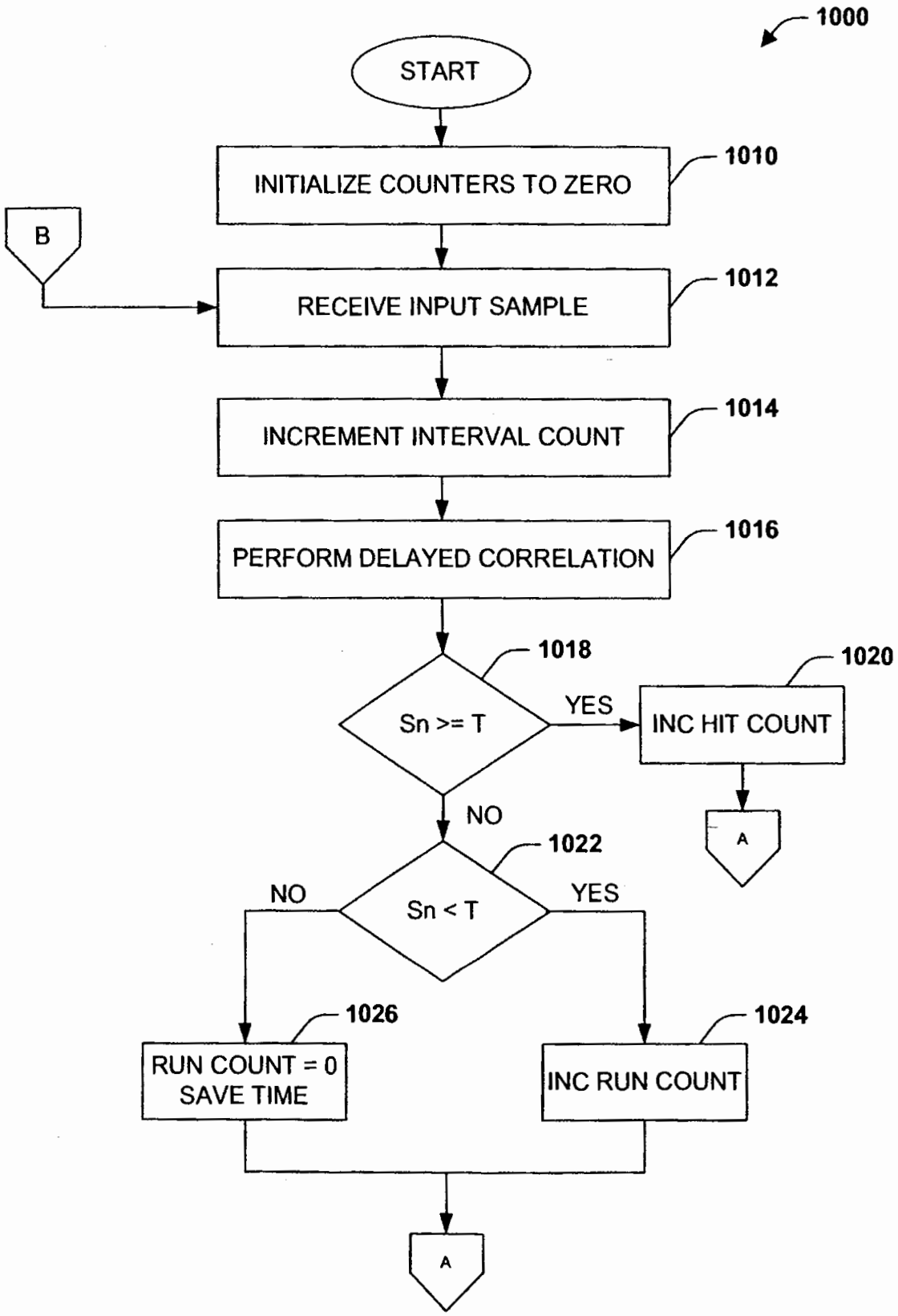

1000
START
INITIALIZE COUNTERS TO ZERO
1010
B
RECEIVE INPUT SAMPLE
1012
INCREMENT INTERVAL COUNT
1014
PERFORM DELAYED CORRELATION
1016
1018
Sn >= T
YES
1020
INC HIT COUNT
A
NO
1022
NO
Sn < T
YES
1026
RUN COUNT = 0
SAVE TIME
1024
INC RUN COUNT
A

Fig. 10a

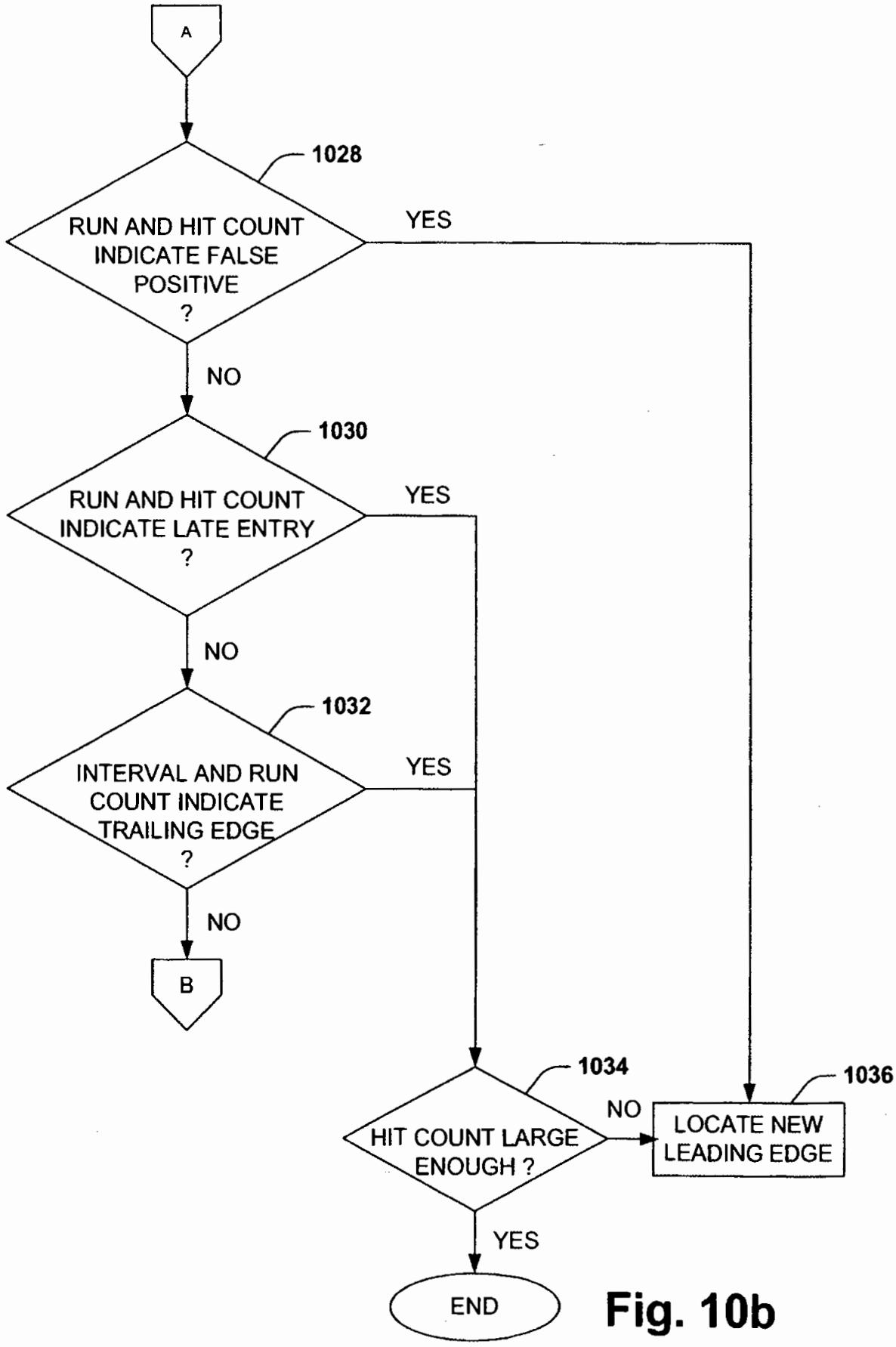

A
1028
RUN AND HIT COUNT INDICATE FALSE POSITIVE ?
YES
NO
1030
RUN AND HIT COUNT INDICATE LATE ENTRY ?
YES
NO
1032
INTERVAL AND RUN COUNT INDICATE TRAILING EDGE ?
YES
NO
B
1034
HIT COUNT LARGE ENOUGH ?
NO
1036
LOCATE NEW LEADING EDGE
YES
END

Fig. 10b

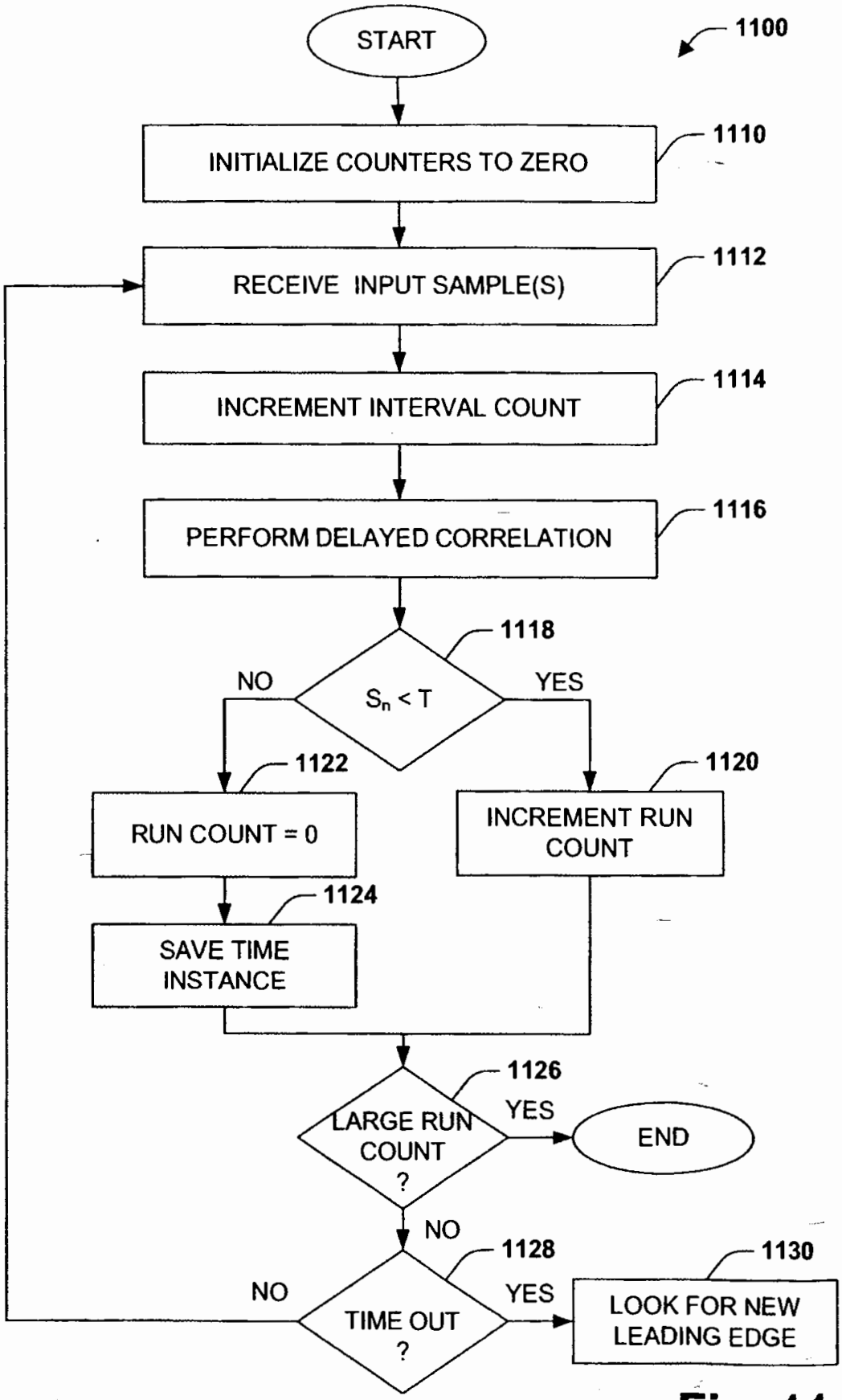

START
1100
INITIALIZE COUNTERS TO ZERO
1110
RECEIVE INPUT SAMPLE(S)
1112
INCREMENT INTERVAL COUNT
1114
PERFORM DELAYED CORRELATION
1116
1118
NO
$S_n < T$
YES
1122
RUN COUNT = 0
1120
INCREMENT RUN COUNT
1124
SAVE TIME INSTANCE
1126
LARGE RUN COUNT ?
YES
END
NO
1128
NO
TIME OUT ?
YES
1130
LOOK FOR NEW LEADING EDGE

Fig. 11

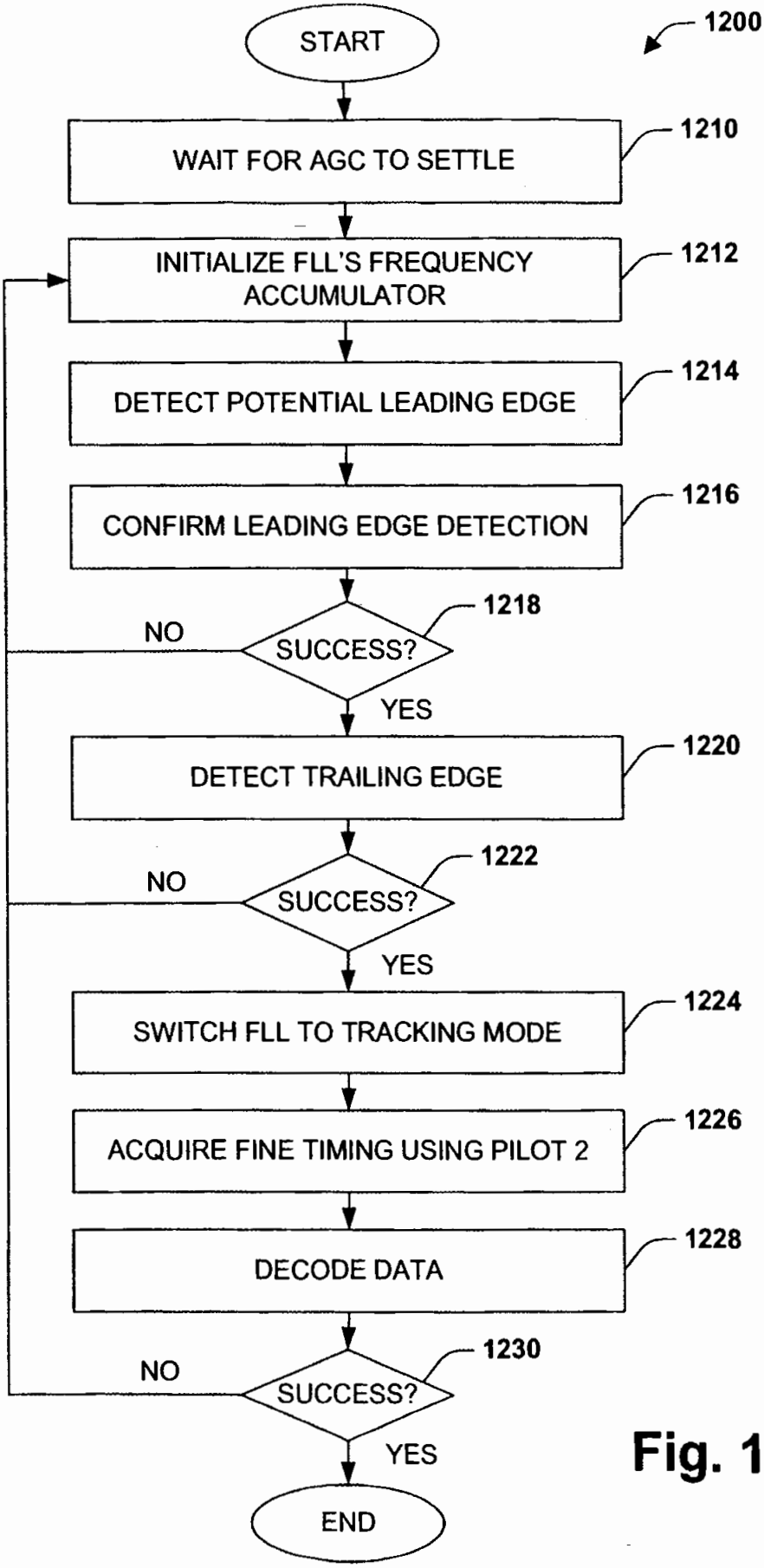

START
1200
WAIT FOR AGC TO SETTLE
1210
INITIALIZE FLL'S FREQUENCY ACCUMULATOR
1212
DETECT POTENTIAL LEADING EDGE
1214
CONFIRM LEADING EDGE DETECTION
1216
SUCCESS?
1218
NO
YES
DETECT TRAILING EDGE
1220
SUCCESS?
1222
NO
YES
SWITCH FLL TO TRACKING MODE
1224
ACQUIRE FINE TIMING USING PILOT 2
1226
DECODE DATA
1228
SUCCESS?
1230
NO
YES
END

Fig. 12

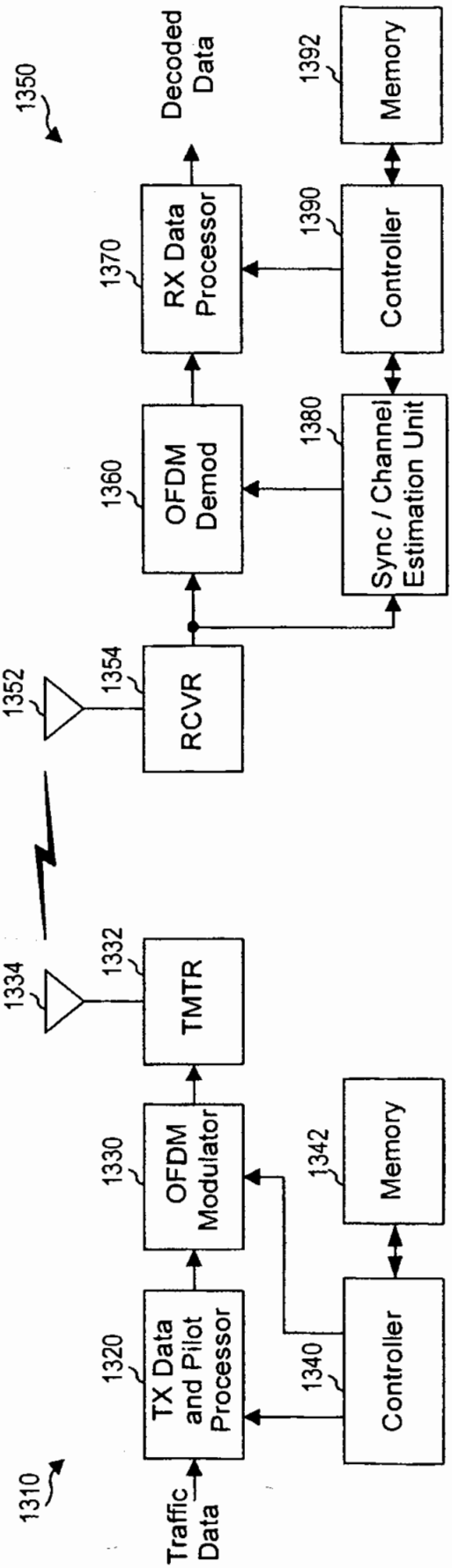

1310
Traffic Data
1320
TX Data and Pilot Processor
1330
OFDM Modulator
1332
TMTR
1334
1340
Controller
1342
Memory
1300
1350
1352
1354
RCVR
1360
OFDM Demod
1370
RX Data Processor
Decoded Data
1380
Sync / Channel Estimation Unit
1390
Controller
1392
Memory

Fig. 13

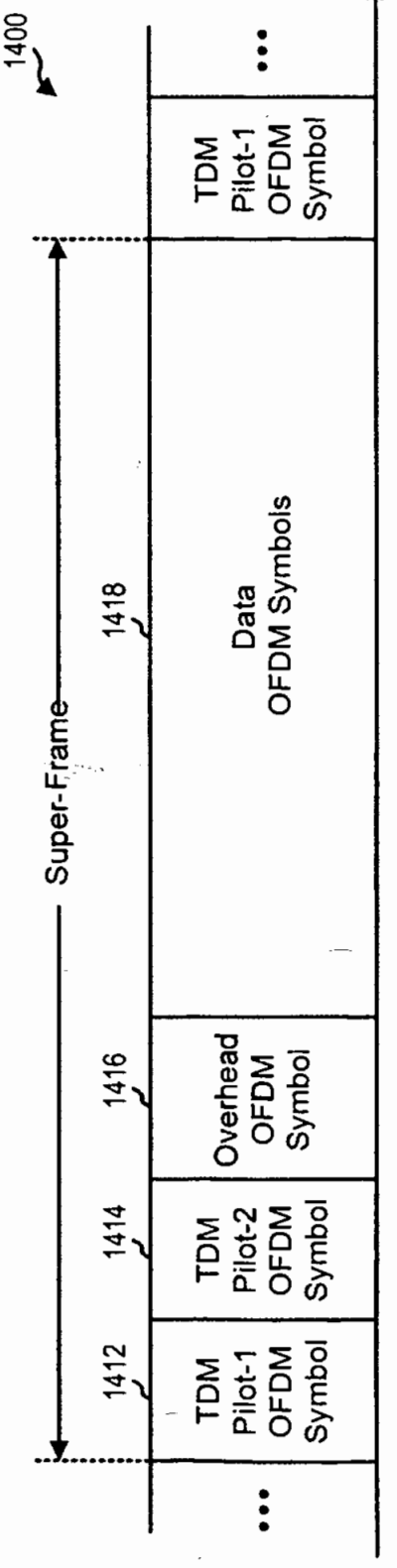

1400
Super-Frame
1412
TDM Pilot-1 OFDM Symbol
1414
TDM Pilot-2 OFDM Symbol
1416
Overhead OFDM Symbol
1418
Data OFDM Symbols
TDM Pilot-1 OFDM Symbol
Time

Fig. 14

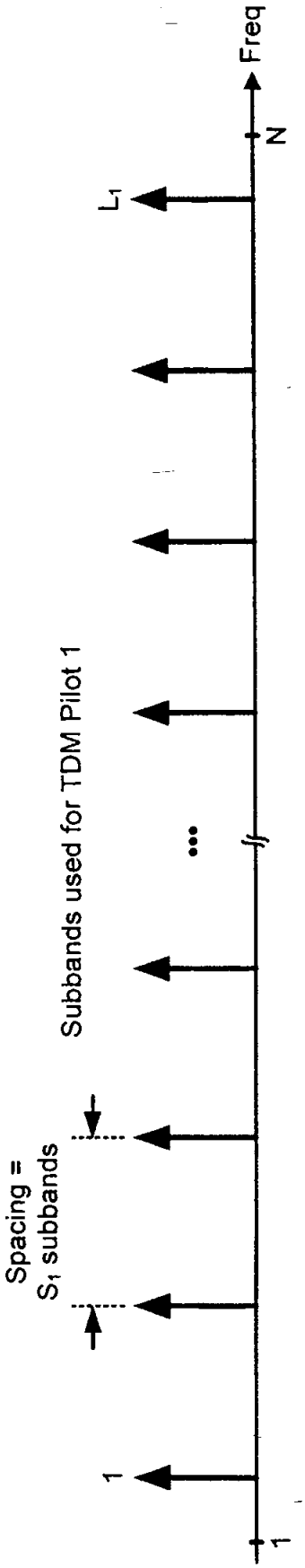


Fig. 15a

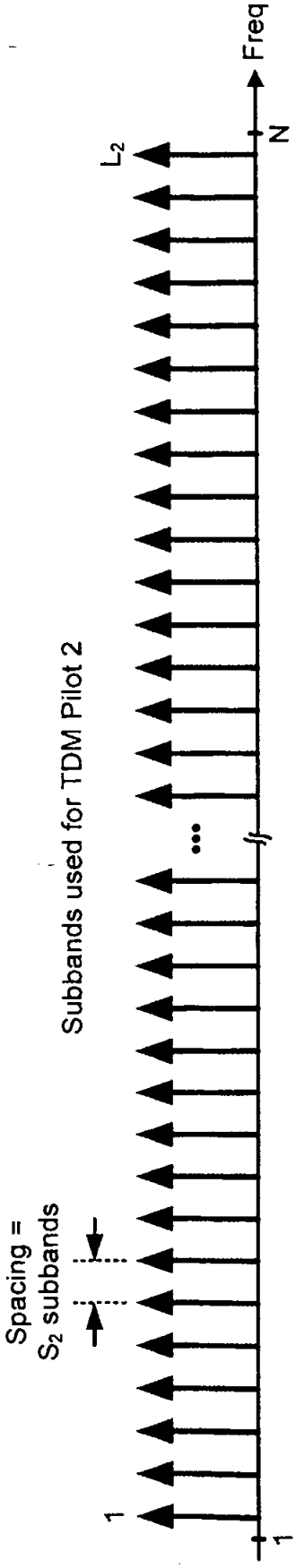


Fig. 15b

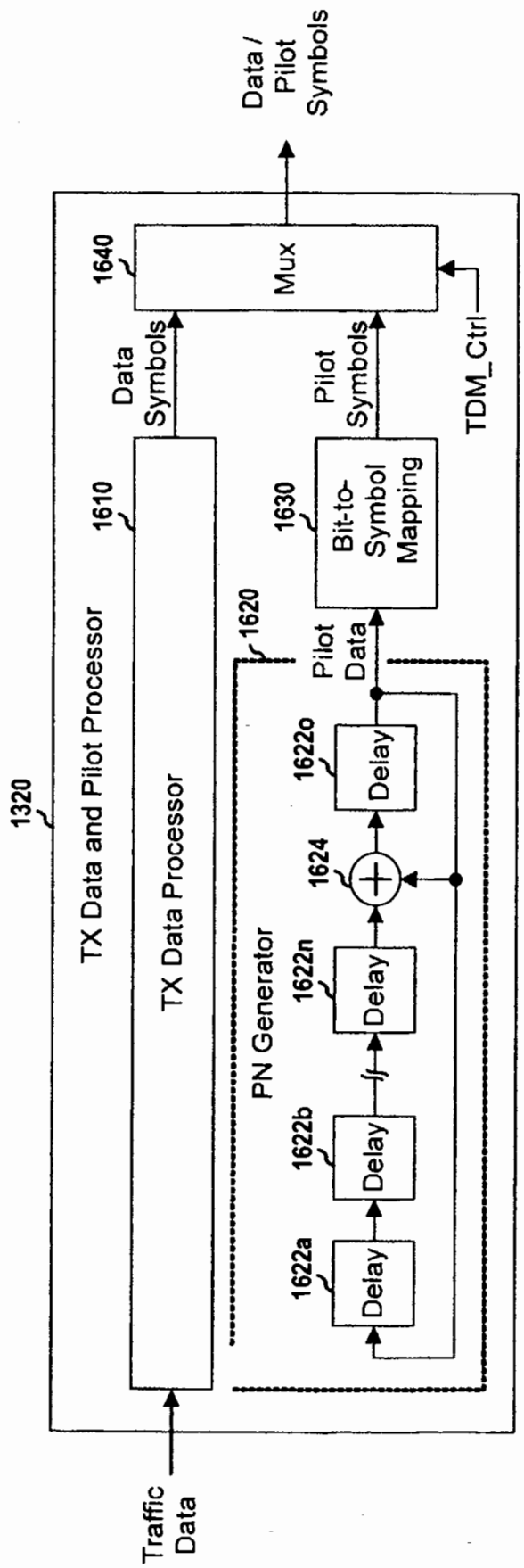

Traffic Data
1320
TX Data and Pilot Processor
TX Data Processor
1610
Data Symbols
1640
Mux
Data / Pilot Symbols
TDM_Ctrl
Pilot Symbols
1630
Bit-to-Symbol Mapping
1620
Pilot Data
PN Generator
1622o
Delay
1624
1622n
Delay
1622b
Delay
1622a
Delay

Fig. 16

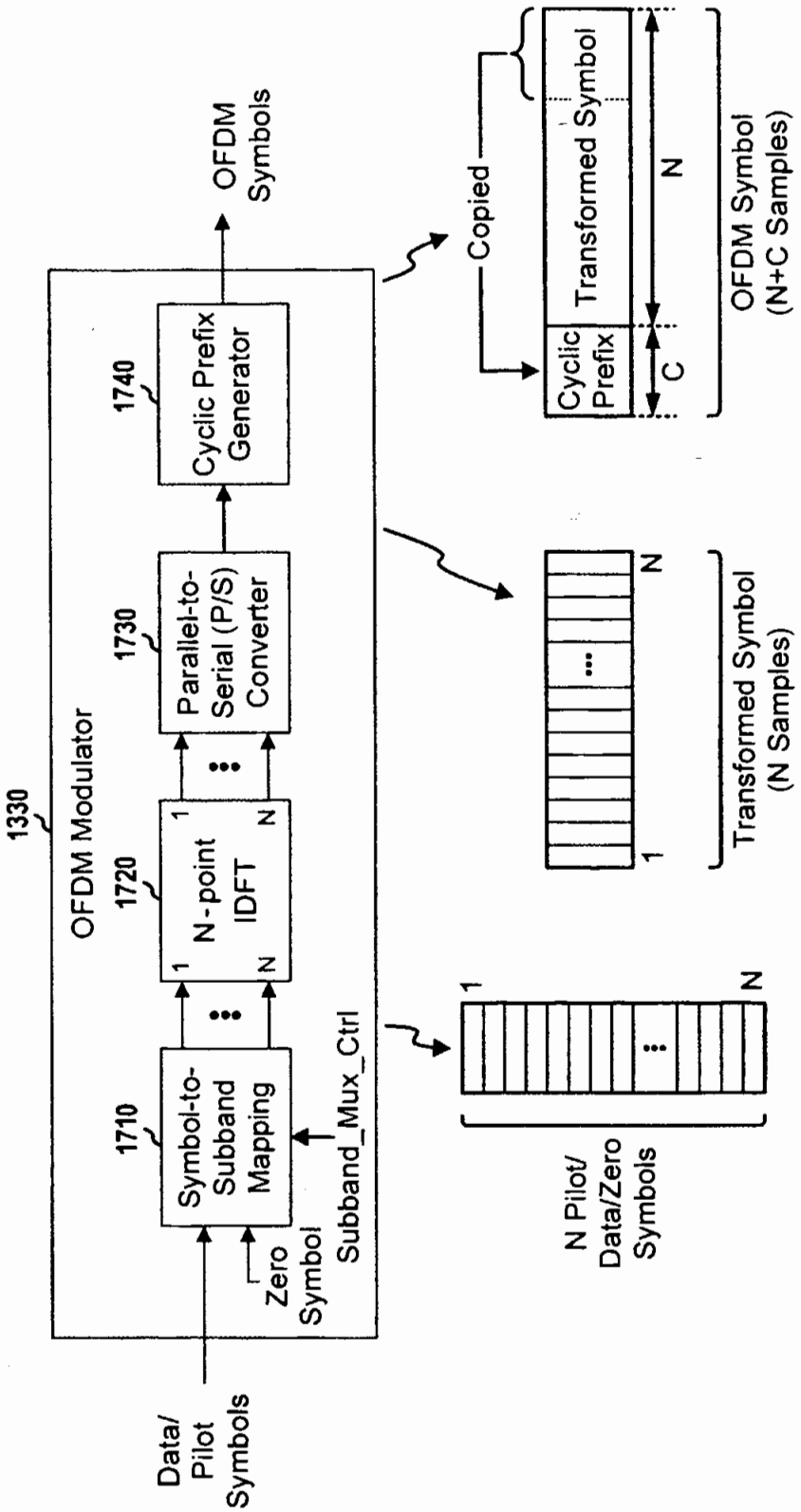

1330
OFDM Modulator
1710
Symbol-to-Subband Mapping
1720
N-point IDFT
1730
Parallel-to-Serial (P/S) Converter
1740
Cyclic Prefix Generator
Data/ Pilot Symbols
Zero Symbol
Subband_Mux_Ctrl
OFDM Symbols
N Pilot/ Data/Zero Symbols
Transformed Symbol (N Samples)
Copied
Cyclic Prefix
Transformed Symbol
C
N
OFDM Symbol (N+C Samples)

Fig. 17

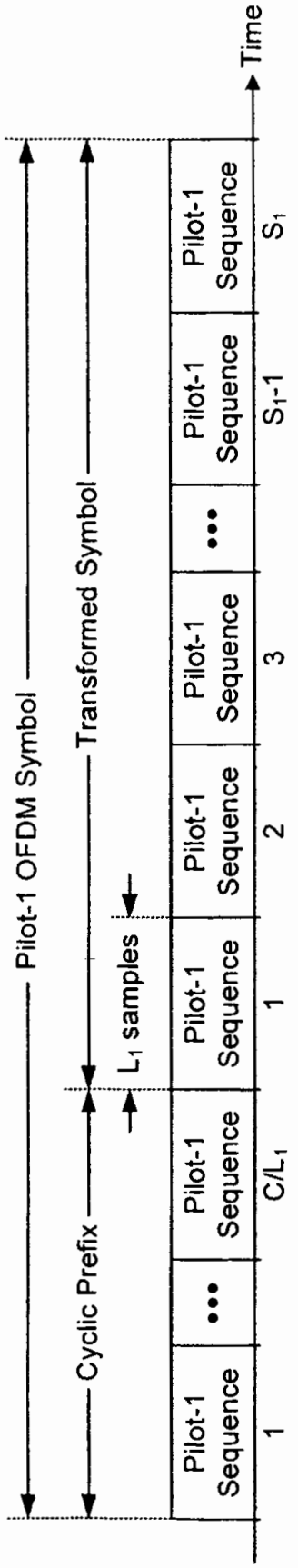


Fig. 18a

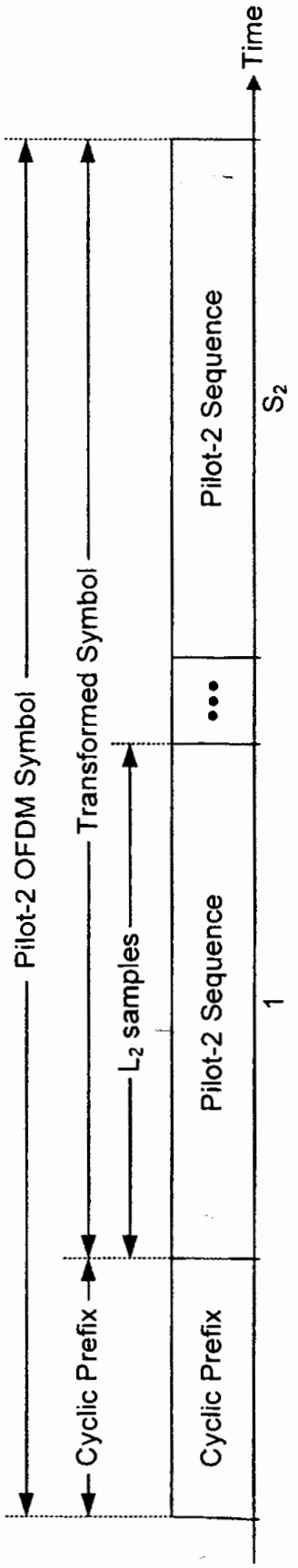


Fig. 18b

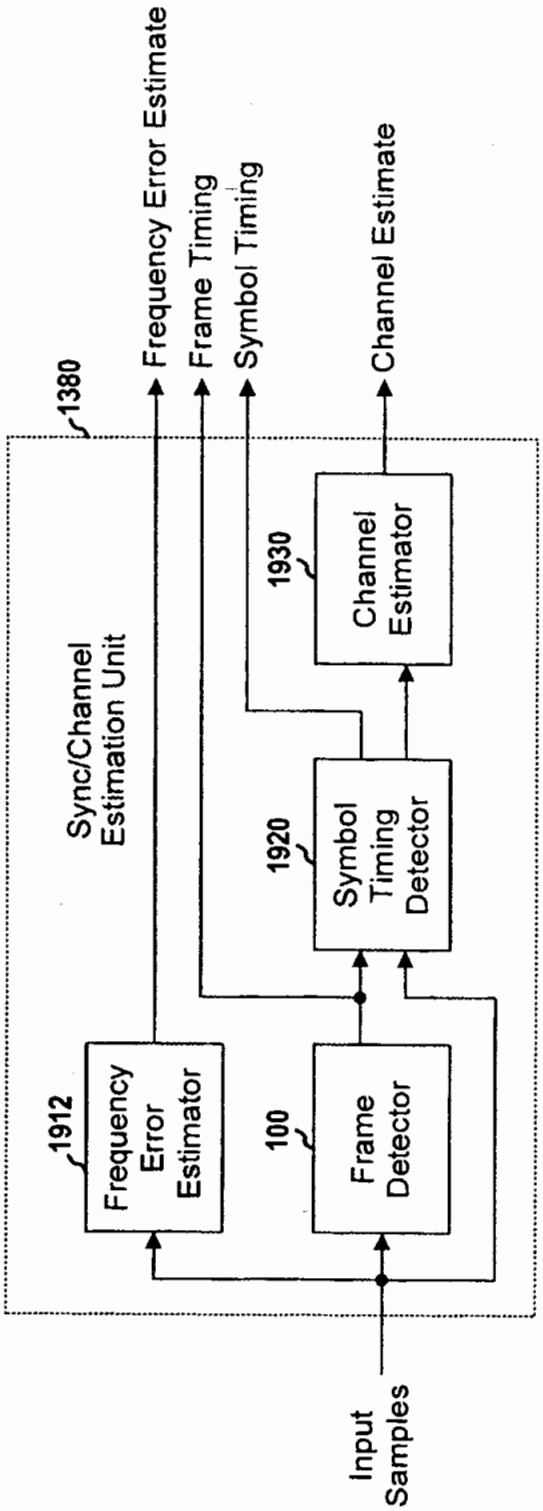

Frequency Error Estimate
Frame Timing
Symbol Timing
Channel Estimate
1380
Sync/Channel Estimation Unit
1930
Channel Estimator
1920
Symbol Timing Detector
1912
Frequency Error Estimator
100
Frame Detector
Input Samples

Fig. 19

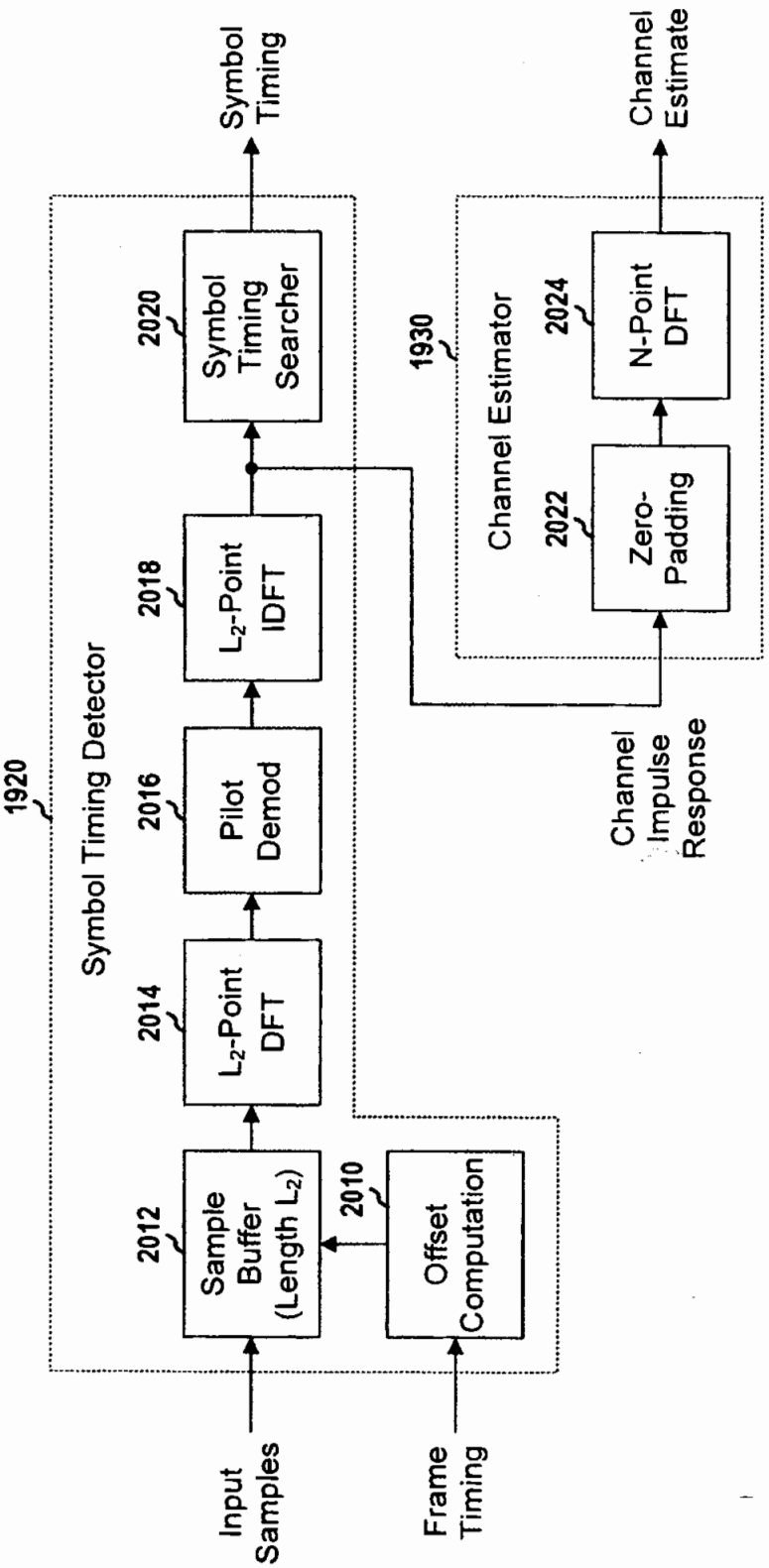

Symbol Timing
Channel Estimate
1920
Symbol Timing Detector
2020
Symbol Timing Searcher
1930
Channel Estimator
2024
N-Point DFT
2018
L_2-Point IDFT
2022
Zero-Padding
2016
Pilot Demod
Channel Impulse Response
2014
L_2-Point DFT
2012
Sample Buffer (Length L_2)
2010
Offset Computation
Input Samples
Frame Timing

Fig. 20

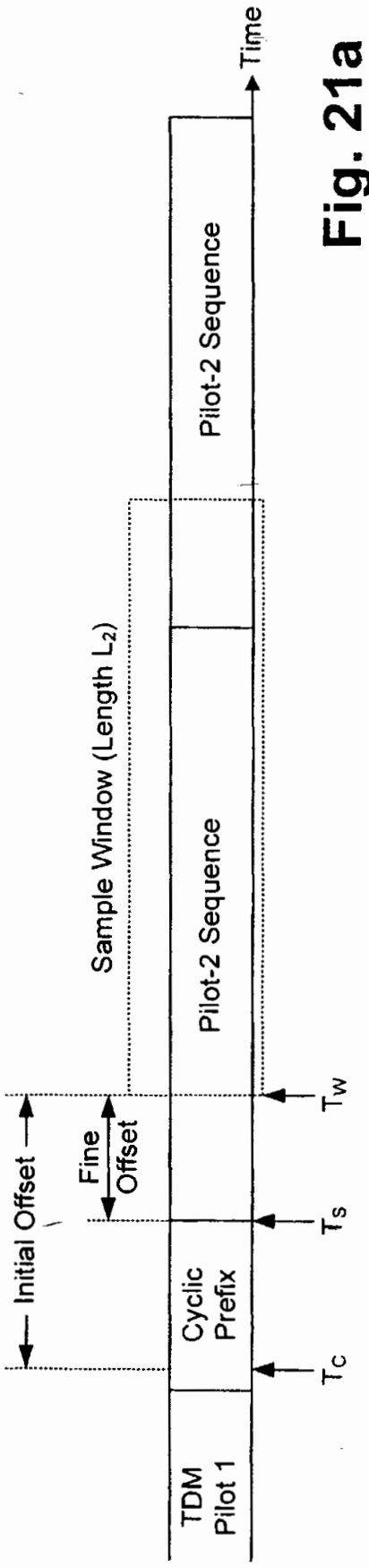


Fig. 21a

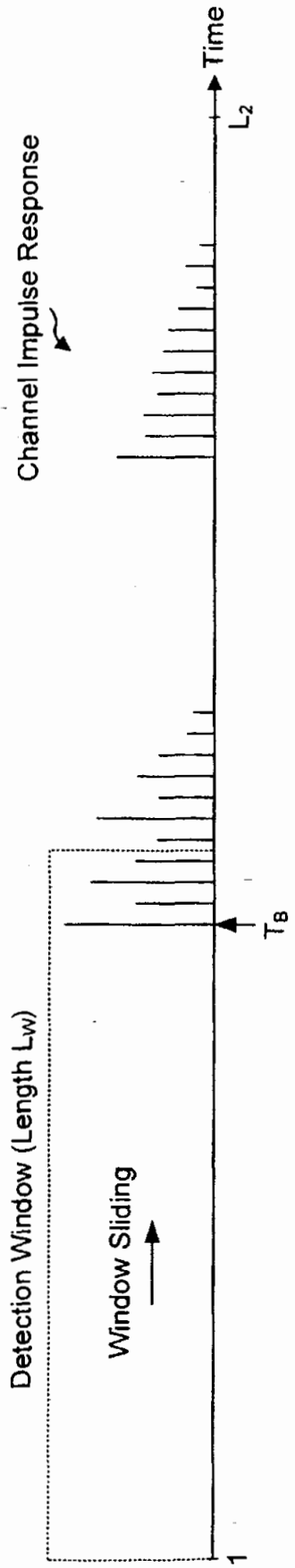


Fig. 21b

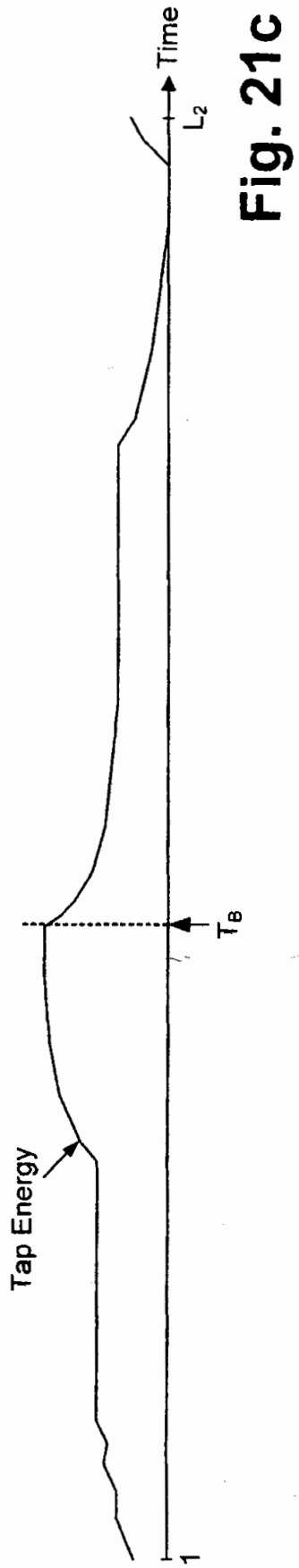


Fig. 21c

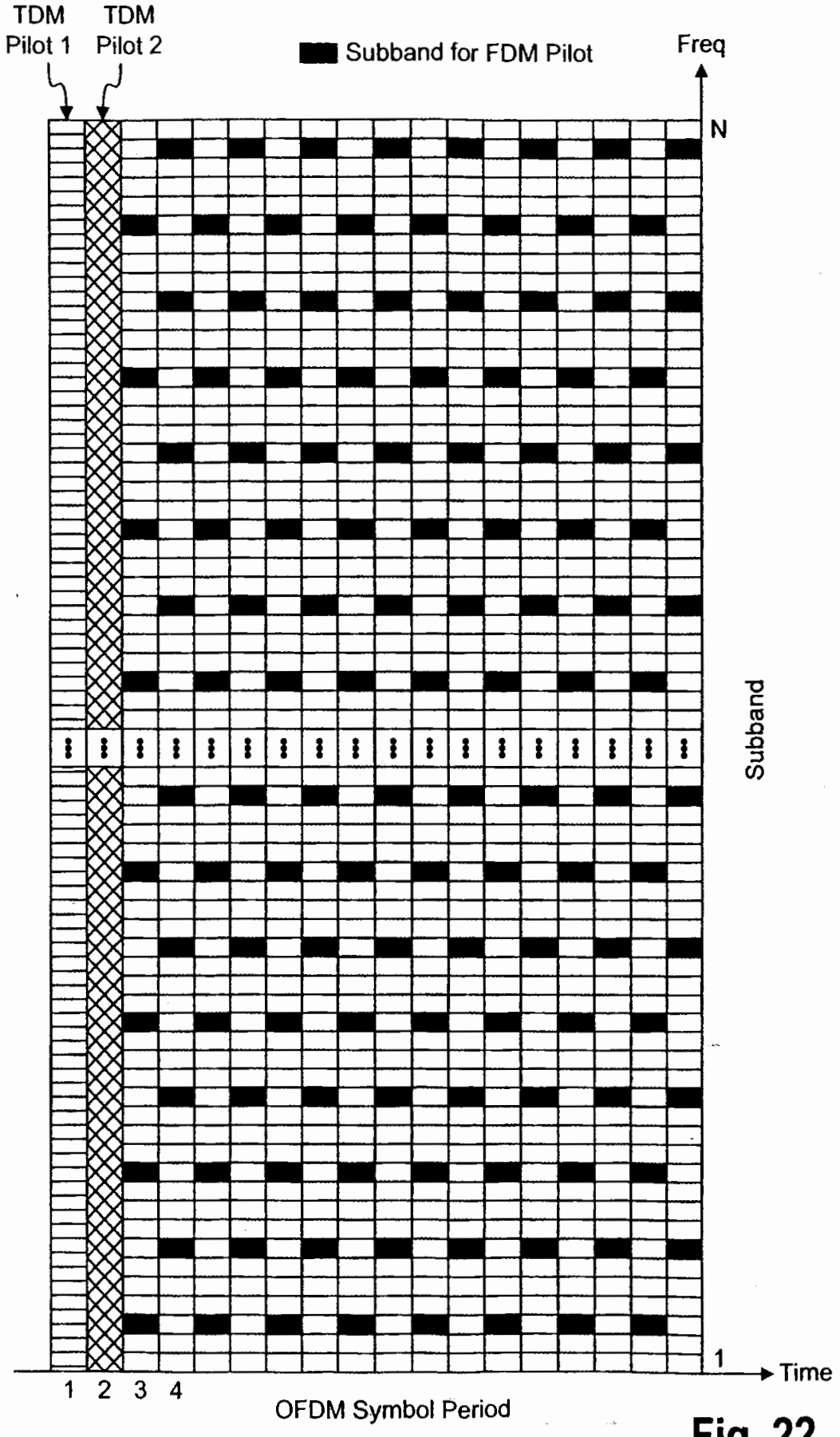

TDM Pilot 1
TDM Pilot 2
Subband for FDM Pilot
Freq
N
Subband
1
Time
1 2 3 4
OFDM Symbol Period

Fig. 22