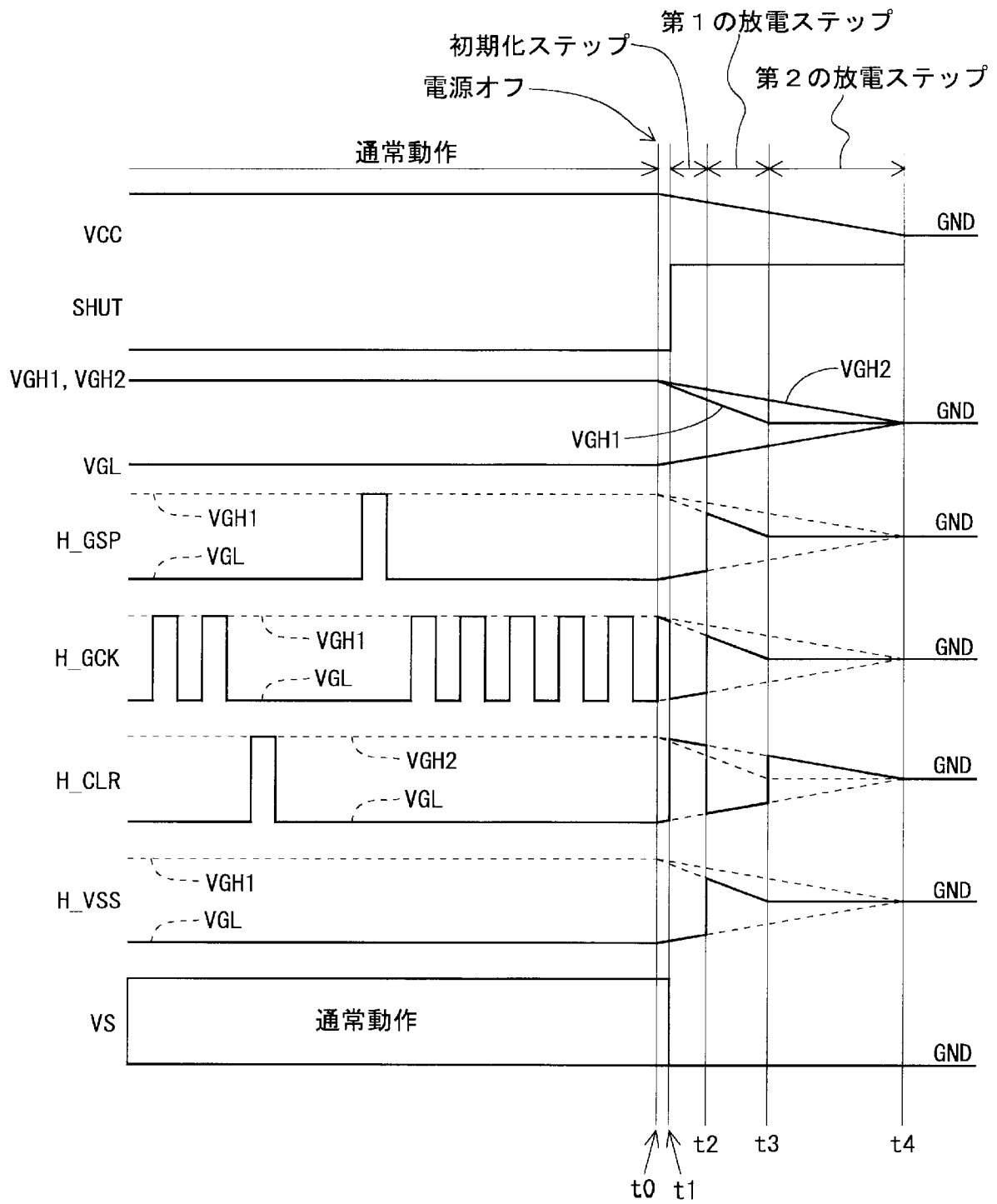
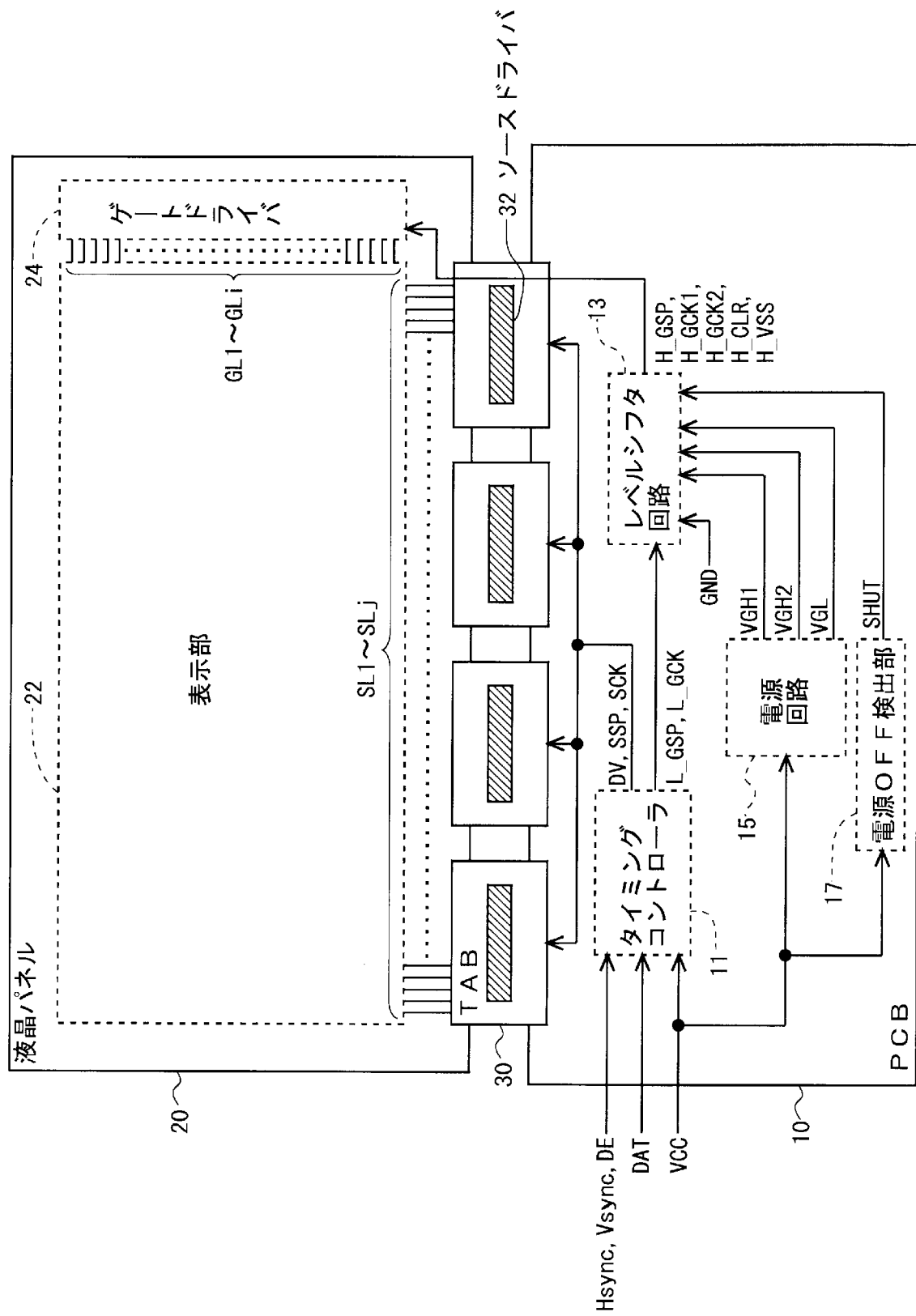


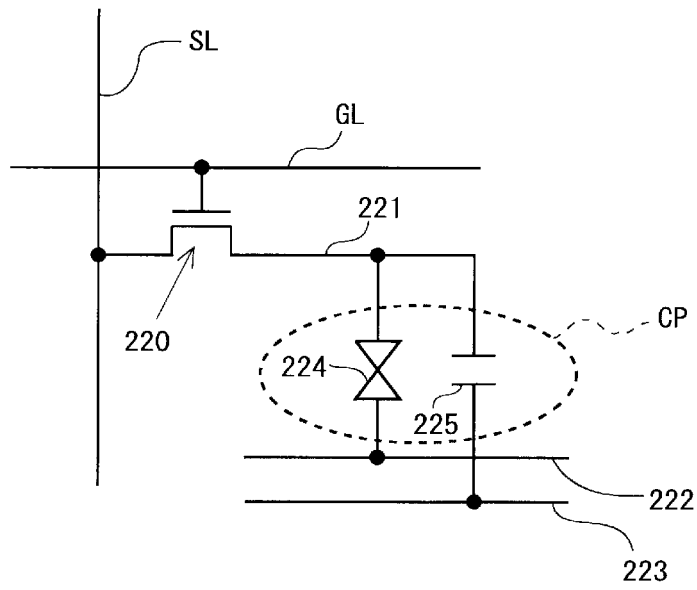
[図1]



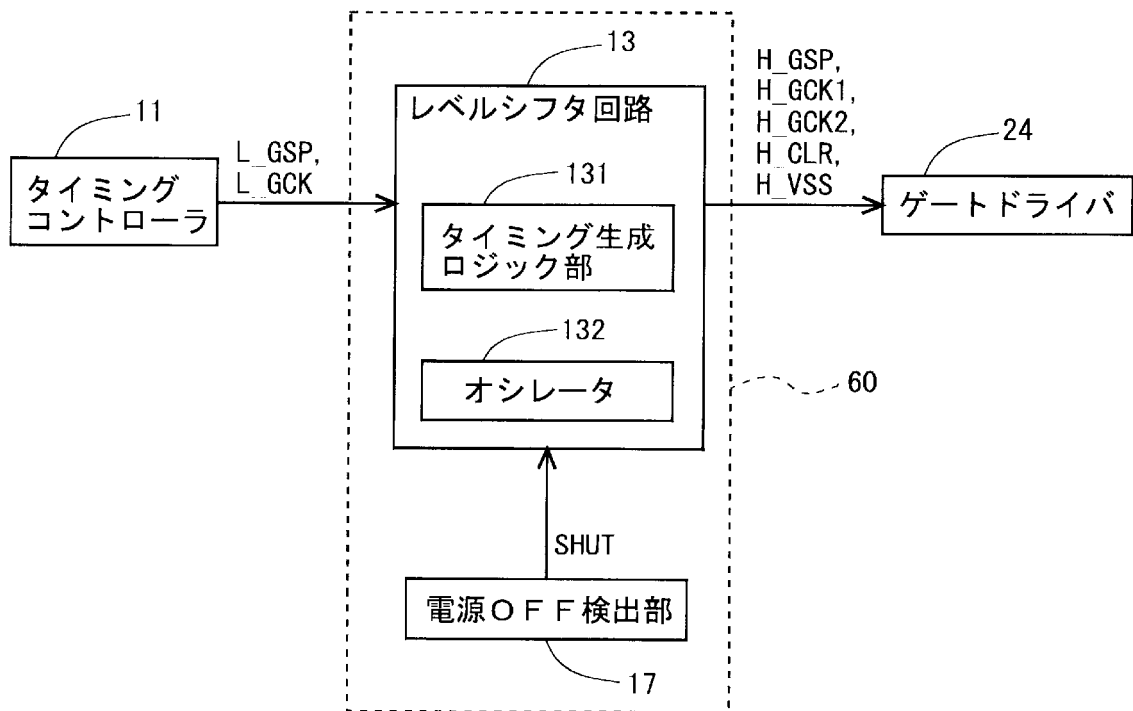
[図2]



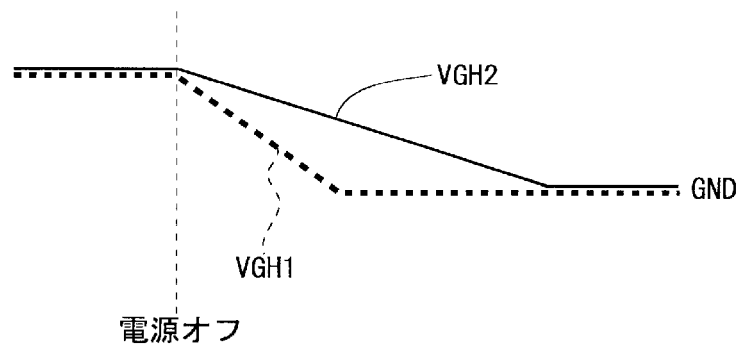
[図3]



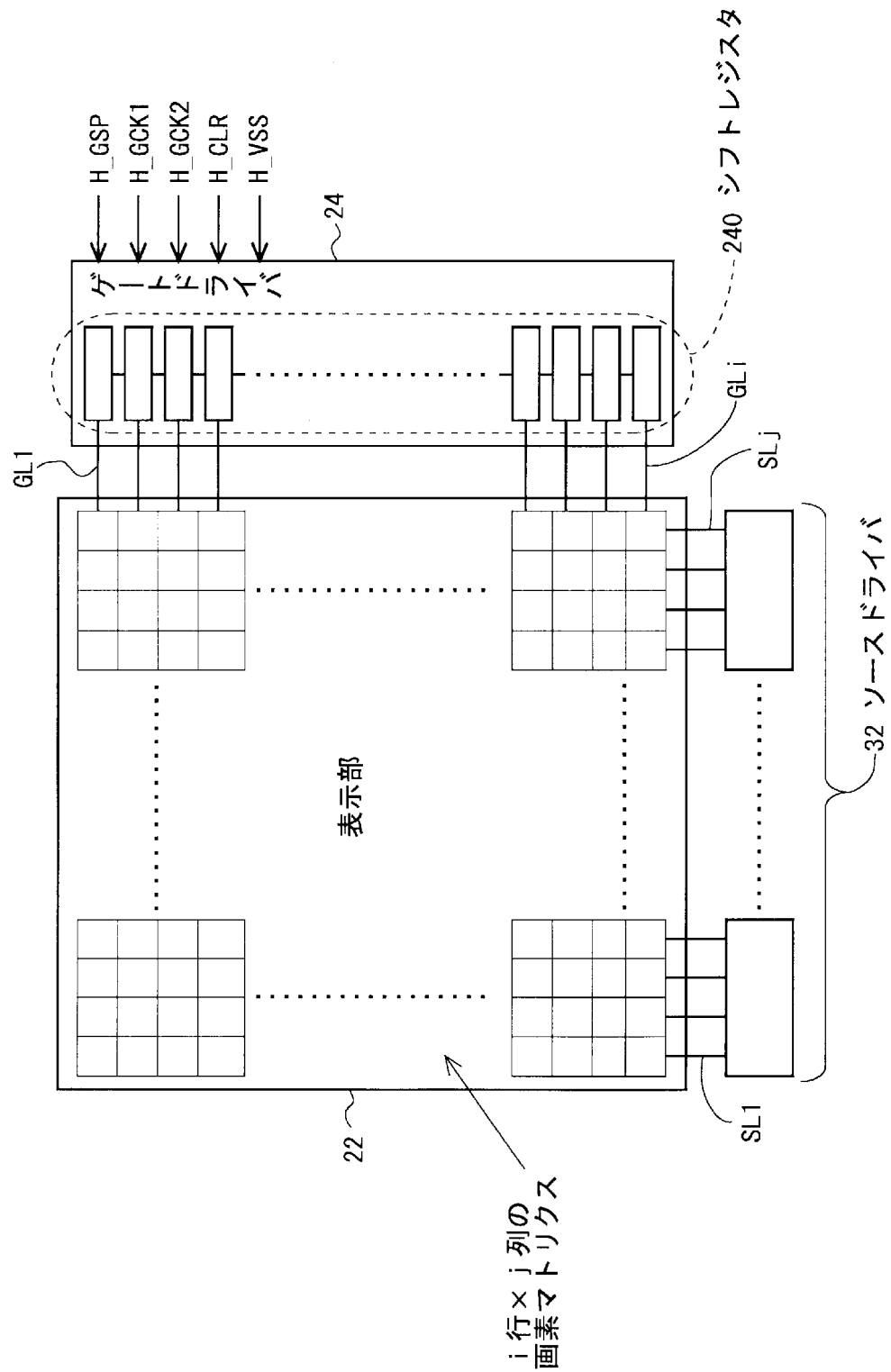
[図4]



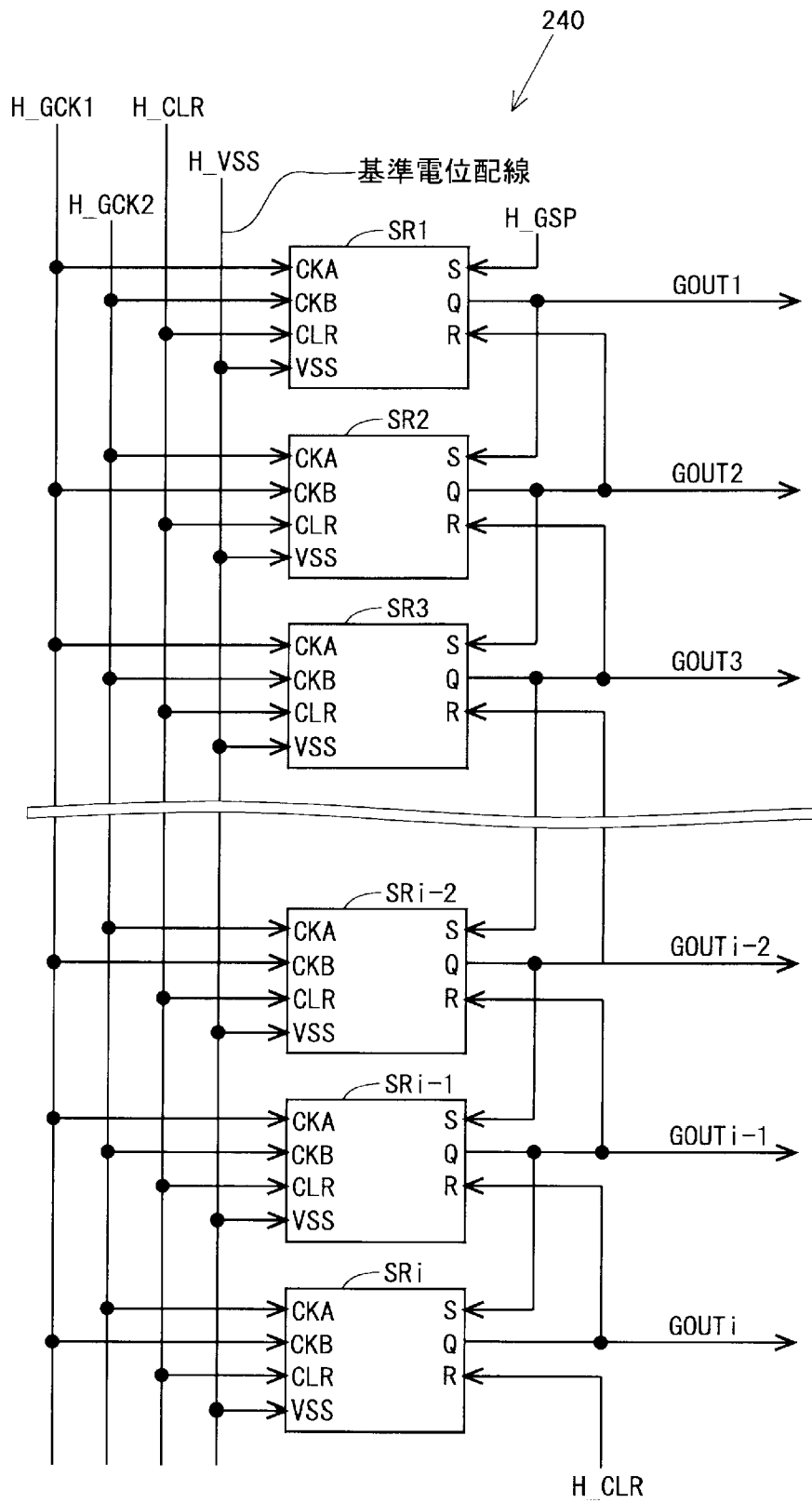
[図6]



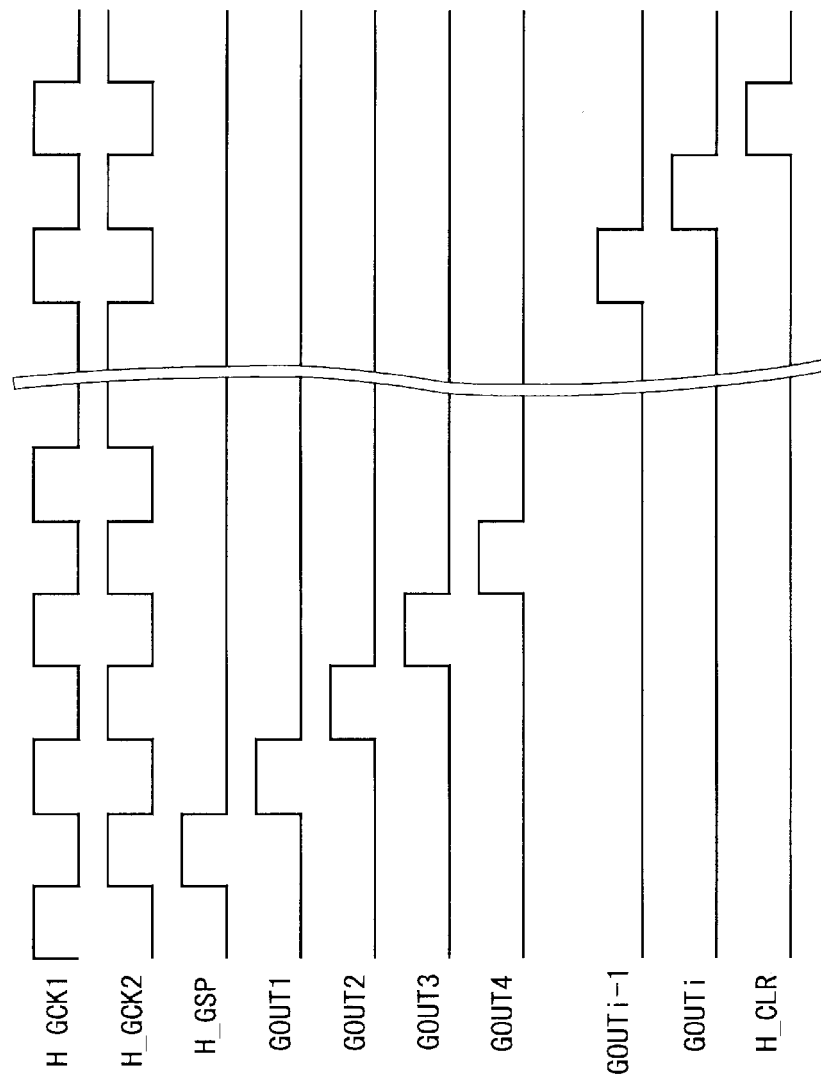
[図7]



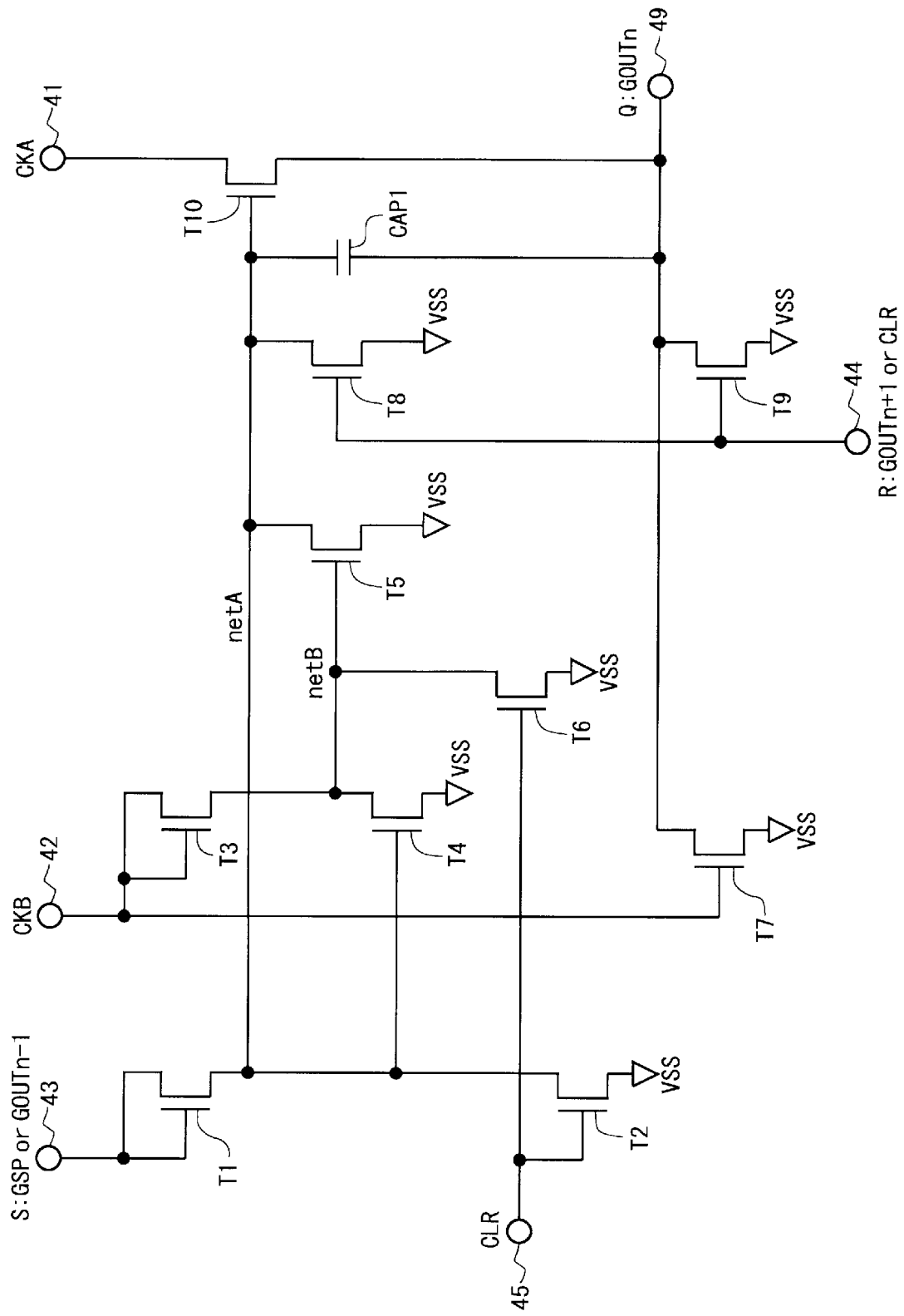
[図8]



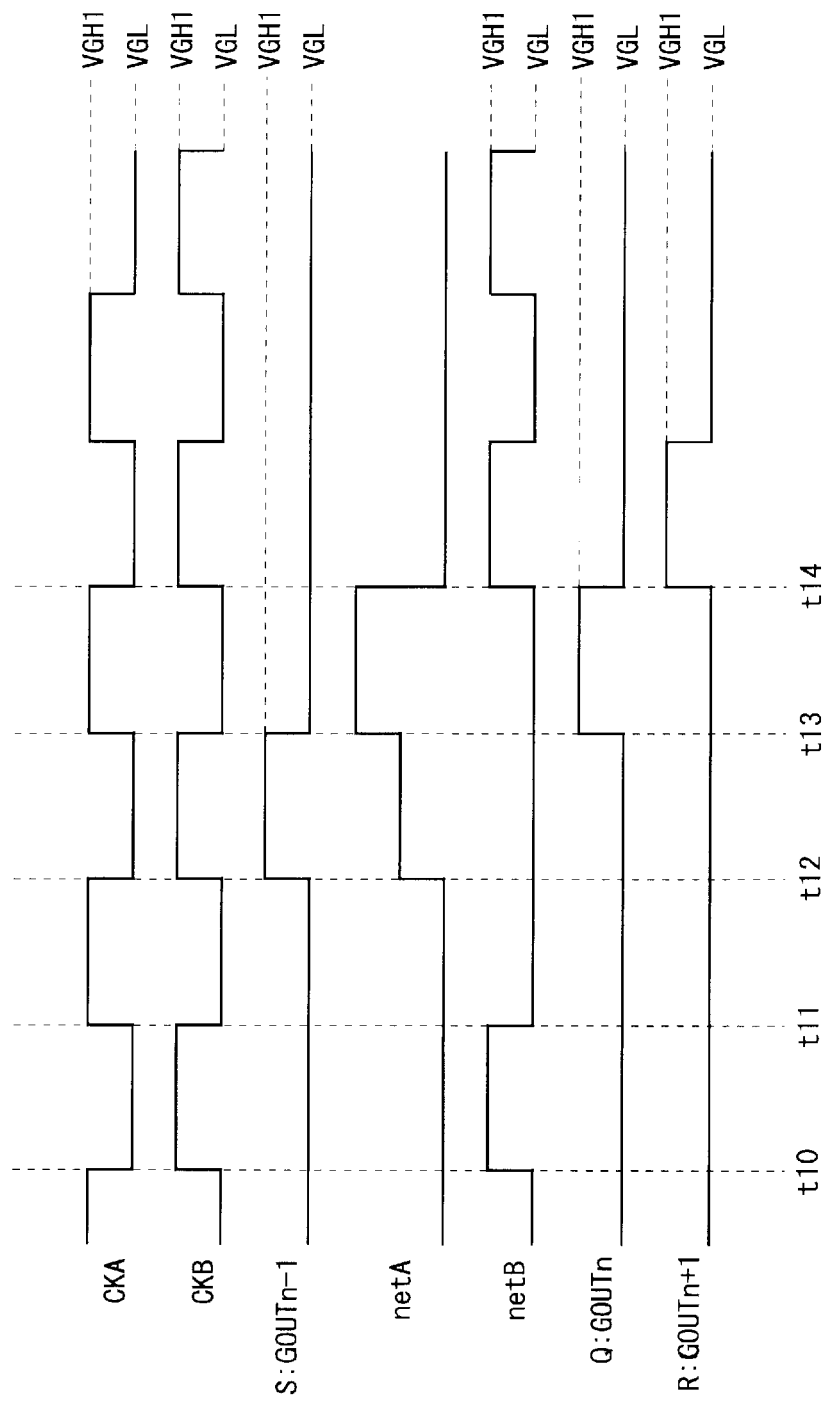
[図9]



[図10]



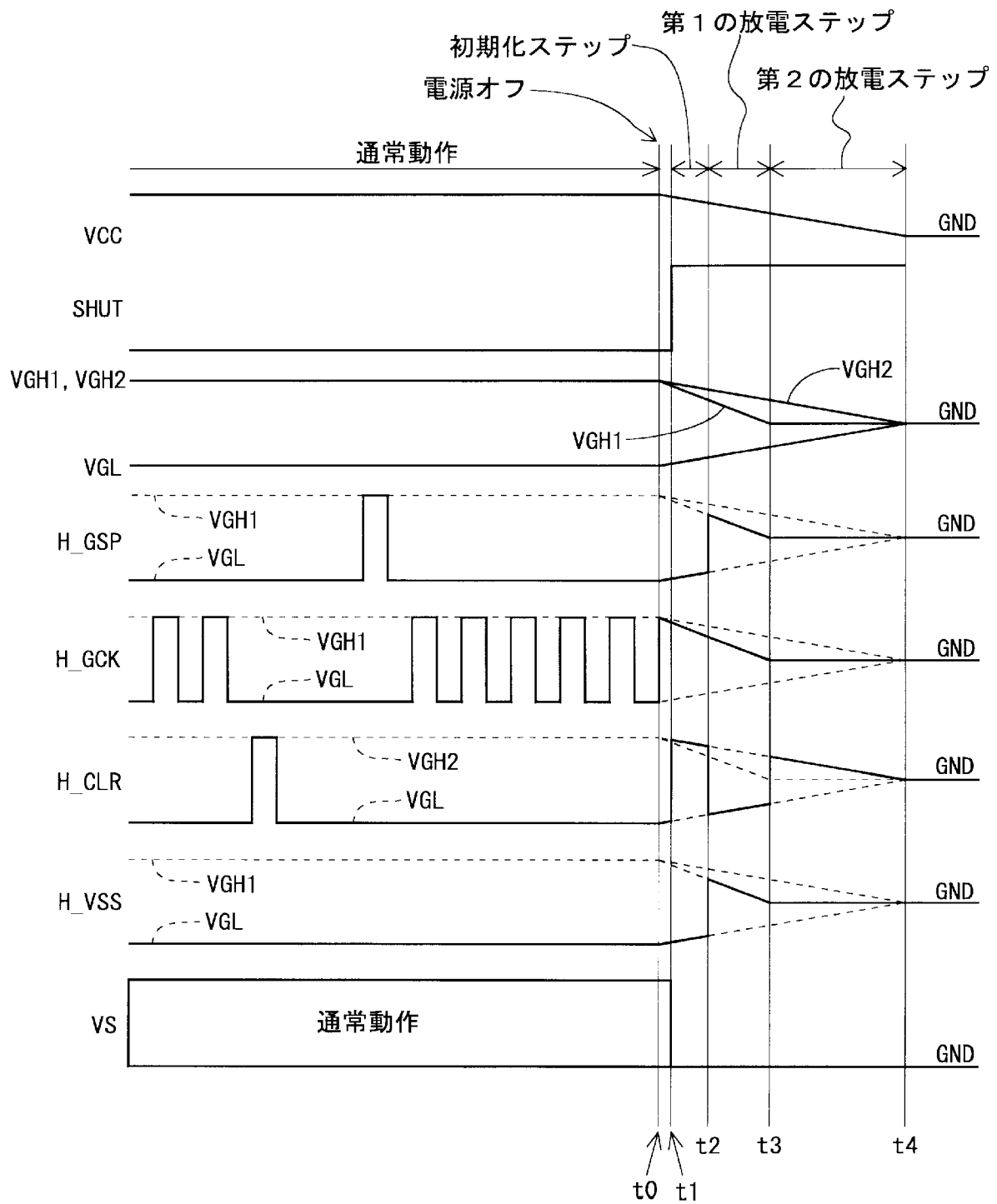
[図11]



[図12]

	通常動作 ($\sim t_0$)	初期化ステップ ($t_1 \sim t_2$)	第 1 の放電ステップ ($t_2 \sim t_3$)	第 2 の放電ステップ ($t_3 \sim t_4$)
H_GSP	VGH1, VGL	VGL	VGH1	VGH1 (GND)
H_GCK	VGH1, VGL	VGL	VGH1	VGH1 (GND)
H_CLR	VGH2, VGL	VGH2	VGL	VGH2
H_VSS	VGL	VGL	VGH1	VGH1 (GND)

[図13]



[図14]

