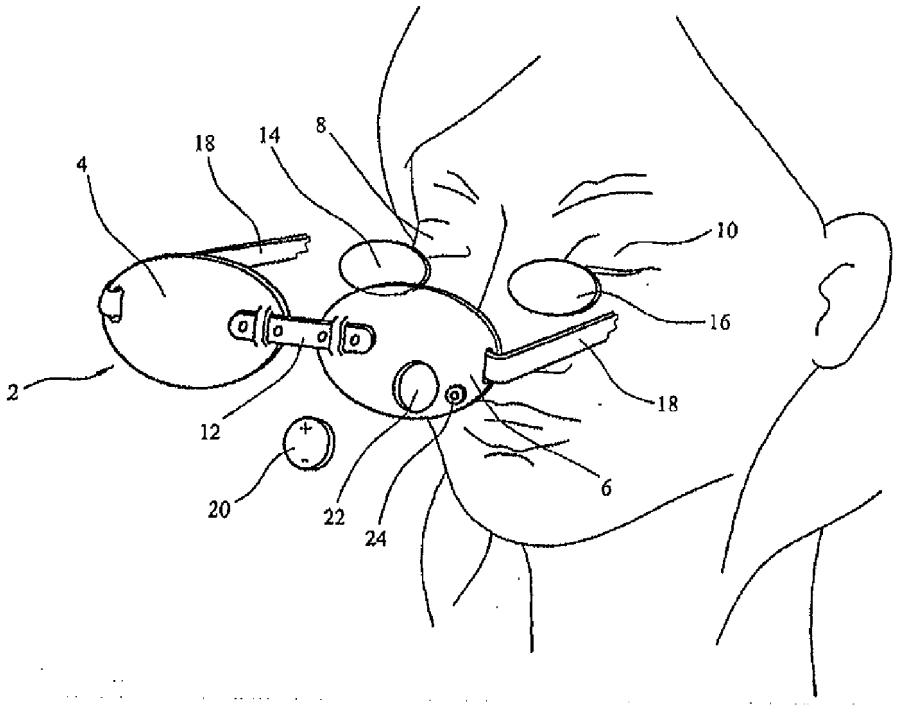


FIG. 1

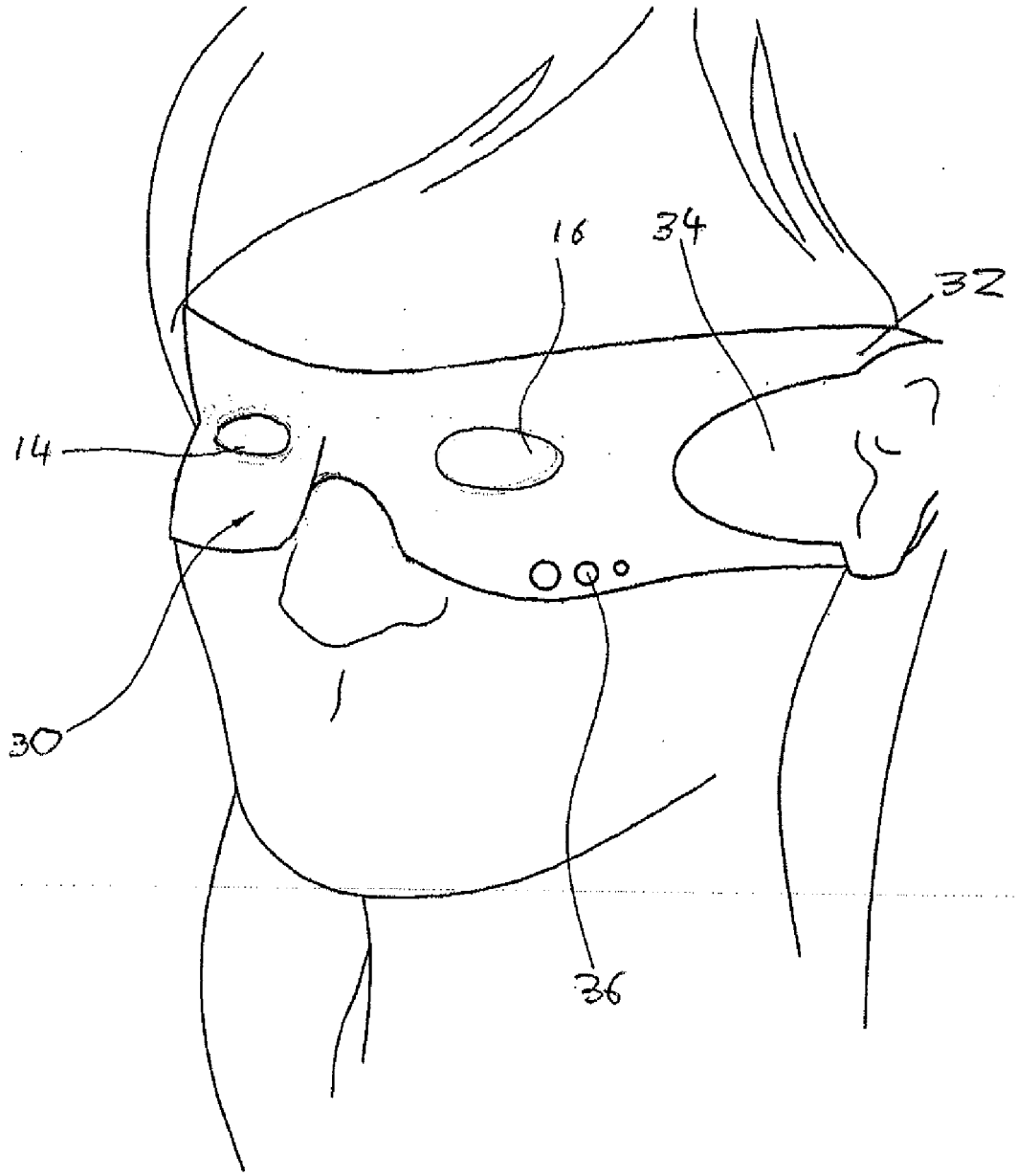


FIG. 2

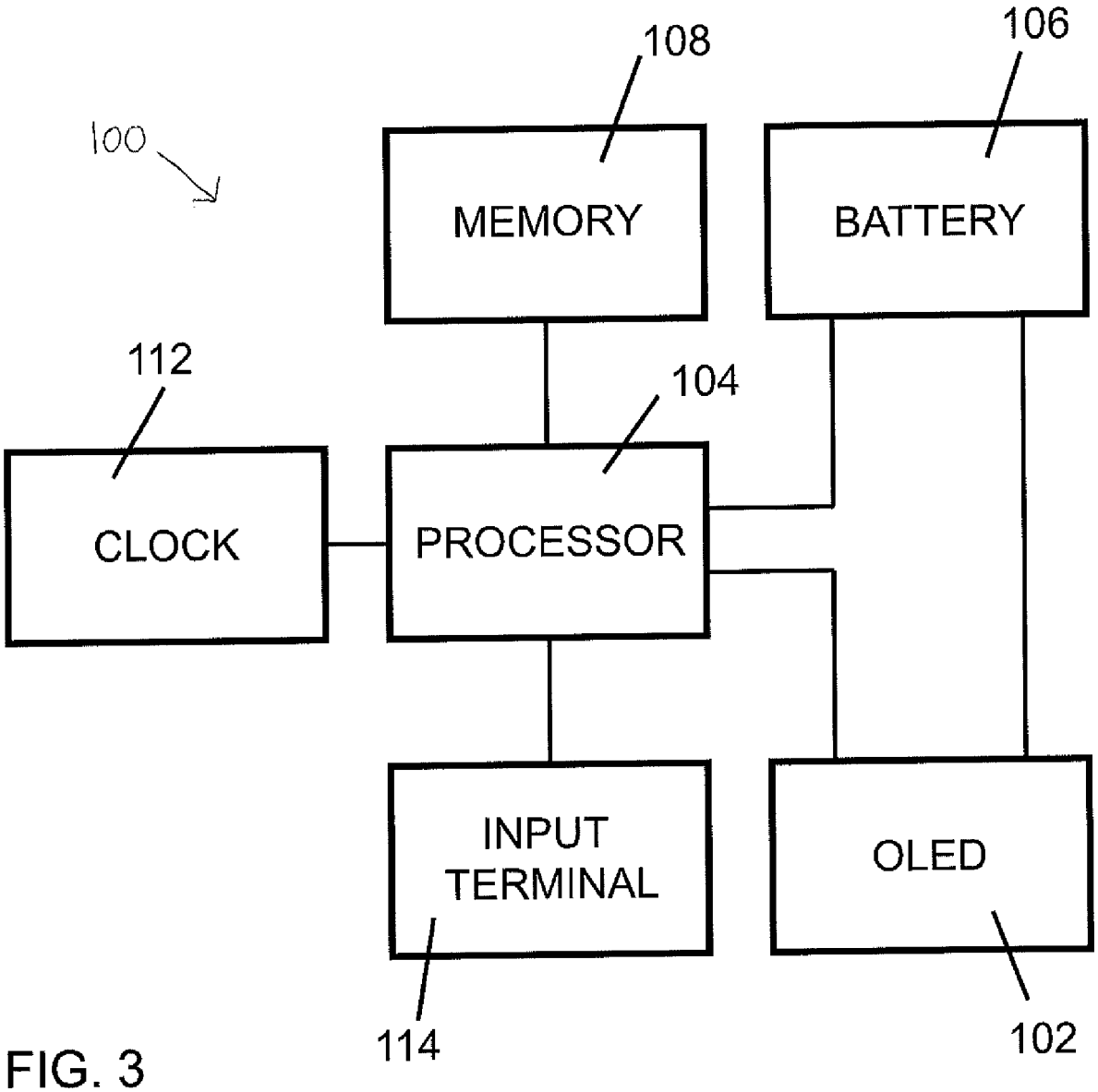

100
108
106
MEMORY
BATTERY
112
104
CLOCK
PROCESSOR
INPUT TERMINAL
OLED
114
102

FIG. 3

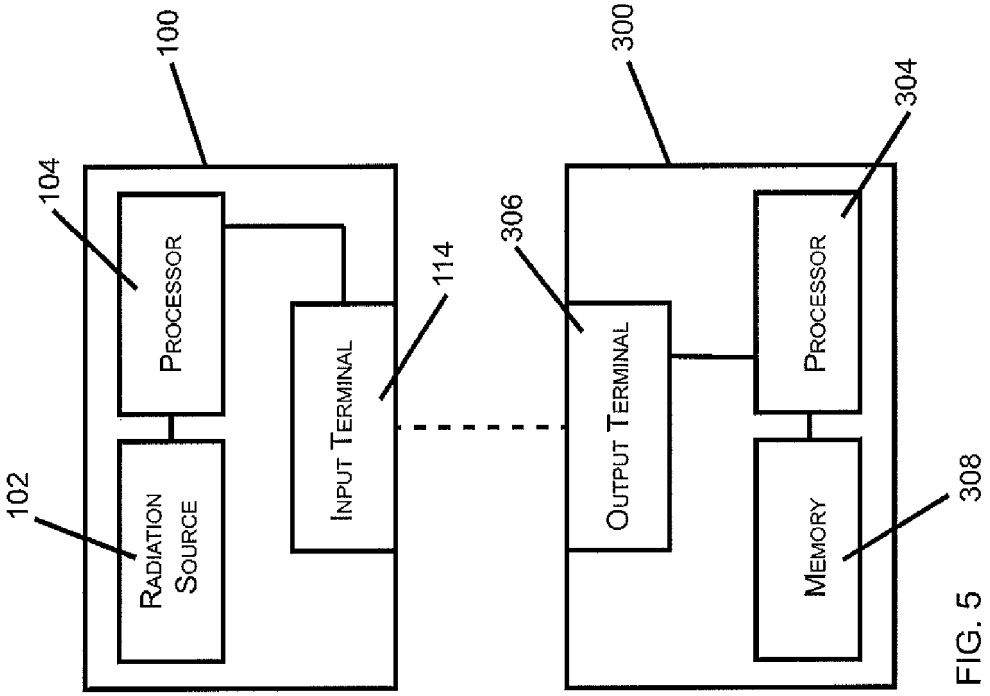

100
104
PROCESSOR
102
RADIATION SOURCE
INPUT TERMINAL
114
300
306
OUTPUT TERMINAL
304
PROCESSOR
MEMORY
308

FIG. 5

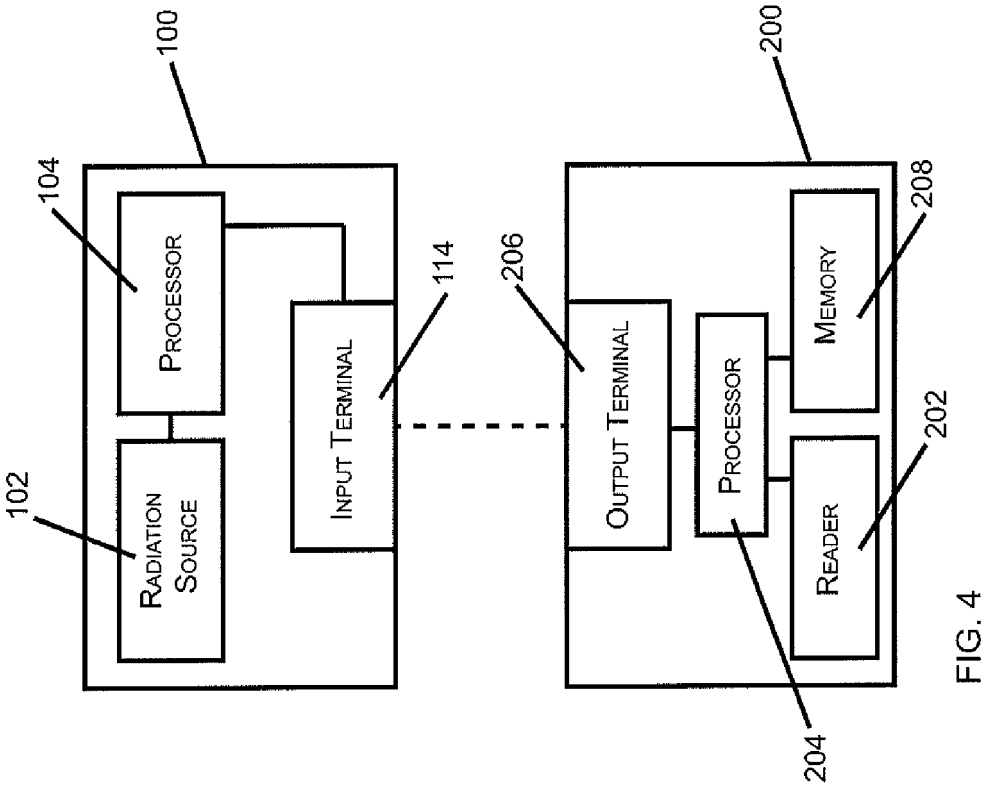

100
104
PROCESSOR
102
RADIATION SOURCE
INPUT TERMINAL
114
200
206
OUTPUT TERMINAL
PROCESSOR
MEMORY
208
READER
202
204

FIG. 4

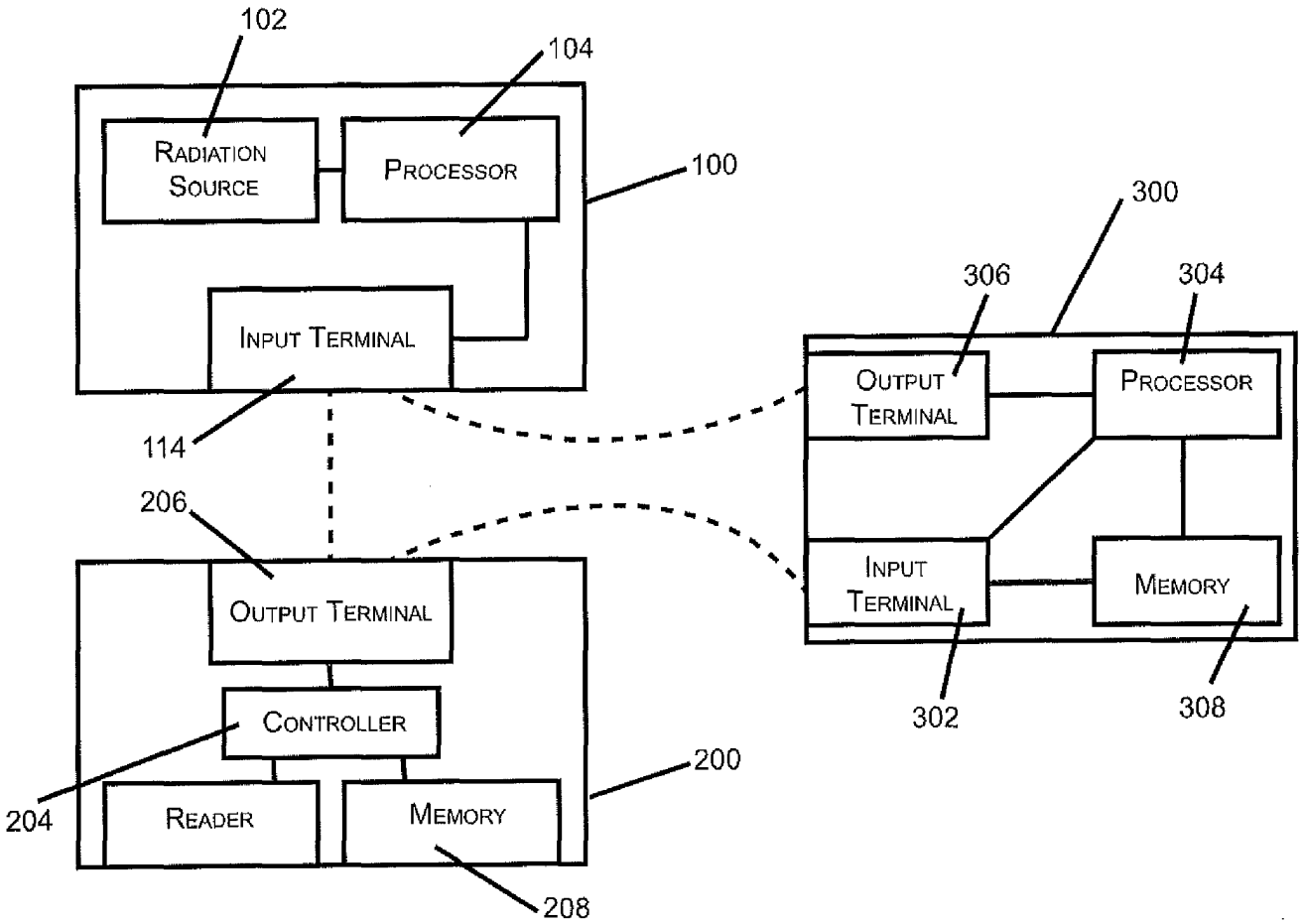

102
104
RADIATION SOURCE
PROCESSOR
100
INPUT TERMINAL
114
300
306
304
OUTPUT TERMINAL
PROCESSOR
INPUT TERMINAL
MEMORY
302
308
206
OUTPUT TERMINAL
CONTROLLER
204
READER
MEMORY
200
208

FIG. 6

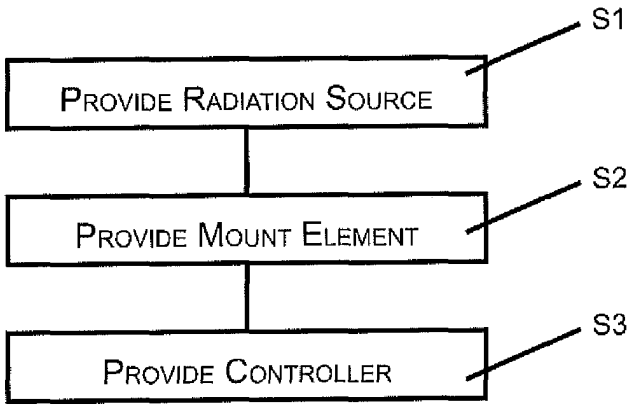

S1
PROVIDE RADIATION SOURCE
S2
PROVIDE MOUNT ELEMENT
S3
PROVIDE CONTROLLER

FIG. 7