

CIRCUIT AND METHOD OF FREQUENCY SYNTHESIZER CONTROL
WITH A SERIAL PERIPHERAL INTERFACE

Abstract of the Disclosure

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A remote wireless communication device (10) receives messages during windows occurring at predetermined intervals as determined by a communication protocol. The remote wireless communication device uses a
10 microcontroller (28) to initialize the functional blocks (18, 20) and perform message and error processing. The microprocessor and most other functional blocks are disabled between receive message windows to conserve power. A frequency synthesizer control function is
15 incorporated with a serial peripheral interface (38) to the frequency synthesizer to enable the frequency synthesizer just prior and during to the receive message window. The frequency synthesizer control saves power by providing the control necessary to monitor for messages
20 during the window without activating the microcontroller.

FIG. 1 accompanies this abstract.