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Abstract

A Receiver And A Signal Processing Circuit Therefor

- 5 A signal processing circuit arranged to be used in a frequency modulated signal receiver is disclosed which includes a complex filter 105-110 connected to analog to digital converter 112, the filter having first 105-108 and second 109,110 complex filter stages and a voltage limiter 108 disposed between the stages. The circuit is of particular applicability for use in a Bluetooth receiver.

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Fig. 1