



(51) International Patent Classification:

G01R 31/28 (2006.01) G01R 1/04 (2006.01)
H01L 21/67 (2006.01) G01R 31/26 (2020.01)
H05K 7/14 (2006.01)

(21) International Application Number:

PCT/US2025/014825

(22) International Filing Date:

06 February 2025 (06.02.2025)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

63/550,427 06 February 2024 (06.02.2024) US

(71) Applicant: AEHR TEST SYSTEMS [US/US]; 400 Kato Terrace, Fremont, California 94539 (US).

(72) Inventors: SPORCK, Alistair N.; 14970 Sobey Rd., Saratoga, California 95070 (US). BREINLINGER,

Richard H.; 9277 Oak Trail Cir, Santa Rosa, California 95409 (US). TONN, Timothy R.; 714 Gunters Mountain Lane, Wylie, Texas 75098 (US). CALDERON, Alberto; P.O. Box 720853, San Jose, California 95172 (US). RICHMOND, II, Donald P.; 743 Montrose Avenue, Palo Alto, California 94303 (US).

(74) Agent: DE KLERK, Stephen M.; Matterlight IP, 2033 Gateway Place, 5th floor, San Jose, CA 95110 (US).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CV, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IQ, IR, IS, IT, JM, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, MG, MK, MN, MU, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, ST, SV, SY, TH,

(54) Title: ELECTRONICS TESTER

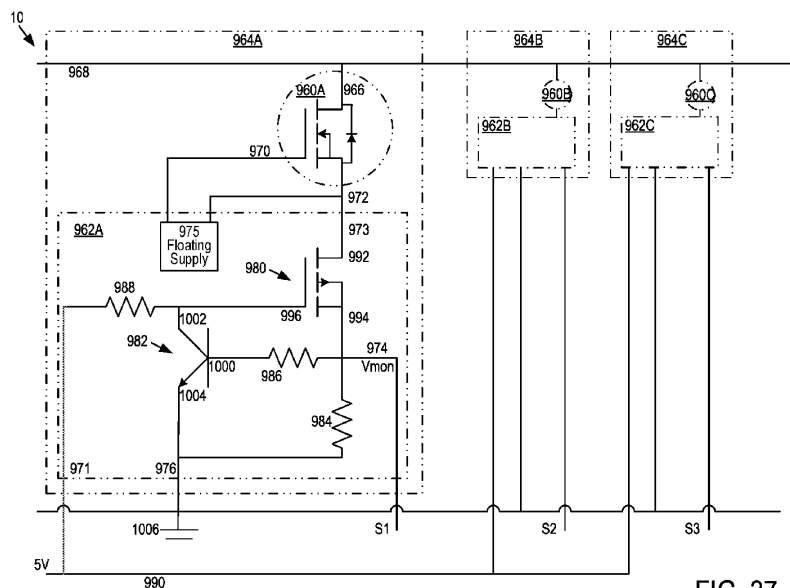


FIG. 37

(57) Abstract: A leakage detection circuit for connecting to a device includes an input voltage terminal, a source voltage terminal for connection to a source of the respective device, a voltage monitor terminal, a reference voltage terminal, a first switch and a second switch. The first switch has a switch terminal connected to the input voltage terminal, an output terminal connected to the source voltage terminal, and an input terminal connected to the voltage monitor terminal. The second switch has a switch terminal connected to the voltage monitor terminal, an input terminal connected to the input voltage terminal, and an output terminal connected to the reference voltage terminal.

[Continued on next page]

TJ, TM, TN, TR, TT, TZ, UA, UG, US, UY, UZ, VC, VN,
WS, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, CV, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SC, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, ME, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))