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Abstract of the Disclosure

A non-interlaced video input signal (Y2) provided by a source (510) is applied to a first processor (20, 30, 40) for producing a first video output signal (Y10) having a high frequency component (Y11) subjected to coring and having a first low frequency component (Y12) subjected to frame recursive filtering. The first processor also provides a plurality of further low frequency frame recursively filtered output signals (Y18, Y20) having differing delays (1-H, 263-H) relative to said first low frequency component. A second processor (50), responsive to the frame recursively filtered low frequency components, provides a video difference signal (Y26) representative of a difference between a low frequency component (Y9) derived from a current line of the video input signal and a second low frequency component (Y24) derived from at least one previous line of the video input signal. An output circuit (60) selectively combines the video difference signal (Y26) and the first video signal (Y10) to provide a noise reduced progressive scan (non-interlaced) video output signal (Y15) in which high frequency components thereof are noise reduced by coring and low frequency components thereof are noise reduced by frame recursive filtering.

This Sheet Has Been Replaced

